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UNIVERSITY OF MUMBAI

Revised syllabus (Rev- 2016) from Academic Year 2016 -17
Under

FACULTY OF TECHNOLOGY

Electronics and Telecommunication Engineering

Second Year with Effect from **AY 2017-18**
Third Year with Effect from **AY 2018-19**
Final Year with Effect from **AY 2019-20**

As per **Choice Based Credit and Grading System**
with effect from the AY 2016–17

Co-ordinator, Faculty of Technology's Preamble:

To meet the challenge of ensuring excellence in engineering education, the issue of quality needs to be addressed, debated and taken forward in a systematic manner. Accreditation is the principal means of quality assurance in higher education. The major emphasis of accreditation process is to measure the outcomes of the program that is being accredited. In line with this Faculty of Technology of University of Mumbai has taken a lead in incorporating philosophy of outcome based education in the process of curriculum development.

Faculty of Technology, University of Mumbai, in one of its meeting unanimously resolved that, each Board of Studies shall prepare some Program Educational Objectives (PEO's) and give freedom to affiliated Institutes to add few (PEO's). It is also resolved that course objectives and course outcomes are to be clearly defined for each course, so that all faculty members in affiliated institutes understand the depth and approach of course to be taught, which will enhance learner's learning process. It was also resolved that, maximum senior faculty from colleges and experts from industry to be involved while revising the curriculum. I am happy to state that, each Board of studies has adhered to the resolutions passed by Faculty of Technology, and developed curriculum accordingly. In addition to outcome based education, semester based credit and grading system is also introduced to ensure quality of engineering education.

Choice based Credit and Grading system enables a much-required shift in focus from teacher-centric to learner-centric education since the workload estimated is based on the investment of time in learning and not in teaching. It also focuses on continuous evaluation which will enhance the quality of education. University of Mumbai has taken a lead in implementing the system through its affiliated Institutes and Faculty of Technology has devised a transparent credit assignment policy and adopted ten points scale to grade learner's performance. Credit assignment for courses is based on 15 weeks teaching learning process, however content of courses is to be taught in 12-13 weeks and remaining 2-3 weeks to be utilized for revision, guest lectures, coverage of content beyond syllabus etc.

Choice based Credit and grading system is implementedfrom the academic year 2016-17 through optional courses at department and institute level. This will be effective for SE, TE and BE from academic year 2017-18, 2018-19 and 2019-20 respectively.

Dr. S. K. Ukarande
Co-ordinator,
Faculty of Technology,
Member - Academic Council
University of Mumbai, Mumbai

Chairman's Preamble:

The curriculum in higher education is a living entity. It evolves with time; it reflects the ever changing needs of the society and keeps pace with the growing talent of the students and the faculty. The engineering education in India is expanding in manifolds and the main challenge is the quality of education. All stakeholders are very much concerned about it. The curriculum of Electronics & Telecommunication in Mumbai University is no exception. In keeping with the demands of the changing times, it contains innovative features. The exposure to the latest technology and tools used all over the world is given by properly selecting the subjects. It is designed in such a way to incorporate the requirements of various industries. The major emphasis of this process is to measure the outcomes of the program. Program outcomes are essentially a range of skills and knowledge that a student will have at the time of post-graduation. So the curriculum must be refined and updated to ensure that the defined objectives and outcomes are achieved.

I, as Chairman Ad-hoc Board of Studies in Electronics and Telecommunication Engineering, University of Mumbai, happy to state here that, the heads of the department and senior faculty from various institutes took timely and valuable initiative to frame the Program Educational objectives as listed below.

Objectives:

1. To produce Electronics & Telecommunication engineers, having strong theoretical foundation, good design experience and exposure to research and development.
2. To produce researcher who have clear thinking, articulation and interest to carry out theoretical and/or applied research resulting in significant advancement in the field of specialization.
3. To develop an ability to identify, formulate and solve electronics and telecommunication engineering problems in the latest technology.
4. To develop the ability among students to synthesize data and technical concepts from applications to product design.

These are the suggested and expected main objectives, individual affiliated institutes may add further in the list. I believe that the small step taken in the right direction will definitely help in providing quality education to the stake holders.

This book of curricula is the culmination of large number of faculty members and supporting staff. It also reflects the creative contribution of hundreds of teachers – both serving and retired. I sincerely hope that the faculty and students of Electronics and Telecommunication in Mumbai University will take full advantage of dynamic features of curriculum and make teaching-learning process a truly sublime experience for all.

At the end I must extend my gratitude to all experts and colleagues who contributed to make curriculum competent at par with latest technological development in the field of Electronics & Telecommunication Engineering.

Dr. Uttam D. Kolekar
Chairman, Ad-hoc Board of Studies in Electronics and Telecommunication Engineering

Program Structure for
B.E. Electronics & Telecommunication Engineering (Rev. 2016)
University of Mumbai
(With Effect from 2017-2018)

Semester III

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned		
		Theory	Pracs	Tut	Theory	TW/ Pracs	Total
ECC301	Applied Mathematics- III	4	-	2@	4	1	5
ECC302	Electronic Devices and Circuits I	4	-	-	4	-	4
ECC303	Digital System Design	4	-	-	4	-	4
ECC304	Circuit Theory and Networks	4	-	2@	4	1	5
ECC305	Electronic Instrumentation and Control	4	-	2@	4	1	5
ECL301	Electronic Devices and Circuits I Laboratory	-	2	-	-	1	1
ECL302	Digital System Design Laboratory	-	2	-	-	1	1
ECL303	OOP using JAVA Laboratory	-	2	-	-	1	1
Total		**20**	**6**	**6**	**20**	**6**	**26**

@ 2 hour to be taken as tutorial classwise

Course Code	Course Name	Examination Scheme							
		Theory					TW	Oral/ Prac	Total
		Internal Assessment			End Sem Exam	Exam Duration (Hrs)			
		Test1	Test 2	Avg					
ECC301	Applied Mathematics-III	20	20	20	80	03	25	--	125
ECC302	Electronic Devices and Circuits I	20	20	20	80	03	--	--	100
ECC303	Digital System Design	20	20	20	80	03	--	--	100
ECC304	Circuit Theory and Networks	20	20	20	80	03	25	--	125
ECC305	Electronic Instrumentation and Control	20	20	20	80	03	25	--	125
ECL301	Electronic Devices and Circuits I Laboratory	--	--	--	--	--	25	25	50
ECL302	Digital System Design Laboratory	--	--	--	--	--	25	25	50
ECL303	OOP using JAVA Laboratory	--	--	--	--	--	25	25	50
Total				**100**	**400**		**150**	**75**	**725**

Semester IV

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned		
		Theory	Pracs	Tut	Theory	TW/ Pracs	Total
ECC401	Applied Mathematics- IV	4	-	2@	4	1	5
ECC402	Electronic Devices and Circuits II	4	-	-	4	-	4
ECC403	Linear Integrated Circuits	4	-	-	4	-	4
ECC404	Signals & Systems	4	-	2@	4	1	5
ECC405	Principles of Communication Engineering	4	-	-	4	-	4
ECL401	Electronic Devices and Circuits II Laboratory	-	2	-	-	1	1
ECL402	Linear Integrated Circuits Laboratory	-	2	-	-	1	1
ECL403	Principles of Communication Engineering Laboratory	-	2	-	-	1	1
Total		**20**	**6**	**4**	**20**	**5**	**25**

@ 2 hour to be taken as tutorial classwise

Course Code	Course Name	Examination Scheme							
		Theory					TW	Oral & Prac	Total
		Internal Assessment			End Sem Exam	Exam Duration (Hrs)			
		Test1	Test 2	Avg					
ECC401	Applied Mathematics- IV	20	20	20	80	03	25	--	125
ECC402	Electronic Devices and Circuits II	20	20	20	80	03	--	--	100
ECC403	Linear Integrated Circuits	20	20	20	80	03	--	--	100
ECC404	Signals & Systems	20	20	20	80	03	25	--	125
ECC405	Principles of Communication Engineering	20	20	20	80	03	--	--	100
ECL401	Electronic Devices and Circuits II Laboratory	--	--	--	--	--	25	25	50
ECL402	Linear Integrated Circuits Laboratory	--	--	--	--	--	25	25	50
ECL403	Principles of Communication Engineering Laboratory	--	--	--	--	--	25	25	50
Total				**100**	**400**		**125**	**75**	**700**

Semester V

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned		
		Theory	Pracs	Tut	Theory	TW/ Pracs	Total
ECC501	Microprocessor & Peripherals Interfacing	4	-	-	4	-	4
ECC502	Digital Communication	4	-	-	4	-	4
ECC503	Electromagnetic Engineering	4	-	1@	4	1	5
ECC504	Discrete Time Signal Processing	4	-	-	4	-	4
ECCDLO 501X	Department Level Optional Course I	4	-	-	4	-	4
ECL501	Microprocessor & Peripherals Interfacing Lab	-	2	-	-	1	1
ECL502	Digital Communication Lab	-	2	-	-	1	1
ECL503	Business Communication & Ethics Lab	-	2+2*	-	-	2	2
ECL504	Open Source Technology for Communication Lab	-	2	-	-	1	1
ECLDLO 501X	Department Level Optional Lab I	-	-	2#	-	1	1
Total		**20**	**10**	**3**	**20**	**7**	**27**

@ 1 hour to be taken as tutorial classwise #2 hours to be taken as either lab or tutorial based on subject requirement
*2 hours to be taken as tutorial batchwise

Course Code	Course Name	Examination Scheme							
		Theory					TW	Oral/ Prac	Total
		Internal Assessment			End Sem Exam	Exam Duration (Hrs)			
		Test1	Test 2	Avg					
ECC501	Microprocessor & Peripherals Interfacing	20	20	20	80	03	--	--	100
ECC502	Digital Communication	20	20	20	80	03	--	--	100
ECC503	Electromagnetic Engineering	20	20	20	80	03	25	--	125
ECC504	Discrete Time Signal Processing	20	20	20	80	03	--	--	100
ECCDLO 501X	Department Level Optional Course I	20	20	20	80	03	--	--	100
ECL501	Microprocessor & Peripherals Interfacing Lab	--	--	--	--	--	25	25	50
ECL502	Digital Communication Lab	--	--	--	--	--	25	25	50
ECL503	Business Communication & Ethics Lab	--	--	--	--	--	50	--	50
ECL504	Open Source Technology for Communication Lab	--	--	--	--	--	25	25	50
ECLDLO 501X	Department Level Optional Lab I	--	--	--	--	--	25	--	25
Total				**100**	**400**		**175**	**75**	**750**

Course Code	Department Level Optional Course I
ECCDLO 5011	Microelectronics
ECCDLO 5012	TV & Video Engineering
ECCDLO 5013	Finite Automata Theory
ECCDLO 5014	Data Compression and Encryption

Semester VI

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned		
		Theory	Pracs	Tut	Theory	TW/ Pracs	Total
ECC601	Microcontrollers & Applications	4	-	--	4	--	4
ECC602	Computer Communication Networks	4	-	-	4	-	4
ECC603	Antenna & Radio Wave Propagation	4	-	-	4	-	4
ECC604	Image Processing and Machine Vision	4	-	--	4	--	4
ECCDLO 602X	Department Level Optional Course II	4	-	-	4	-	4
ECL601	Microcontroller & Applications Lab	-	2	-	-	1	1
ECL602	Computer Communication Network Lab	-	2	-	-	1	1
ECL603	Antenna & Radio Wave Propagation Lab	-	2	-	-	1	1
ECL604	Image Processing and Machine Vision Lab	-	2	-	-	1	1
ECLDLO 602X	Department Level Optional Lab II	-	2	-	-	1	1
Total		**20**	**10**	**-**	**20**	**5**	**25**

Course Code	Course Name	Examination Scheme							
		Theory					TW	Oral & Prac	Total
		Internal Assessment			End Sem Exam	Exam Duration (Hrs)			
		Test1	Test 2	Avg					
ECC601	Microcontroller& Applications	20	20	20	80	03	--	--	100
ECC602	Computer Communication Network	20	20	20	80	03	--	--	100
ECC603	Antenna & Radio Wave Propagation	20	20	20	80	03	--	--	100
ECC604	Image Processing and Machine Vision Lab	20	20	20	80	03	--	--	100
ECCDLO 602X	Department Level Optional Course II	20	20	20	80	03	--	--	100
ECL601	Microcontroller & Applications Lab	--	--	--	--	--	25	25	50
ECL602	Computer Communication Network Lab	--	--	--	--	--	25	25	50
ECL603	Antenna & Radio Wave Propagation Lab	--	--	--	--	--	25	25	50
ECL604	Image Processing and Machine Vision Lab	--	--	--	--	--	25	25	50
ECLDLO 602X	Department Level Optional Lab II	--	--	--	--	--	25	--	25
Total				**100**	**400**		**125**	**100**	**725**

Course Code	Department Level Optional Course II
ECCDLO 6021	Digital VLSI Design
ECCDLO 6022	Radar Engineering
ECCDLO 6023	Database Management System
ECCDLO 6024	Audio Processing

Semester VII

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned		
		Theory	Pracs	Tut	Theory	TW/ Pracs	Total
ECC701	Microwave Engineering	4	-	-	4	-	4
ECC702	Mobile Communication System	4	-	-	4	-	4
ECC703	Optical Communication	4	-	--	4	-	4
ECCDLO 703X	Department Level Optional Course III	4	-	-	4	-	4
ILO701X	Institute Level Optional Course I	3	-	-	3	-	3
ECL701	Microwave Engineering Lab	-	2	-	-	1	1
ECL702	Mobile Communication System Lab	-	2	-	-	1	1
ECL703	Optical Communication Lab	-	2	-	-	1	1
ECLDLO 703X	Department Level Optional Lab III	-	2	-	-	1	1
ECL704	Project-I	-	6	-	-	3	3
Total		**19**	**14**	**-**	**19**	**7**	**26**

Course Code	Course Name	Examination Scheme							
		Theory					TW	Oral & Prac	Total
		Internal Assessment			End Sem Exam	Exam Duration (Hrs)			
		Test1	Test 2	Avg					
ECC701	Microwave Engineering	20	20	20	80	03	--	--	100
ECC702	Mobile Communication System	20	20	20	80	03	--	--	100
ECC703	Optical Communication	20	20	20	80	03	--	--	100
ECCDLO 703X	Department Level Optional Course III	20	20	20	80	03	--	--	100
ILO701X	Institute Level Optional Course I	20	20	20	80	03	--	--	100
ECL701	Microwave Engineering Lab	--	--	--	--	--	25	25	50
ECL702	Mobile Communication System Lab	--	--	--	--	--	25	25	50
ECL703	Optical Communication Lab	--	--	--	--	--	25	25	50
ECLDLO 703X	Department Level Optional Lab III	--	--	--	--	--	25	25	50
ECL704	Project-I	--	--	--	--	--	50	50	100
Total				**100**	**400**		**150**	**150**	**800**

Course Code	Department Level Optional Course III	Course Code	Institute Level Optional Course I[#]
ECCDLO7031	Neural Networks and Fuzzy Logic	ILO7011	Product Lifecycle Management
ECCDLO7032	Big Data Analytics	ILO7012	Reliability Engineering
ECCDLO7033	Internet Communication Engineering	ILO7013	Management Information System
ECCDLO7034	CMOS Mixed Signal VLSI	ILO7014	Design of Experiments
ECCDLO7034	Embedded System	ILO7015	Operation Research
		ILO7016	Cyber Security and Laws
		ILO7017	Disaster Management and Mitigation Measures
		ILO7018	Energy Audit and Management
		ILO7019	Development Engineering

 # Common with all branches

Semester VIII

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned		
		Theory	Pracs	Tut	Theory	TW/ Pracs	Total
ECC801	RF Design	4	-	--	4	--	4
ECC802	Wireless Networks	4	-	-	4	-	4
ECCDLO 804X	Department Level Optional Course IV	4	-	-	4	-	4
ILO802X	Institute Level Optional Course II	3	-	-	3	-	3
ECL801	RF Design Lab	-	2	-	-	1	1
ECL802	Wireless Networks Lab	-	2	-	-	1	1
ECLDLO 804X	Department Level Optional Lab IV	-	2	-	-	1	1
ECL803	Project-II	-	12	-	-	6	6
Total		**15**	**18**	**-**	**15**	**9**	**24**

Course Code	Course Name	Examination Scheme							
		Theory					TW	Oral & Prac	Total
		Internal Assessment			End Sem Exam	Exam Duration (Hrs)			
		Test1	Test 2	Avg					
ECC801	RF Design	20	20	20	80	03	--	--	100
ECC802	Wireless Networks	20	20	20	80	03	--	--	100
ECCDLO 804X	Department Level Optional Course IV	20	20	20	80	03	--	--	100
ILO802X	Institute Level Optional Course II	20	20	20	80	03	--	--	100
ECL801	RF Design Lab	--	--	--	--	--	25	25	50
ECL802	Wireless Networks Lab	--	--	--	--	--	25	25	50
ECLDLO 804X	Department Level Optional Lab IV	--	--	--	--	--	25	25	50
ECL803	Project-II	--	--	--	--	--	100	50	150
Total				**80**	**320**		**175**	**125**	**700**

Course Code	Department Level Elective Course IV	Course Code	Institute Level Elective Course II[#]
ECCDLO8041	Optical Networks	ILO8021	Project Management
ECCDLO8042	Advanced Digital Signal Processing	ILO8022	Finance Management
ECCDLO8043	Satellite Communication	ILO8023	Entrepreneurship Development and Management
ECCDLO8044	Network management in Telecommunication	ILO8024	Human Resource Management
		ILO8025	Professional Ethics and CSR
		ILO8026	Research Methodology
		ILO8027	IPR and Patenting
		ILO8028	Digital Business Management
		ILO8029	Environmental Management

 # Common with all branches

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECC301	Applied Mathematics-III	04	--	@2	04	--	01	05

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical & Oral	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test2	Avg. Of Test 1 and Test 2					
ECC301	Applied Mathematics-III	20	20	20	80	25	--	--	125

@ 2 hour to be taken as tutorial classwise

Course Pre-requisite:

- Applied Mathematics I
- Applied Mathematics II

Course Objectives:

1. To build the strong foundation in Mathematics of students needed for the field of electronics and Telecommunication Engineering
2. To provide students with mathematics fundamentals necessary to formulate, solve and analyses complex engineering problems.
3. To prepare student to apply reasoning informed by the contextual knowledge to engineering practice.
4. To prepare students to work as part of teams on multi-disciplinary projects.

Course Outcome:

After successful completion of the course student will be able to

1. Students will demonstrate basic knowledge of Laplace Transform. Fourier series, Bessel Functions, Vector Algebra and Complex Variable.
2. Students will demonstrate an ability to identify and Model the problems of the field of Electronics and Telecommunication and solve it.
3. Students will be able to apply the application of Mathematics in Telecommunication Engineering

Module No.	Unit No.	Detailed Content	Hours
1		**Laplace Transform**	**07**
	1.1	Laplace Transform (LT) of Standard Functions: Definition of Laplace transform, Condition of Existence of Laplace transform, Laplace transform of $e^{at}, Sin(at), \cos(at), \sinh(at), \cosh(at), t^n$ Heaviside unit step function, Dirac-delta function, Laplace transform of Periodic function	
	1.2	Properties of Laplace Transform: Linearity, first shifting theorem, second shifting theorem, multiplication by t^n, Division by *t,* Laplace Transform of derivatives and integrals, change of scale, convolution theorem, Evaluation of integrals using Laplace transform.	
2		**Inverse Laplace Transform & its Applications**	**06**
	2.1	Partial fraction method, Method of convolution, Laplace inverse by derivative	
	2.2	Applications of Laplace Transform: Solution of ordinary differential equations, Solving RLC circuit differential equation of first order and second order with boundary condition using Laplace transform (framing of differential equation is not included)	
3		**Fourier Series**	**11**
	3.1	Introduction: Orthogonal and orthonormal set of functions, Introduction of Dirchlet's conditions, Euler's formulae.	
	3.2	Fourier Series of Functions: Exponential, trigonometric functions of any period =2L, even and odd functions, half range sine and cosine series	
	3.3	Complex form of Fourier series, Fourier integral representation, Fourier Transform and Inverse Fourier transform of constant and exponential function.	
4		**Vector Algebra & Vector Differentiation**	**07**
	4.1	Review of Scalar and Vector Product: Scalar and vector product of three and four vectors, Vector differentiation, Gradient of scalar point function, Divergence and Curl of vector point function	
	4.2	Properties: Solenoidal and irrotational vector fields, conservative vector field	

5		**Vector Integral**	**06**
	5.1	Line integral	
	5.2	Green's theorem in a plane, Gauss' divergence theorem and Stokes' theorem	
6		**Complex Variable & Bessel Functions**	**11**
	6.1	Analytic Function: Necessary and sufficient conditions (No Proof), Cauchy Reiman equation Cartesian form (No Proof) Cauchy Reiman Equation in polar form (with Proof), Milne Thomson Method and it application, Harmonic function, orthogonal trajectories	
	6.2	Mapping: Conformal mapping, Bilinear transformations, cross ratio, fixed points	
	6.3	Bessel Functions: Bessel's differential equation, Properties of Bessel function of order +1/2 and -1/2, Generating function, expression of $\cos(x\sin\theta)$, $\sin(x\sin\theta)$ in term of Bessel functions	

Note: Term Work should be based on Tutorials.

Textbooks :

1. H.K. Das, "*Advanced engineering mathematics*", S . Chand, 2008
2. A. Datta, *"Mathematical Methods in Science and Engineering",* 2012
3. B.S. Grewal, *"Higher Engineering Mathematics",* Khanna Publication

Reference Books:

1. B. V. Ramana, *"Higher Engineering Mathematics",* Tata Mc-Graw Hill Publication
2. Wylie and Barret, *"Advanced Engineering Mathematics",* Tata Mc-Graw Hill 6th Edition
3. Erwin Kreysizg, *"Advanced Engineering Mathematics",* John Wiley & Sons, Inc
4. Murry R. Spieget, *"Vector Analysis",* Schaum's outline series, Mc-Graw Hill Publication

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECC302	Electronic Devices & Circuits-I	04	--	--	04	--	--	04

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical & Oral	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test2	Avg. Of Test 1 and Test 2					
ECC302	Electronic Devices & Circuits-I	20	20	20	80	--	--	--	100

Course Pre-requisite:

- Basic Electrical Engineering
- Applied Physics

Course Objectives:

1. To understand operation of semiconductor devices.
2. To understand DC analysis and AC models of semiconductor devices.
3. To apply concepts for the design of Regulators and Amplifiers
4. To verify the theoretical concepts through laboratory and simulation experiments.
5. To implement mini projects based on concept of electronics circuit concepts.

Course Outcome:

After successful completion of the course student will be able to

1. Understand the current voltage characteristics of semiconductor devices,
2. Analyze dc circuits and relate ac models of semiconductor devices with their physical Operation,
3. Design and analyze of electronic circuits,
4. Evaluate frequency response to understand behaviour of Electronics circuits.

Module No.	Unit No.	Detailed Content	Hours
1		**Diode and material science**	**06**
	1.1	Study of various types of resistor, capacitor and inductors	
	1.2	Basic fabrication steps of passive elements.	
	1.3	PN junction Diode characteristics, small signal model	
2		**Rectifier, Filters and Regulator**	**08**
	2.1	Analysis and design of rectifier circuit with Filters (L, LC, C,CLC,CRC)	
	2.2	Concept of load and line regulation in power supply circuits.	
	2.3	Analysis and design of zener voltage regulator	
3		**Transistor biasing and design**	**08**
	3.1	Operation of BJT, FET (N-CHANNEL, P-CHANNEL) with characteristics and equation.	
	3.2	Bipolar Junction Transistor: BJT characteristics, DC/AC load line, DC analysis and design of fixed bias, collector to base bias and voltage divider bias, stability factor analysis	
	3.3	Junction Field Effect Transistor: Analysis and design of self-bias and voltage divider bias, zero temp drift biasing.	
4		**Transistor modeling and Small signal analysis of amplifier**	**12**
	4.1	Hybrid and hybrid-pi model of BJT with graphical representation.	
	4.2	Small signal model of FET with graphical representation.	
	4.3	Small signal analysis (Zi, Zo, Av and Ai) of CE, CB, and CC configurations using hybrid-pi model of BJT	
	4.4	small signal (mid-frequency) analysis of CS, CD and CG amplifiers using FET	
5		**High frequency response of BJT and FET amplifiers**	**08**
	5.1	High frequency hybrid-pi equivalent Circuits of BJT and FET, Miller effect and Miller capacitance, gain bandwidth product	
	5.2	Effects of capacitors on frequency response of single stage amplifier using BJT and FET	
	5.3	Analysis of single stage amplifiers at HF and gain bandwidth product.	

6		**Design of small signal amplifiers**	**06**
	6.1	Design of single stage RC Coupled CE amplifier.	
	6.2	Design of single stage RC Coupled CS amplifier. (USE of parameters from data sheet compulsory)	

Textbooks :

1. D. A. Neamen, *"Electronic Circuit Analysis and Design,"* Tata McGraw Hill, 2nd Edition.
2. A. S. Sedra, K. C. Smith, and A. N. Chandorkar, *"Microelectronic Circuits Theory and Applications,"* International Version, OXFORD International Students, 6th Edition
3. R. S. Dudhe and M. Farhan, "*Electronic Devices and Circuits*," Synergy Knowledgeware, 1st Edition, 2013.

Reference Books:

1. Boylestad and Nashelesky, "*Electronic Devices and Circuits Theory*," Pearson Education, 11th Edition.
2. A. K. Maini , "*Electronic Devices and Circuits*," Wiley.
3. T. L. Floyd, "*Electronic Devices,"*Prentice Hall, 9th Edition, 2012.
4. A. Rockett, "*Material Science of Semiconductors*,", Springer, 1stEdition, 2009
5. A. Mottershead, "*Electronic Devices and Circuits; An Introduction,*"

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECC303	Digital System Design	04	--	--	04	--	--	04

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical & Oral	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test2	Avg. Of Test 1 and Test 2					
ECC303	Digital System Design	20	20	20	80	--	--	--	100

Course Objectives:

1. To understand number representation and conversion between different representation in digital electronic circuits.
2. To analyze logic processes and implement logical operations using combinational logic circuits.
3. To understand characteristics of memory and their classification.
4. To understand concepts of sequential circuits and to analyze sequential systems in terms of state machines.
5. To understand concept of Programmable Devices, PLA, PAL, CPLD and FPGA and implement digital system using VHDL.
6. To implement combinational and sequential circuits using VHDL.

Course Outcome:

After successful completion of the course student will be able to

1. Develop a digital logic and apply it to solve real life problems.
2. Analyze, design and implement combinational logic circuits.
3. Classify different semiconductor memories.
4. Analyze, design and implement sequential logic circuits.
5. Analyze digital system design using PLD.
6. Simulate and implement combinational and sequential circuits using VHDL systems.

Module	Unit No.	Detailed Content	Hours

No.			
1		**Number Systems and Codes**	**04**
	1.1	Review of Number System, Binary Code, Binary Coded Decimal, Octal Code, Hexadecimal Code and their conversions, Binary Arithmetics, Gray Code	
2		**Logic Gates and Combinational Logic Circuits**	**18**
	2.1	Analog and Digital signals and systems, Logic levels, TTL and CMOS Logic families and their characteristics	
	2.2	Digital logic gates, Realization using NAND, NOR gates, Boolean Algebra, De Morgan's Theorem, SOP and POS representation, K Map up to four variables and Quine-McClusky method	
	2.3	Arithmetic Circuits: Half adder, Full adder, Half Subtractor, Full Subtractor, Serial and Parallel Addition, Carry Look ahead adder and BCD adder. Binary Multiplier, Magnitude Comparator,	
	2.4	Multiplexer and De-multiplexer: Multiplexer operations, cascading of Multiplexer, Boolean Function implementation using multiplexer and basic gates, de-multiplexer, encoder and decoder	
3		**Different Types of Memory**	**02**
		Classification and Characteristics of memory, SRAM, DRAM, ROM, PROM, EPROM and Flash memories	
4		**Sequential Logic Circuits:**	**12**
	4.1	Flip flops: RS, JK, Master slave flip flops; T & D flip flops with various triggering methods, Conversion of flip flops, Registers: SISO, SIPO, PISO, PIPO, Universal shift registers.	
	4.2	Counters: Asynchronous and Synchronous, Up/Down, MOD N, BCD	
	4.3	Applications of Sequential Circuits: Frequency division, Ring Counter, Johnson Counter. models, State transition diagram, Design of Moore and Mealy circuits-Design of Serial Adder and vending Machine	
	4.4	State Reduction Techniques: Row elimination and Implication table methods	
5		**Programmable Logic Devices:**	**09**
		Introduction : Programmable Logic Devices (PLD), Programmable Logic Array (PLA), Programmable Array	

		Logic(PAL), CPLD and FPGA, Keyboard Encoder system design using PLD	
6		**VHSIC Hardware Description Language (VHDL)**	**03**
	6.1	Data types, Structural modeling using VHDL, Attributes, Data Flow behavioral, Implementation of Priority Encoder-combinational circuit and Fibonacci Series Generator-sequential circuits using VHDL	

Textbooks :

1. John F. Warkerly, "*Digital Design Principles and Practices*", Pearson Education, Fourth Edition (2008).
2. R. P. Jain, "*Modern Digital Electronics*", Tata McGraw Hill Education, Third Edition (2003).
3. J. Bhaskar, "*VHDL Primer"*, PHI, Third Edition (2009).
4. Volnei A. Pedroni, "*Digital Electronics and Design with VHDL*" Morgan Kaufmann Publisher (2008)

Reference Books:

1. Morris Mano / Michael D. Ciletti, "*Digital Design"*, Pearson Education, Fourth Edition (2008).
2. Thomas L. Floyd, "*Digital Fundamentals*", Pearson Prentice Hall, Eleventh Global Edition (2015).
3. Mandal, "*Digital Electronics Principles and Applications*", McGraw Hill Education, First Edition (2010).
4. Stephen Brown & Zvonko Vranesic, "*Fundamentals of Digital Logic Design with VHDL*", Second Edition, TMH (2009).
5. Ronald J. Tocci, Neal S. Widmer, "*Digital Systems Principles and Applications*", Eighth Edition, PHI (2003)
6. Donald P. Leach / Albert Paul Malvino/Gautam Saha, "*Digital Principles and Applications"*, The McGraw Hill, Seventh Edition (2011).

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECC304	Circuit Theory and Networks	04	--	@2	04	--	1	05

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical & Oral	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test2	Avg. Of Test 1 and Test 2					
ECC304	Circuit Theory and Networks	20	20	20	80	25	--	--	125

@ 2 hour to be taken as tutorial classwise

Course Pre-requisite:

- Basic Electrical Engineering
- Solution to Differential Educations and Laplace Transform

Course Objectives:

1. To analyze the Circuits in time and frequency domain
2. To study network Topology, network Functions, two port network
3. To synthesize passive network by various methods

Course Outcome:

After successful completion of the course student will be able to

1. Apply their knowledge in analysing Circuits by using network theorems.
2. Apply the time and frequency method of analysis.
3. Find the various parameters of two port network.
4. Apply network topology for analyzing the circuit
5. Synthesize the network using passive elements.

Module No.	Unit No.	Detailed Content	Hours
1		**Electrical circuit analysis**	**08**
	1.1	Analysis of DC & AC Circuits: Analysis of Circuits with and without controlled sources using generalized loop and node matrix methods Circuit Theorems: Superposition, Thevenin's, Norton's, maximum power transfer and reciprocity theorems	
	1.2	Magnetic circuits: Concept of Self and mutual inductances, coefficient of coupling, dot convention, equivalent circuit Coupled circuit- solution using mesh analysis	
2		**Graph Theory**	**08**
	2.1	Objectives of graph theory, Linear Oriented Graphs, graph terminologies Matrix representation of a graph: Incidence matrix, Circuit matrix, Cut-set matrix, reduced incident matrix, tieset matrix, f-cutset matrix.	
	2.2	Relationship between sub matrices A, B & Q.	
	2.3	KVL & KCL using matrix	
3		**Time and frequency domain analysis**	**08**
	3.1	Time domain analysis of R-L and R-C Circuits: Forced and natural response, initial and final values Solution using first order differential equation for impulse, step, ramp, exponential & sinusoidal signals	
	3.2	Time domain analysis of R-L-C Circuits: Forced and natural response, effect of damping factor. Solution using second order equation for step, ramp, exponential & sinusoidal signals.	
	3.3	Frequency domain analysis: Frequency - domain representation of R, L,C , initial value theorem & final value theorem, applications of Laplace Transform in analyzing electrical circuits	
4		**Network functions**	**08**
	4.1	Network functions for the one port and two port networks, Driving point and transfer functions, Poles and Zeros of Network functions, necessary condition for driving point	

		functions, necessary condition for transfer functions, calculation of residues by analytical and graphical methods, Time domain behavior as related to the Pole-Zero plot Stability & causality, testing for Hurwitz polynomial	
	4.2	Analysis of ladder & symmetrical lattice network	
5		**Two port Networks**	**08**
	5.1	Parameters: Open Circuits, short Circuit, Transmission and Hybrid parameters, relationship among parameters, conditions for reciprocity and symmetry	
	5.2	Interconnections of Two-Port networks T & π representation.	
	5.3	Terminated two-port networks	
6		**Synthesis of RLC circuits**	**08**
	6.1	Positive Real Functions: Concept of positive real function , testing for necessary and sufficient conditions for Positive real Functions	
	6.2	Synthesis of LC, RC & RL Circuits: properties of LC, RC & RL driving point functions, LC, RC & RL network Synthesis in Cauer-I & Cauer-II , Foster-I & Foster-II forms	

Note: Term Work should be based on Tutorials.

Textbooks :

1. Franklin F Kuo, *"Network Analysis and Synthesis",* Wiley Toppan, 2nd.ed. 1966
2. M E Van Valkenburg, *"Network Analysis",* Prentice-Hall of India Pvt Ltd, New Delhi, 26th Indian Reprint, 2000

Reference Books:

1. A Chakrabarti, *"Circuit Theory",* Dhanpat Rai & Co., Delhi, 6h Edition
2. A. Sudhakar, Shyammohan S. Palli "circuits and Networks, Tata McGraw-Hill education
3. Smarajit Ghosh, Network Theory Snallysis & Syntshesis, PHI learning
4. K.S. Suresh Kumar, Elecric circuit analysis, Pearson (2013)
5. D Roy Choudhury, Networks and Systems, New Age International 1998.

TUTORIALS: At least 10 tutorials covering various topics of the syllabus.

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECC305	Electronic Instrumentation & Control	04	--	@2	04	--	1	05

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical & Oral	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test2	Avg. Of Test 1 and Test 2					
ECC305	Electronic Instrumentation & Control	20	20	20	80	25	--	--	125

@ 2 hour to be taken as tutorial classwise

Course Pre-requisite:

- Basic Electrical Engineering

Course Objectives:

1. To provide basic knowledge about the various sensors and data acquisition systems applied in Wireless sensor network.
2. To provide fundamental concepts of control system such as mathematical modeling, time response and frequency response.
3. To develop concepts of stability and its assessment criteria.

Course Outcome:

After successful completion of the course student will be able to

1. Students will be able to explain principle of operation for various sensors.
2. Students will be able to describe functional blocks of data acquisition system.
3. Students will be able to find transfer functions for given system.
4. Students will be able to calculate time domain and frequency domain parameter for given system
5. Students will be able to predict stability of given system using appropriate criteria.

Module No.	Unit No.	Detailed Content	Hours
1		**Principle of Measurement, Testing and Measuring instruments**	**07**
	1.1	Introduction to Basic instruments: Components of generalized measurement system Concept of accuracy, precision, linearity, sensitivity, resolution, hysteresis, calibration.	
	1.2	Measurement of Resistance: Kelvin's double bridge, Wheatstone bridge and Mega ohm bridge Measurement of Inductance: Maxwell bridge and Hey bridge Measurement of Capacitance: Schering bridge Q-Meter: Operating principle and applications Energy and power meters: Working of energy and power meter	
2		**Sensors and Transducers**	**08**
	2.1	Basics of sensors and Transducers-Active and passive transducers, characteristics and selection criteria of transducers, working principle of Eddy-current sensors, Pizoelectric transducers, photoelectric and photovoltaic sensors, capacitive sensors	
	2.2	Displacement and pressure- Potentiometers, pressure gauges, linear Variable differential transformers(LVDT) for measurement of pressure and displacement strain gauges	
	2.3	Temperature Transducers- Resistance temperature detectors(RTD). Thermistors and thermocouples , their ranges and applications	
3		**Telemetry and Data Acquisition System**	**08**
	3.1	Introduction and characteristics, Landline Telemetry, Radio Telemetry Types of Multiplexing Systems,	
	3.2	Data Acquisition: Components of Analog and Digital Data Acquisition System,	
	3.3	Uses of Data Acquisition System, Use of recorders in Digital systems, Modern Digital Data Acquisition System.	

4		**Introduction to control system Analysis**	**07**
	4.1	Introduction: Open and closed loop systems, example of control systems	
	4.2	Modelling: Modelling, Transfer function model of electrical systems, Block diagram reduction techniques and Signal flow graph	
	4.3	Dynamic Response: Standard test signals, transient and steady state behaviour of first and second order systems , steady state errors in feedback control systems and their types	
5		**Stability Analysis in Time Domain**	**08**
	5.1	Concept of stability: Routh and Hurwitz stability criterion	
	5.2	Root locus Analysis: Root locus concept, general rules for constructing root-locus ,root locus analysis of control system, concept of design of lag and lead compensator	
6		**Stability Analysis in frequency domain**	**10**
	6.1	Introduction: Frequency domain specification, Relationship between time and frequency domain specification of system,stability margins	
	6.2	Bode Plot: Magnitude and phase plot, Method of plotting Bode plot,Stability margins and analysis using bode plot. Frequency response analysis of RC,RL,RLC circuits	
	6.3	Nyquist Criterion: Concept of Polar plot and Nyquist plot, Nyquist stability criterion ,gain and phase margin	

Note: Term Work should be based on Tutorials.

Textbooks :

1. A.K. Sawhney, "*Electrical & Electronic Measurement & Instrumentation"* – DRS . India
2. M.M.S. Anand, "*Electronic Instruments and instrumentation Technology"*.
3. H.S.Kalsi, "*Electronic Instrumentation"*-TMH, 2nd Edition.
4. Nagrath, M.Gopal, "*Control System Engineering*", Tata McGraw Hill.
5. K.Ogata, "*Modern Control Engineering*, Pearson Education", IIIrd edition.

Reference Books:

1. Helfrick&Copper, "*Modern Electronic Instrumentation & Measuring Techniques"* – PHI
2. W.D. Cooper, "*Electronic Instrumentation And Measuring Techniques"* – PHI

3. Benjamin C.Kuo, "*Automatic Control Systems*, Eearson education", VIIth edition
4. Rangan C. S., Sarma G. R. and Mani V. S. V., "*Instrumentation Devices And Systems*", Tata McGraw-Hill, 2nd Ed., 2004.
5. Bell David A."*Electronic Instrumentation and Measurements*", PHI Pearson Education, 2006.
6. Madan Gopal, "*Control Systems Principles and Design"*, Tata McGraw hill, 7th edition,1997.
7. Normon, "*Control System Engineering"*, John Wiley & sons, 3rd edition.

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	TW/Pracs	Tutorial	Total
ECL301	Electronic Devices & Circuits-I Laboratory	--	02	--	--	1	--	1

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical & Oral	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test2	Avg. Of Test 1 and Test 2					
ECL301	Electronic Devices & Circuits-I Laboratory	--	--	--	--	25	25	--	50

Laboratory plan

Maximum of 8 practicals including **minimum 2 simulations** should be conducted based on following topics

- Study of different measuring instruments such as CRO, Function Generator, Multimeter, and Power Supply. (Compulsory)
- Filter circuits
- Biasing of BJT and FET
- Frequency response
- Zener regulator
- Single stage amplifiers

Minimum One project based on:

- Design of single stage CE and CS amplifier
- Design of filter and regulator circuits
- Design of power supply
- Any other relevant topic based on syllabus

Note : Small project should be considered as a part of term-work.

Term Work:

At least 08 Experiments including 02 simulations covering entire syllabus must be given during the "**Laboratory session batch wise"**. Computation/simulation based experiments are also encouraged. The experiments should be students centric and attempt should be made to make experiments more meaningful, interesting and innovative. Application oriented one mini-project can be conducted for maximum batch of four students.

Term work assessment must be based on the overall performance of the student with every experiments/tutorials and mini-projects are graded from time to time. The grades will be converted to marks as per "**Choice Based Credit and Grading System"** manual and should be added and averaged. Based on above scheme grading and term work assessment should be done.
The practical and oral examination will be based on entire syllabus.

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	TW/Pracs	Tutorial	Total
ECL302	Digital System Design Laboratory	--	02	--	--	1	--	1

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical & Oral	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test2	Avg. Of Test 1 and Test 2					
ECL302	Digital System Design Laboratory	--	--	--	--	25	25	--	50

Laboratory plan

Maximum of 8 practicals including **minimum 2 simulations** should be conducted.

Suggested list of experiments:

1. Verify different logic gates.
2. Simplification of Boolean functions.
3. Verify Universal gates NAND and NOR and design EXOR and EXNOR gates using Universal gates.
4. Implement Half adder, Full adder, Half subtractor and Full subtractor circuits.
5. Implement BCD adder using four bit binary adder IC-7483.
6. Flip flops conversion JK to D, JK to T and D to TFF.
7. Implement logic equations using Multiplexer.
8. Design synchronous MOD N counter using IC-7490.
9. Verify encoder and decoder operations.
10. Implement digital circuits to perform binary to gray and gray to binary operations.
11. Verify truth table of different types of flip flops.
12. Verify different counter operations.
13. Write VHDL simulation code for different logic gates.
14. Write VHDL simulation code for combinational and sequential circuits
15. Write VHDL simulation code for 4:1 Multiplexer, 2 line to 4 line binary decoder

Minimum One project

Suggested list of Mini Projects:

1. Design Clock pulse generator.
2. Design Clap operated remote control for Fan.
3. Design BCD counter and show operation on Seven Segment Display.
4. Design digital stop watch.
5. Write VHDL code to implement traffic light controller.
6. Design water level indicator for overhead water tank.
7. Design frequency divider circuit.
8. Design switch debounce circuit.
9. Design sequence generator circuit.
10. Design sequence detector circuit.
11. Design Even/Odd parity generator/checker circuit.
12. Design simple LED flasher circuit.
13. Design digital dice.
14. Design fastest finger first indicator.
15. Design Toggle switch using TFF.

Note : Small project should be considered as a part of term-work.

Term Work:

At least 08 Experiments including 02 simulations covering entire syllabus must be given during the "**Laboratory session batch wise"**. Computation/simulation based experiments are also encouraged. The experiments should be students centric and attempt should be made to make experiments more meaningful, interesting and innovative. Application oriented one mini-project can be conducted for maximum batch of four students.

Term work assessment must be based on the overall performance of the student with every experiments/tutorials and mini-projects are graded from time to time. The grades will be converted to marks as per "**Choice Based Credit and Grading System"** manual and should be added and averaged. Based on above scheme grading and term work assessment should be done.
The practical and oral examination will be based on entire syllabus.

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	TW/Pracs	Tutorial	Total
ECL303	OOP using JAVA laboratory	--	02	--	--	1	--	1

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical & Oral	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test2	Avg. Of Test 1 and Test 2					
ECL303	OOP using JAVA laboratory	--	--	--	--	25	25	--	50

Course Pre-requisites:

- Fundamentals of C-Programming
- Control Structures
- Arrays and String

Course Objectives:

1. To understand Object Oriented Programming and its principles.
2. To describe & explain keywords and Data types.
3. Able to implement Methods, Constructors, Arrays, Multithreding and Applet
4. To help students to understand how to use a programming language to resolve problems.

Course Outcomes:

1. Students will be able to code a program using JAVA constructs.
2. Students will be able to understand fundamental features of an object oriented language: object classes and interfaces, exceptions and libraries of object collections.
3. Students will be able to develop a program that efficiently implements the algorithm for given tasks.
4. Students will be able to utilize the knowledge acquired in this course to develop higher level algorithms.

Module No.	Unit No.	Detailed Content	Hours
1		**Fundamental Concepts of Object Oriented Programming**	**06**
	1.1	Introduction to Object-Oriented Programming	
	1.2	Classes, Objects, Creating Classes and Objects, Principles of OOP**:** Abstraction, Encapsulation, Inheritance, Polymorphism	
	1.3	Differences And Similarity Between C and Java	
2		**Fundamental Of Java Programming**	**08**
	2.1	Features of Java, JDK Environment & Tools, Structure of Java Program	
	2.2	Java Keywords, Super Keyword, Final Keyword, Abstract Class	
	2.3	Data Types, Variables, Operators,Expressions	
	2.4	Input Output Using Scanner Class	
	2.5	Exception Handling, Object-Oriented Containers	
3		**Method, Constructors, Destructors And Arrays**	**04**
	3.1	Passing and Returning Parameters to Methods	
	3.2	Constructor and Types, Destructor	
	3.3	Arrays and Types**:** Create, One Dimensional Arrays, Two Dimensional Array, Multidimensional Array, String Array	
4		**Inheritance, Interface And Package**	**04**
	4.1	Types of Inheritance: Single, Multilevel, Hierarchical	
	4.2	Method Overloading and Method Overriding	
	4.3	Interface	
	4.4	Packages	
5		**Multithreading And Applet**	**04**
	5.1	Life Cycle Of Thread	
	5.2	Priority In Multithreading	
	5.3	Applet Life Cycle	
	5.4	Creating Applet, Applet Tag	

Textbooks :

1. Herbert Schidt, "*The Complete Reference*", Tata McGraw-Hill Publishing Company Limited, Ninth Edition
2. D.T. Editorial Services ,"*Java 8 Programming Black Book*", Dreamtech Press, Edition: 2015
3. Yashwant Kanitkar,"*Let Us Java*", BPB Publications; 2nd Edition edition.

Reference Books:

1. Java: How to Program, 8/e, Dietal, Dietal, PHI
2. Grady Booch, James Rumbaugh, Ivar Jacobson, "*The Unified Modeling Languageser Guide*", Pearson Education
3. Sachin Malhotra, Saurabh Chaudhary "*Programming in Java*", Oxford University Press, 2010

Software Tools:

1. Raptor-Flowchart Simulation:http://raptor.martincarlisle.com/
2. Eclipse: https://eclipse.org/
3. Netbeans:https://netbeans.org/downloads/
4. CodeBlock:http://www.codeblocks.org/
5. J-Edit/J-Editor/Blue J

Online Repository:

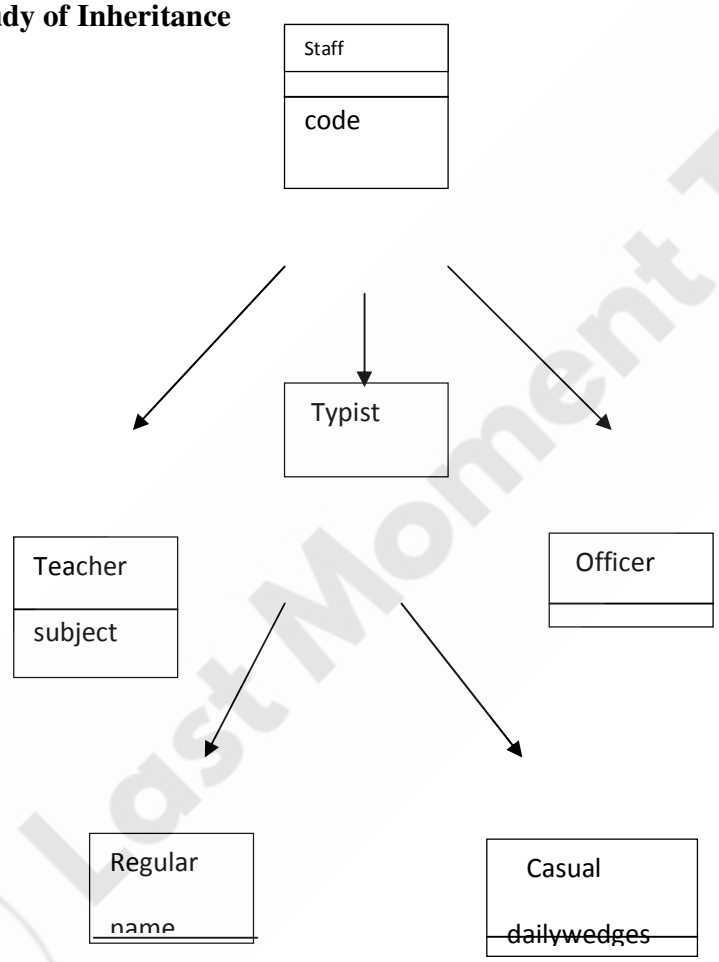
1. Google Drive
2. GitHub
3. Code Guru

Laboratory plan

Maximum of 8 practicals including **minimum 2 simulations** should be conducted based on following topics

Section	Experiment Name	Module
1.	**Write a program using command line argument in java.** • Echoing Command-Line Arguments. • Parsing Numeric Command-Line arguments.	Module 1
2.	**Study of simple java programs** • WAP to calculate area & circumference of circle • WAP to swap given two strings • WAP to separate out digits of a number • WAP to convert temperature from Fahrenheit to Celsius • WAP to find a square , squarroot, and Cube of a given no. using abstraction	Module 1
3.	**Study of different operators in java** • WAP to compare two numbers. • WAP to print truth table for java logical operators • WAP to read the number & shift left & right by 3 bits.	Module 1
4.	**Write a program for various ways of accepting data through keyboard & display its content.** • Read through DataInputstream. • Read input through Scanner. • Read input through BufferedReader.	Module 2
5.	**Study of Arrays** Write a program for addition, subtraction and multiplication of two matrices.	Module 3
6.	**Study of Objects and Classes** • Define a class to represent a bank account. Include the following members: Data: name of the depositor account number	Module 3

	type of account balance amount in the account Methods: 1.to assign initial values 2.to deposit an amount 3.to withdraw an amount after checking balance. 4.to display the name & balance • WAP using this keyword	
7.	**Study of Strings**. Accept the two strings from user & do the following operations • convert to lowercase • convert to uppercase • Replace all appearance of one character by another • Compare two strings • Derive the substring of a string • Derive the position of a character in a string • Calculate the length of a string • Derive the nth character of a string	Module 2
8.	**WAP to implement following constructors** • Default constructor • Parameterized constructor	Module 3
9.	**Study of Interface**. Create an interface Area & implement the same in different classes Rectangle ,circle ,triangle.	Module 4
10.	**Study of utility package** • WAP to generate a year using random class and check whether it is leap or not. • Write a program to display current date. Also display Time in hours &	Module 4

	Minutes using Date class.	
11.	**Study of Inheritance** 	Module 4
12.	**Study of Exception Handling in java**. Write a program to use throw finally and try catch to handle exception.	Module 2

13.	**Study of Multithreading.** WAP to illustrate function yield(), isAlive(), sleep(), join(). Create three threads as P,Q,R. Thread P has maximum priority, thread Q has minimum priority, thread R has normal priority.	Module 5
14.	**Study graphics using applet.** WAP to draw all geometric shapes and fill them with different colors.	Module 5

Minimum One project
Suggested list of mini projects

1. Inventory Control System
2. Develop Calculator
3. Develop Editor (Example: Notepad)
4. Devlop Multimedia App to teach primary students (Sahpes, Colors, etc.)
5. Create an audio or video applet or swing based application with play, pause and stop options.

Note : Small project should be considered as a part of term-work.

Term Work:

At least **10** experiments covering entire syllabus should be set to have well predefined inference and conclusion.

The experiments should be students centric and attempt should be made to make experiments more meaningful, interesting and innovative. Application oriented one mini-project can be conducted for maximum batch of four students.

Term work assessment must be based on the overall performance of the student with every experiments/tutorials and mini-projects are graded from time to time. The grades will be converted to marks as per "**Choice Based Credit and Grading System"** manual and should be added and averaged. Based on above scheme grading and term work assessment should be done.
The practical and oral examination will be based on entire syllabus.
Students are encourages to share their experiments/mini project codes on online repository.

Practical from any 10 sections out of 14 sections is compulsory . Practical exam slip should cover all at least 10 sections.

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECC401	Applied Mathematics-IV	04	--	@2	04	--	01	05

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical & Oral	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test2	Avg. Of Test 1 and Test 2					
ECC401	Applied Mathematics-IV	20	20	20	80	25	--	--	125

@2 hour to be taken as tutorial classwise

Course Pre-requisite:

- Applied Mathematics I
- Applied Mathematics II
- Applied Mathematics III

Course Objectives:

1. To build the strong foundation in Mathematics of students needed for the field of Electronics and Telecommunication Engineering
2. To provide students with mathematics fundamentals necessary to formulate, solve and analyses complex engineering problems.
3. To prepare student to apply reasoning informed by the contextual knowledge to engineering practice.
4. To prepare students to work as part of teams on multi-disciplinary projects

Course Outcome:

After successful completion of the course student will be able to

1. Demonstrate basic knowledge of Calculus of variation, Vector Spaces, Matrix Theory, Random Variables, Probability Distributions, Correlation and Complex Integration.
2. Demonstrate an ability to identify and Model the problems in the field of Electronics and Telecommunication and solve it.
3. Apply the application of Mathematics in Telecommunication Engineering.

Module No.	Unit No.	Detailed Content	Hours
1		**Calculus of Variation:**	**06**
	1.1	Euler's Langrange equation, solution of Euler's Langrange equation (only results for different cases for Function) independent of a variable, independent of another variable, independent of differentiation of a variable and independent of both variables	
	1.2	Isoperimetric problems, several dependent variables	
	1.3	Functions involving higher order derivatives: Rayleigh-Ritz method	
2		**Linear Algebra: Vector Spaces**	**06**
	2.1	Vectors in n-dimensional vector space: properties, dot product, cross product, norm and distance properties in n-dimensional vector space.	
	2.2	Vector spaces over real field, properties of vector spaces over real field, subspaces	
	2.3	The Cauchy-Schwarz inequality, Orthogonal Subspaces, Gram-Schmidt process	
3		**Linear Algebra: Matrix Theory**	10
	3.1	Characteristic equation, Eigen values and Eigen vectors, properties of Eigen values and Eigen vectors.	
	3.2	Cayley-Hamilton theorem (without proof), examples based on verification of Cayley- Hamilton theorem.	
	3.3	Similarity of matrices, Diagonalisation of matrices.	
	3.4	Functions of square matrix, derogatory and non-derogatory matrices.	
4		**Probability**	**10**
	4.1	Baye's Theorem (without proof)	
	4.2	Random variable: Probability distribution for discrete and continuous random variables, Density function and distribution function, expectation, variance.	
	4.3	Moments, Moment Generating Function.	

	4.4	Probability distribution: Binomial distribution, Poisson & normal distribution (For detailed study)	
5		**Correlation**	**04**
	5.1	Karl Pearson's coefficient of correlation, Covariance, Spearman's Rank correlation,	
	5.2	Lines of Regression.	
6		**Complex integration**	**12**
	6.1	Complex Integration:Line Integral, Cauchy's Integral theorem for simply connected regions, Cauchy's Integral formula.	
	6.2	Taylor's and Laurent's Series	
	6.3	Zeros, singularities, poles of f(z), residues, Cauchy's Residue theorem.	
	6.4	Applications of Residue theorem to evaluate real Integrals of different types.	

Note: Term Work should be based on Tutorials.

Textbooks :

1. H.K. Das, "*Advanced engineering mathematics*", S . Chand, 2008
2. A. Datta, *"Mathematical Methods in Science and Engineering",* 2012
3. B.S. Grewal, *"Higher Engineering Mathematics",* Khanna Publication
4. P.N.Wartilar&J.N.Wartikar, "*A Text Book of Applied Mathematics*" Vol. I & II, Vidyarthi Griha Prakashan, Pune

Reference Books:

1. B. V. Ramana, *"Higher Engineering Mathematics",* Tata Mc-Graw Hill Publication
2. Wylie and Barret, *"Advanced Engineering Mathematics",* Tata Mc-Graw Hill 6th Edition
3. Erwin Kreysizg, *"Advanced Engineering Mathematics",* John Wiley & Sons, Inc
4. Seymour Lipschutz "*Beginning Linear Algebra*" Schaum's outline series, Mc-Graw Hill Publication
5. SeymourLipschutz "*Probability*" Schaum's outline series, Mc-Graw Hill Publication

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECC402	Electronic Devices & Circuits-II	04	--	--	04	--	--	04

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical & Oral	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test2	Avg. Of Test 1 and Test 2					
ECC402	Electronic Devices & Circuits-II	20	20	20	80	--	--	--	100

Course Pre-requisite:

- Electronic Devices & Circuits-I

Course Objectives:

1. To understand the operation of the various bias circuits of MOSFET and Analyze and design MOSFET bias circuits.
2. To understand the operation and design of multistage amplifier for a given specification.
3. To understand the operation and design of transformer coupled various types of power amplifier circuits.
4. To understand the effects of negative feedback on amplifier circuits.
5. To analyze the different *RC* and *LC* oscillator circuits to determine the frequency of oscillation.

Course Outcome:

After successful completion of the course student will be able to

1. Design and analyse the basic operations of MOSFET.
2. Know about the multistage amplifier using BJT and FET in various configuration to determine frequency response and concept of voltage gain.
3. Know about different power amplifier circuits, their design and use in electronics and communication circuits.
4. Know the concept of feedback amplifier and their characteristics.
5. Design the different oscillator circuits for various frequencies

Module No.	Unit No.	Detailed Content	Hours
1		**Introduction to MOSFET**	**08**
	1.1	MOSFET - Symbol, Types of MOSFET - Depletion and Enhancement type MOSFET (N channel and P channel),	
	1.2	Construction, Operation, and V-I characteristics of MOSFET	
	1.3	MOSFET biasing - Types of Depletion & enhancement MOSFET biasing,	
	1.4	MOSFET as amplifier	
2		**Introduction of Multistage amplifiers**	**06**
	2.1	RC coupled, transformer coupled, direct coupled,	
	2.2	Low and high frequency considerations of cascade amplifier, cascode amplifier (CE-CB), Darlington pair amplifier.	
3		**Design of Multistage amplifiers**	**10**
		Analysis and design considerations of multistage amplifiers (CE-CE, CS-CS, CS-CE,), effect of source and load resistance	
4		**Large signal amplifiers**	**08**
	4.1	Harmonic distortion and power efficiency of Class A, B, AB, and C amplifiers	
	4.2	Design of Class A, Class B, and Push-Pull Power amplifier design.	
	4.3	Thermal considerations and design selection of heat sinks.	
5		**Feedback amplifiers**	**08**
	5.1	Feedback concept, ideal feedback amplifier, classification of feedbacks, Various topologies	
	5.2	Analysis and design of different types of negative feedback.	
6		**Oscillators**	**08**
	6.1	Principle of oscillation, RC oscillator, twin T oscillator	
	6.2	Oscillator with LC feedback. Colpitts oscillator, Hartley oscillator, Crystal controlled oscillator.	
	6.3	Design of different oscillator circuits.	

Textbooks :

1. D. A. Neamen, *"Electronic Circuit Analysis and Design,"* Tata McGraw Hill, 2nd Edition.
2. R. L. Boylestad,"Electronic Devices and Circuit Theory," Pearson, 11th Edition.
3. T. F. Bogart,"Electronic Devices And Circuit," Merrill, 6th Edition.
4. R. S. Dudhe and M. Farhan, "Electronic Devices and Circuits," Synergy Knowledgeware, 1st Edition

Reference Books:

1. Salivahanan, N. Suresh Kumar,"*Electronic Devices and Circuits*," Tata McGraw Hill, 3rd Edition
2. J. Millman, Christos CHalkias, and Satyabratatajit, Millman's,"*Electronic Devices and Circuits*," McGrawHill, 3rd Edition
3. Muhammad H. Rashid, "*Microelectronics Circuits Analysis and Design*," Cengage Learning, 2nd Edition.

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECC403	Linear Integrated Circuits	04	--	--	04	--	--	04

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical & Oral	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test2	Avg. Of Test 1 and Test 2					
ECC403	Linear Integrated Circuits	20	20	20	80	--	--	--	100

Course Pre-requisite:

- Basic Electrical Engineering
- Electronic Devices & Circuits-I

Course Objectives:

1. To understand the concepts, working principles and key applications of linear integrated circuits.
2. To perform analysis of circuits based on linear integrated circuits.
3. To design circuits and systems for particular applications using linear integrated circuits.

Course Outcome:

After successful completion of the course student will be able to

1. Understand the fundamentals and areas of applications for the integrated circuits.
2. Analyze important types of integrated circuits.
3. Demonstrate the ability to design practical circuits that perform the desired operations.
4. Understand the differences between theoretical, practical & simulated results in integrated circuits.
5. Select the appropriate integrated circuit modules to build a given application.

Module No.	Unit No.	Detailed Content	Hours
1		**Introduction to operational amplifiers**	**08**
	1.1	Analysis of differential amplifier circuit configurations using FETs, Effect of Swamping resistor, Current sources using FETs, Widlar current source, Wilson current source, Voltage sources and references, DC level shifters.	
	1.2	Ideal & Practical Operational Amplifiers, Operational amplifier characteristics, Operational amplifier parameters, Operational amplifier open loop and closed loop configurations.	
2		**Applications of Operational Amplifier**	**08**
	2.1	Amplifiers: Inverting, non-inverting, buffer, summing & difference amplifiers, integrator & differentiator (ideal & practical), current amplifier, instrumentation amplifier, log and antilog amplifiers..	
	2.2	Converters: Current to voltage converters, voltage to current converters, voltage to frequency converter, frequency to voltage converter.	
	2.3	Active Filters: Second order active low pass, high pass, band pass and band reject filters, Introduction to switch capacitor filters.	
	2.4	Sine Wave Oscillators: RC phase shift oscillator, Wien bridge oscillator.	
3		**Non-Linear Applications of Operational Amplifier**	**08**
	3.1	Comparators: Inverting comparator, non-inverting comparator, zero crossing detector, window detector, peak detector, sample & hold circuits.	
	3.2	Schmitt Triggers: Inverting Schmitt trigger, non-inverting Schmitt trigger.	
	3.3	Waveform Generators: Square wave generator and triangular wave generator.	
	3.4	Precision Rectifiers: Half wave and full wave precision rectifiers.	
4		**Analog to Digital and Digital to Analog Convertors**	**08**
	4.1	Performance specifications of ADC, single ramp ADC, ADC using DAC, dual slope ADC, successive approximation ADC.	
	4.2	Performance specifications of DAC, binary weighted resistor DAC, R/2R ladder DAC, inverted R/2R ladder DAC.	
5		**Special Purpose Integrated Circuits**	**08**
	5.1	Functional block diagram and working of IC 555, design of astable and monostable multivibrator using IC 555, application	

		of IC 555 as pulse position modulator, pulse width modulator and Schmitt Trigger.	
	5.2	Functional block diagram and working of VCO IC 566 and application as frequency modulator, Functional block diagram and working of PLL IC 565 and application as FSK Demodulator, Functional block diagram and working of multiplier IC 534andapplication as a phase detector, Functional block diagram and working of waveform generator XR 2206 and application as sinusoidal FSK generator.	
6		**Voltage Regulators**	**08**
	6.1	Functional block diagram, working and design of three terminal fixed (78XX, 79XX series) and three terminal adjustable (LM 317, LM 337) voltage regulators.	
	6.2	Functional block diagram, working and design of general purpose 723 (LVLC, LVHC, HVLC and HVHC) with current limit and current fold-back protection, Switching regulator topologies, Functional block diagram and working of LT1070 monolithic switching regulator.	

Textbooks :

1. Ramakant A. Gayakwad, "*Op-Amps and Linear Integrated Circuits*", Pearson Prentice Hall, 4th Edition.
2. K. R. Botkar, "*Integrated Circuits*", Khanna Publishers (2004)
3. D. Roy Choudhury and S. B. Jain, "*Linear Integrated Circuits*", New Age International Publishers, 4th Edition.

Reference Books:

1. Sergio Franco, "*Design with operational amplifiers and analog integrated circuits*", Tata McGraw Hill, 3rd Edition.
2. David A. Bell, "*Operation Amplifiers and Linear Integrated Circuits*", Oxford University Press, Indian Edition.
3. R. F. Coughlin and F. F. Driscoll, "*Operation Amplifiers and Linear Integrated Circuits*", Prentice Hall, 6th Edition.
4. "J. Millman, Christos CHalkias, and Satyabratatajit, Millman's,"Electronic Devices and Circuits," McGrawHill, 3rdEdition".

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECC404	Signals and Systems	04	--	2@	04	--	01	05

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical & Oral	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test2	Avg. Of Test 1 and Test 2					
ECC404	Signals and Systems	20	20	20	80	25	--	--	125

@2 hour to be taken as tutorial classwise

Course Pre-requisite:

- Applied Maths-III
- Circuit Theory and Networks

Course Objectives:

1. To introduce students the concept and theory of signals and systems needed in electronics and telecommunication engineering fields.
2. To introduce students to the basic idea of signal and system analysis and its characterization in time and frequency domain

Course Outcome:

After successful completion of the course student will be able to

1. Understand about various types of signals and systems, classify them, analyze them, and perform various operations on them,
2. Understand use of transforms in analysis of signals and system in continuous and discrete time domain.
3. Observe the effect of various properties and operations of signals and systems.
4. Evaluate the time and frequency response of Continuous and Discrete time systems which are useful to understand the behaviour of electronic circuits and communication systems.

Module No.	Unit No.	Detailed Content	Hours
1		**Introduction to signals and systems**	**08**
	1.1	Introduction to signals: Definition, sampling theorem, sampling of continuous time signals, elementary signals: exponential, sine, step, impulse, ramp, rectangular, triangular, signum, sinc, operations on signals,	
	1.2	Classification of signals: Continuous and discrete time, deterministic and non-deterministic, periodic and aperiodic, symmetric (even) and asymmetric (odd), energy and power, causal and anti-causal signal, Case study of different signals from communication and biomedical field	
	1.3	Introduction to systems: Definition, Classification of systems: Static and dynamic, time variant and time invariant, linear and nonlinear, causal and non-causal, stable and unstable systems., communication and control system as examples	
2		**Time domain analysis of continuous time and discrete time systems**	**08**
	2.1	Representation of systems using differential /difference equation, Impulse, step and exponential response, system stability	
	2.2	Use of convolution integral and convolution sum for analysis of LTI systems, properties of convolution integral/sum, impulse response of interconnected systems	
	2.3	Correlation and spectral Density**:** auto-correlation, cross correlation, analogy between correlation and convolution, energy spectral density, power spectral density, relation of ESD,PSD with auto-correlation	
3		**Frequency domain analysis of continuous and discrete signals:**	**10**
	3.1	**Review of Fourier series:** Trigonometric and exponential Fourier series representation of signals, Gibbs phenomenon, Discrete Time Fourier Series, properties, analogy between Continuous Time Fourier Series (CTFS) and Discrete Time Fourier Series (DTFS).	
	3.2	**Fourier Transform (FT):** Fourier Transform and Inverse Fourier Transform on periodic and non-periodic signals, limitations of CT/DT Fourier Transform and need for Laplace/Z Transform.	
	3.3	**Overview of Laplace Transform:** Need of Laplace Transform, review of unilateral and bilateral Laplace	

		Transform, properties, inverse of Laplace Transform, concept of Region of Convergence (ROC),poles and zeros, relation between continuous time Fourier Transform and Laplace Transform.	
4		**Z-Transform**	**08**
	4.1	Need of Z-Transform, definition of unilateral and bilateral Z-Transform, Z-Transform of finite and infinite duration sequences, properties, Inverse Z-Transform, relation between discrete time Fourier Transform and Z-Transform, Z-Transform of standard signals, ROC for ZT, plotting poles and zeros of transfer function.	
	4.2	Analysis of discrete time LTI systems using Z-Transform: Transfer Function, causality and stability of systems, frequency response (impulse and step), relation between Laplace Transform and Z–Transform.	
5		**State Space Analysis and Realization Structures**	**08**
	5.1	State Variable Analysis: Introduction to the notion of 'state', systematic procedure for determining state equations, solution of state equations using Laplace transform, definition of $exp(A)$ where A is a matrix, time domain solution of state equations.	
	5.2	Systems with finite duration and infinite duration, impulse response, recursive and non-recursive discrete time system, realization structures: direct form–I, direct form–II, Transpose, cascade, and parallel forms.	
6		**Applications of Signals and Systems**	**06**
	6.1	Signal Processing Applications: Speech and Audio Processing, Multimedia (image & video) processing, Underwater acoustic signal processing, Biological signal analysis	
	6.2	Communication and Control System Application: Modulation (Analog and Digital) process, Feedback/Feedforward Control system	

Textbooks :

1. NagoorKani, "*Signals and Systems"*, Tata McGraw Hill, Third Edition, 2011.
2. B.P. Lathi, "*Principles of Linear Systems and Signals"*, Oxford, Second Edition, 2010.
3. S. L. Nalbalwar, A. M. Kulkarni and S. P. Sheth, "*Signals and Systems"*, Synergy Knowledgeware, 2016.
4. Simon Haykin and Barry Van Veen, "*Signals and Sytems"*, John Wiley and Sons, Second Edition,2004.

Reference Books:

1. Hwei. P Hsu, "*Signals and Systems"*, Tata McGraw Hill, Third edition, 2010
2. V. Krishnaveni and A.Rajeshwari, "*Signals and Systems"*, Wiley-India, First Edition 2012.
3. NarayanaIyer, "*Signals and Systems"*, Cenage Learning, First Edition 2011.
4. Michael J Roberts, "*Fundamentals of Signals and systems*", Tata McGraw Hill, special Indian Economy edition, 2009.
5. Rodger E Ziemer, William H. Tranter and D. Ronald Fannin, "*Signals and Systems"*, Pearson Education, Fourth Edition 2009.
6. Alan V. Oppenhiem, Alan S. Willsky and S. Hamid Nawab, "*Signals and Systems*", Prentice-Hall of India, Second Edition, 2002.

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECC405	Principles of Communication Engineering	04	--	--	04	--	--	04

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical & Oral	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test2	Avg. Of Test 1 and Test 2					
ECC405	Principles of Communication Engineering	20	20	20	80	--	--	--	100

Course Pre-requisite:

- Applied Maths III
- Electronic Devices and Circuits I

Course Objectives:

1. To introduce students to various modulation and demodulation techniques of analog communication.
2. To analyze different parameters of analog communication techniques.
3. To study pulse modulation and demodulation.

Course Outcome:

After successful completion of the course student will be able to

1. Use different modulation and demodulation techniques used in analog communication
2. Identify and solve basic communication problems
3. Analyze transmitter and receiver circuits
4. Compare and contrast design issues, advantages, disadvantages and limitations of analog communication systems

Module No.	Unit No.	Detailed Content	Hours
1		**Basics of Communication System**	**06**
	1.1	Block diagram, electromagnetic spectrum, signal bandwidth and power, types of communication channels, Introduction to time and frequency domain.	
	1.2	Types of noise, signal to noise ratio, noise figure and noise temperature, Friss transmission formula.	
2		**Amplitude Modulation and Demodulation**	**12**
	2.1	Basic concepts, signal representation, need for modulation	
	2.2	Spectrum, waveforms, modulation index, bandwidth, voltage distribution and power calculations	
	2.3	DSBFC: Principles, modulating circuits, low level and high level transmitters DSB suppressed carrier :Multiplier modulator, nonlinear modulator and switching modulator	
	2.4	Amplitude demodulation: Diode detector, practical diode detector, square law detector	
	2.5	Comparison of different AM techniques, Applications of AM and use of VSB in broadcast television	
3		**Angle Modulation and Demodulation**	**12**
	3.1	Frequency modulation (FM): Basic concept, mathematical analysis, spectrum of FM wave, sensitivity, phase deviation and modulation index, deviation and percent modulated waves, bandwidth requirement of angle modulated waves, deviation ratio, narrowband FM and wideband FM	
	3.2	Varactor diode modulator, FET reactance modulator, stabilized AFC, Direct FM transmitter, indirect FM Transmitter, noise emphasis and de-emphasis	
	3.3	Phase modulation (PM): Principle and working of transistor direct PM modulator and relationship and comparison between FM and PM	
	3.4	FM demodulation: Balance slope detector, Foster-Seely discriminator, ratio detector, FM demodulator using Phase lock loop (PLL), amplitude limiting and thresholding, comparison between FM demodulators, comparison between AM, FM and PM	
	3.5	Applications of FM and PM	

4		**Radio Receivers**	**06**
	4.1	TRF, Super - heterodyne receiver, receiver parameters and choice of IF	
	4.2	AM receiver circuits and analysis, simple AGC, delayed AGC, forward AGC, and communication receiver	
	4.3	FM receiver circuits, comparison with AM receiver	
	4.4	Single and independent sideband (SSB and ISB) receivers	
5		**Analog Pulse Modulation & Demodulation**	**08**
	5.1	Sampling theorem for low pass signal, proof with spectrum, Nyquist criteria	
	5.2	Sampling techniques, aliasing error and aperture effect	
	5.3	PAM,PWM, PPM generation and detection	
	5.4	Applications of Pulse Communication	
6		**Multiplexing & De-multiplexing**	**04**
	6.1	Frequency Division Multiplexing transmitter & receiver block diagram	
	6.2	Time Division Multiplexing transmitter & receiver block diagram	
	6.3	Examples and applications of FDM and TDM	

Textbooks :

1. Kennedy and Davis, "*Electronics Communication System*", Tata McGraw Hill, Fourth edition.
2. B.P. Lathi, Zhi Ding "*Modern Digital and Analog Communication system*", Oxford University Press, Fourth edition.
3. Wayne Tomasi, "*Electronics Communication Systems*", Pearson education, Fifth edition.

Reference Books:

1. Taub, Schilling and Saha, "*Taub's Principles of Communication systems*", Tata McGraw Hill, Third edition.
2. P. Sing and S.D. Sapre, "*Communication Systems: Analog and Digital*", Tata McGraw Hill, Third edition.
3. Simon Haykin, Michel Moher, "*Introduction to Analog and Digital Communication*", Wiley, Second edition.

4. Dennis Roddy and John Coolen, "*Electronic Communication*", Prentice Hall, Third Edition.
5. Louis Frenzel, "*Communication Electronics*", Tata McGraw Hill, Third Edition.
6. Roy Blake, "*Electronic Communication Systems*", Delmar Publication, Second edition

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	TW/Pracs	Tutorial	Total
ECL401	Electronic Devices & Circuits-II Laboratory	--	02	--	--	1	--	1

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical & Oral	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test2	Avg. Of Test 1 and Test 2					
ECL401	Electronic Devices & Circuits-II Laboratory	--	--	--	--	25	25	--	50

Laboratory plan

Minimum 8 practicals including **minimum 2 simulations** should be conducted.

Suggested list of experiments

1. Design and Analyze two stage BJT amplifier (Frequency response and performance parameters)
2. Design and Analyze two stage FET amplifier (Frequency response and performance parameters)
3. Design Multistage BJT amplifier and finding its parameters, Verify.
4. Design and Analyze Voltage series feedback amplifier using BJT/FET and verify its effect on frequency response. x
5. Design and Analyze Current series feedback using BJT/FET and verify its effect on frequency response.
6. Design Multistage JFET amplifier and finding its parameters, verify.
7. Design and Analyze RC Phase shift oscillator for different amplitude and frequency.
8. Design and Analyze Colpitt / Hartley oscillator for different amplitude and frequency.
9. Class C power amplifier and its efficiency

Minimum One project based on:

1. Simple Emergency light.
2. DC servo amplifier using MOSFET.
3. Audio tone control circuit.
4. Public address system.
5. Automatic Door Bell

6. Clapp Switch
7. Topic related to syllabus

Note :Small project should be considered as a part of term-work.

Term Work:

At least 08 Experiments including 02 simulations covering entire syllabus must be given during the "**Laboratory session batch wise"**. Computation/simulation based experiments are also encouraged. The experiments should be students centric and attempt should be made to make experiments more meaningful, interesting and innovative. Application oriented one mini-project can be conducted for maximum batch of four students.

Term work assessment must be based on the overall performance of the student with every experiments/tutorials and mini-projects are graded from time to time. The grades will be converted to marks as per "**Choice Based Credit andGrading System"** manual and should be added and averaged. Based on above scheme gradingand term work assessment should be done.

The practical and oral examination will be based on entire syllabus.

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	TW/Pracs	Tutorial	Total
ECL402	Linear Integrated Circuits Laboratory	--	02	--	--	1	--	1

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical & Oral	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test2	Avg. Of Test 1 and Test 2					
ECL402	Linear Integrated Circuits Laboratory	--	--	--	--	25	25	--	50

Laboratory plan

Minimum 8 practicals including **minimum 2 simulations** should be conducted.

Suggested list of experiments

1. Discrete Differential Amplifier
2. Inverting, Non inverting, Buffer, Summing & Difference amplifiers
3. Differentiator & Integrator
4. Instrumentation amplifier
5. I to V and V to I converters
6. V to F and F to V convertors
7. Active Filters
8. Wien Bridge Oscillator
9. RC Phase shift Oscillator
10. Inverting & Non inverting Schmitt trigger
11. Square & Triangular wave generator
12. Precision rectifiers
13. Peak detector & Sample & Hold Circuits
14. Analog to Digital converter

15. Digital to Analog converter
16. Multivibrators using IC 555
17. PPM, PWM and Schmitt trigger using 555
18. Frequency modulatorusing VCO IC 566.
19. FSK DemodulatorusingPLL IC 565.
20. Phase detectorusing multiplier IC 534.
21. Sinusoidal FSK generator using XR 2206
22. Voltage Regulators using 78XX/79XX, 317/337, 723

Minimum One project based on:

1. Variable Power Supply
2. Data Acquisition System
3. Function Generator
4. Topic related to syllabus

Note :Small project should be considered as a part of term-work.

Term Work:

At least 08 Experiments including 02 simulations covering entire syllabus must be given during the "**Laboratory session batch wise"**. Computation/simulation based experiments are also encouraged. The experiments should be students centric and attempt should be made to make experiments more meaningful, interesting and innovative. Application oriented one mini-project can be conducted for maximum batch of four students.

Term work assessment must be based on the overall performance of the student with every experiments/tutorials and mini-projects are graded from time to time. The grades will be converted to marks as per "**Choice Based Credit andGrading System"** manual and should be added and averaged. Based on above scheme gradingand term work assessment should be done.

The practical and oral examination will be based on entire syllabus.

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	TW/Pracs	Tutorial	Total
ECL403	Signals and Systems Laboratory	--	02	--	--	1	--	1

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical & Oral	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test2	Avg. Of Test 1 and Test 2					
ECL403	Signals and Systems Laboratory	--	--	--	--	25	25	--	50

Laboratory plan

Minimum 8 practicals including **minimum 2 simulations** should be conducted.

Suggested list of experiments

1. Generation of signals, perform different operation on signals and plot them.
2. Generation of Gibbs phenomenon and observe the behavior of the signal.
3. Simulation of continuous time LTI system using convolution or Simulation of discrete time LTI systems using convolution.
4. Implementation of energy spectral and power spectral density.
5. Perform correlation, auto-correlation operations on different signals.
6. Obtaining impulse response of the systems.
7. Computing FT and DTFT of the CT signals and DT sequences.
8. Observing the effects of lower sampling rate and higher sampling rate on CT signal.
9. Modeling/Simulating Realization Structure of Direct form-I and II.
10. Modeling/Simulating State Space Realization.

Minimum One project based on:

1. Biometric Identification
2. Classification of Audio signal
3. Image Enhancement and Denoising
4. Designing of Feed-forward and Feedback Systems
5. Relevant topics in scope of subject.

Note :Small project should be considered as a part of term-work.

Term Work:

At least 08 Experiments including 02 simulations covering entire syllabus must be given during the "**Laboratory session batch wise"**. Computation/simulation based experiments are also encouraged. The experiments should be students centric and attempt should be made to make experiments more meaningful, interesting and innovative. Application oriented one mini-project can be conducted for maximum batch of four students.

Term work assessment must be based on the overall performance of the student with every experiments/tutorials and mini-projects are graded from time to time. The grades will be converted to marks as per "**Choice Based Credit and Grading System"** manual and should be added and averaged. Based on above scheme grading and term work assessment should be done.

The practical and oral examination will be based on entire syllabus.

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	TW/Pracs	Tutorial	Total
ECL404	Principles of Communication Engineering Laboratory	--	02	--	--	1	--	1

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical & Oral	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test2	Avg. Of Test 1 and Test 2					
ECL404	Principles of Communication Engineering Laboratory	--	--	--	--	25	25	--	50

Laboratory plan

Minimum 8 practicals including **minimum 2 simulations** should be conducted.

Suggested list of experiments

1. Generation and detection of AM (DSB-FC, DSB-SC,SSB) signal.
2. Generation and detection of FM signal.
3. Study of AM broadcast receiver (Super heterodyne).
4. Generation of PAM signal and verify the sampling theorem.
5. Generation of PPM, PWM signal.
6. Study of TDM and FDM multiplexing techniques.

Suggested list of Minimum projects

1. AM transmitter /receiver.
2. FM transmitter /receiver.
3. PAM,PPM,PWM circuits with IC 555
4. FM remote encoder/decoder circuits,
5. Transistor Intercom circuit
6. Walkie -Talkie Circuit

7. Arduino based communication circuits
8. Electronic voting machine.
9. Electronic Notice Board Using Android.
10. Home security system.

Note :Small project should be considered as a part of term-work.

Term Work:

At least 08 Experiments including 02 simulations covering entire syllabus must be given during the "**Laboratory session batch wise"**. Computation/simulation based experiments are also encouraged. The experiments should be students centric and attempt should be made to make experiments more meaningful, interesting and innovative. Application oriented one mini-project can be conducted for maximum batch of four students.

Term work assessment must be based on the overall performance of the student with every experiments/tutorials and mini-projects are graded from time to time. The grades will be converted to marks as per "**Choice Based Credit and Grading System"** manual and should be added and averaged. Based on above scheme grading and term work assessment should be done.

The practical and oral examination will be based on entire syllabus.

Semester V

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned		
		Theory	Pracs	Tut	Theory	TW/ Pracs	Total
ECC501	Microprocessor & Peripherals Interfacing	4	-	-	4	-	4
ECC502	Digital Communication	4	-	-	4	-	4
ECC503	Electromagnetic Engineering	4	-	1@	4	1	5
ECC504	Discrete Time Signal Processing	4	-	-	4	-	4
ECCDLO 501X	Department Level Optional Course I	4	-	-	4	-	4
ECL501	Microprocessor & Peripherals Interfacing Lab	-	2	-	-	1	1
ECL502	Digital Communication Lab	-	2	-	-	1	1
ECL503	Business Communication & Ethics Lab	-	2+2*	-	-	2	2
ECL504	Open Source Technology for Communication Lab	-	2	-	-	1	1
ECLDLO 501X	Department Level Optional Lab I	-	-	2#	-	1	1
Total		**20**	**10**	**3**	**20**	**7**	**27**

@ 1 hour to be taken as tutorial classwise #2 hours to be taken as either lab or tutorial based on subject requirement
*2 hours to be taken as tutorial batchwise

Course Code	Course Name	Examination Scheme							
		Theory					TW	Oral/ Prac	Total
		Internal Assessment			End Sem Exam	Exam Duration (Hrs)			
		Test1	Test 2	Avg					
ECC501	Microprocessor & Peripherals Interfacing	20	20	20	80	03	--	--	100
ECC502	Digital Communication	20	20	20	80	03	--	--	100
ECC503	Electromagnetic Engineering	20	20	20	80	03	25	--	125
ECC504	Discrete Time Signal Processing	20	20	20	80	03	--	--	100
ECCDLO 501X	Department Level Optional Course I	20	20	20	80	03	--	--	100
ECL501	Microprocessor & Peripherals Interfacing Lab	--	--	--	--	--	25	25	50
ECL502	Digital Communication Lab	--	--	--	--	--	25	25	50
ECL503	Business Communication & Ethics Lab	--	--	--	--	--	50	--	50
ECL504	Open Source Technology for Communication Lab	--	--	--	--	--	25	25	50
ECLDLO 501X	Department Level Optional Lab I	--	--	--	--	--	25	--	25
Total				**100**	**400**		**175**	**75**	**750**

Course Code	Department Level Optional Course I
ECCDLO 5011	Microelectronics
ECCDLO 5012	TV & Video Engineering
ECCDLO 5013	Finite Automata Theory
ECCDLO 5014	Data Compression and Encryption

Semester VI

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned		
		Theory	Pracs	Tut	Theory	TW/ Pracs	Total
ECC601	Microcontrollers & Applications	4	-	--	4	--	4
ECC602	Computer Communication Networks	4	-	-	4	-	4
ECC603	Antenna & Radio Wave Propagation	4	-	-	4	-	4
ECC604	Image Processing and Machine Vision	4	-	--	4	--	4
ECCDLO 602X	Department Level Optional Course II	4	-	-	4	-	4
ECL601	Microcontroller & Applications Lab	-	2	-	-	1	1
ECL602	Computer Communication Network Lab	-	2	-	-	1	1
ECL603	Antenna & Radio Wave Propagation Lab	-	2	-	-	1	1
ECL604	Image Processing and Machine Vision Lab	-	2	-	-	1	1
ECLDLO 602X	Department Level Optional Lab II	-	2	-	-	1	1
Total		**20**	**10**	**-**	**20**	**5**	**25**

Course Code	Course Name	Examination Scheme							
		Theory					TW	Oral & Prac	Total
		Internal Assessment			End Sem Exam	Exam Duration (Hrs)			
		Test1	Test 2	Avg					
ECC601	Microcontroller& Applications	20	20	20	80	03	--	--	100
ECC602	Computer Communication Network	20	20	20	80	03	--	--	100
ECC603	Antenna & Radio Wave Propagation	20	20	20	80	03	--	--	100
ECC604	Image Processing and Machine Vision Lab	20	20	20	80	03	--	--	100
ECCDLO 602X	Department Level Optional Course II	20	20	20	80	03	--	--	100
ECL601	Microcontroller & Applications Lab	--	--	--	--	--	25	25	50
ECL602	Computer Communication Network Lab	--	--	--	--	--	25	25	50
ECL603	Antenna & Radio Wave Propagation Lab	--	--	--	--	--	25	25	50
ECL604	Image Processing and Machine Vision Lab	--	--	--	--	--	25	25	50
ECLDLO 602X	Department Level Optional Lab II	--	--	--	--	--	25	--	25
Total				**100**	**400**		**125**	**100**	**725**

Course Code	Department Level Optional Course II
ECCDLO 6021	Digital VLSI Design
ECCDLO 6022	Radar Engineering
ECCDLO 6023	Database Management System
ECCDLO 6024	Audio Processing

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECC501	Microprocessors & Peripherals	04	--	--	04	--	--	04

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical & Oral	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test2	Avg. Of Test 1 and Test 2					
ECC501	Microprocessors & Peripherals	20	20	20	80	--	--	--	100

Course prerequisite:

- Digital System Design

Course objectives:

- To understand the basic concepts of microcomputer systems.
- To develop background knowledge and core expertise in 8086 microprocessor and co-processor 8087.
- To write assembly language programs for 8086 microprocessor
- To understand peripheral devices and their interfacing to 8086 and to study the design aspects of basic microprocessor based system.

Course outcomes:

After successful completion of the course student will be able to

- Understand the basic concepts of microcomputer systems.
- Understand the architecture and software aspects of microprocessor 8086.
- Write Assembly language program in 8086.
- Know the Co-processor configurations.
- Interface peripherals for 8086.
- Design elementary aspect of microprocessor based system.

Module No.	Unit No.	Topics	Hrs.
1.0		**Introduction to Microcomputer System**	**06**
	1.1	Block diagram of microprocessor based system: CPU, I/O Devices, Clock, Memory, Concept of Address, Data and Control Bus and Tristate logic.	
	1.2	Need of Assembly Language and its Comparison with higher level languages	
	1.3	Need of Assembler and Compiler and their comparison.	
2.0		**Architecture of 8086 Microprocessor**	**06**
	2.2	8086 Architecture and organization, pin configuration.	
	2.3	Minimum and Maximum modes of 8086.	
	2.4	Read and Write bus cycle of 8086.	
3.0		**Instruction set and programming of 8086**	**10**
	3.1	8086 Addressing modes.	
	3.2	8086 Instruction encoding formats and instruction set.	
	3.3	Assembler directives.	
	3.4	8086 programming and debugging of assembly language program. Programs related to: arithmetic, logical, delay, string manipulation, stack and subroutines, input, output, timer/counters.	
	3.5	Elementary DOS Programming: Introduction to int-21h services.	
4.0		**Peripherals interfacing with 8086 and applications.**	**10**
	4.1	8086-Interrupt structure.	
	4.2	Programmable peripheral Interface 8255.	
	4.3	Programmable interval Timer 8254.	
	4.4	Elementary features of 8259A and 8257 and interface.	
	4.5	Interfacing 8255, 8254 with 8086 and their applications	
5.0		**ADC, DAC interfacing with 8086 and its application**	**08**
	5.1	Analog to Digital Converter (ADC) 0809	
	5.2	Digital to Analog Converter (DAC) 0808	
	5.3	Interfacing ADC 0809, DAC 0808 with 8086 and their applications.	
	5.4	8086 based data Acquisition system.	
6.0		**8086 Microprocessor interfacing**	**08**
	6.1	8087 Math co-processor, its data types and interfacing with 8086.	
	6.2	Memory interfacing with 8086 microprocessor	
		Total	**48**

Text Books:

1. John Uffenbeck: "8086/8088 family: "Design, Programming and Interfacing", Prentice Hall, 2nd Edition
2. B. B. Brey: "The Intel Microprocessors 8086/8088, 80186/80188, 80286, 80386, 80486, Pentium and Pentium Pro Processor", Pearson Pub, 8th Edition
3. Hall D.V: "Microprocessor and Interfacing Programming and Hardware", Tata McGraw Hill, 2nd Edition.
4. Yu-Cheng Liu/Glenn A. Gibson: "Microcomputer Systems: The 8086/8088 Family Architecture, Programming and Design", Phi Learning.

Reference Books:

1. Peter Abel: "IBM PC ASSEMBLY LANGUAGE & PROGRAMMING", Phi Learning.
2. A. K. Ray and K. M. Burchandi: "Advanced Microprocessor and Peripherals, Architecture Programming and Interfacing", Tata McGrawHill, 3rd Edition
3. Don Anderson, Tom Shanley: "Pentium Processor System Architecture", MindShare Inc., 2nd Edition
4. National Semiconductor: Data Acquisition Linear Devices Data Book
5. Intel Peripheral Devices: Data Book.
6. The Intel 8086 family user manual.

Internal Assessment:
Assessment consists of two class tests of 20 marks each. The first class test is to be conducted when approximately 40% syllabus is completed and second class test when additional 40% syllabus is completed. The average marks of both the test will be considered for final Internal Assessment. Duration of each test shall be of one hour.

End Semester Examination:
1. Question paper will comprise of 6 questions, each carrying 20 marks.
2. The students need to solve total 4 questions.
3. Question No.1 will be compulsory and based on entire syllabus.
4. Remaining question (Q.2 to Q.6) will be selected from all the modules.

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECC502	Digital Communication	04	--	--	04	--	--	04

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical & Oral	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test2	Avg. Of Test 1 and Test 2					
ECC502	Digital Communication	20	20	20	80	--	--	--	100

Prerequisites:

- Analog Communication

Course objectives:

- To identify the signals and functions of its different components,
- To learn about theoretical aspects of digital communication system and Draw signal space diagrams, compute spectra of modulated signals,
- To learn about error detection and correction to produce optimum receiver.

Course outcomes:

After successful completion of the course student will be able to

- Understand random variables and random processes of signal,
- Apply the concepts of Information Theory in source coding,
- Evaluate different methods to eliminate Inter-symbol interference,
- Compare different band-pass modulation techniques,
- Evaluate performance of different error control codes.

Module No.	Unit No.	Top	Hrs.
1.0		**Probability Theory & Random Variables and**	**08**
	1.1	Information, Probability, Conditional Probability of independent events, Relation between probability and probability Density , Raleigh Probability Density , CDF, PDF.	
	1.2	Random Variables, Variance of a Random Variable, correlation between Random Variables, Statistical Averages(Means),Mean and Variance of sum of Random variables, Linear mean square Estimation, Central limit theorem, Error function and Complementary error function Discrete and Continuous Variable, Gaussian PDF, Threshold Detection, Statistical Average, Chebyshev In-Equality, Auto-correction.	
	1.3	Random Processes	
2.0		**Information Theory and Source Coding**	**06**
	2.1	Block diagram and sub-system description of a digital communication system, measure of information and properties, entropy and it's properties	
	2.2	Mini Source Coding, Shannon's Source Coding Theorem, Shannon-Fano Source Coding, Huffman Source Coding	
	2.3	Differential Entropy, joint and conditional entropy, mutual information and channel capacity, channel coding theorem, channel capacity theorem	
3.0		**Error Control Systems**	**12**
	3.1	Types of error control, error control codes, linear block codes, systematic linear block codes, generator matrix, parity check matrix, syndrome testing ,error correction, and decoder implementation	
	3.2	**Systematic and Non-systematic Cyclic codes**: encoding with shift register and error detection and correction	
	3.3	**Convolution Codes**: Time domain and transform domain approach, graphical representation, code tree, trellis, state diagram, decoding methods.	
4.0		**Bandpass Modulation & Demodulation**	**10**
	4.1	Band-pass digital transmitter and receiver model, digital modulation schemes	
	4.2	Generation, detection, signal space diagram, spectrum, bandwidth efficiency, and probability of error analysis of: Amplitude Shift Keying (ASK), Frequency Shift Keying (FSK)Modulations, Binary Phase Shift Keying (BPSK) Modulation, Quaternary Phase Shift Keying QPSK), M- ary PSK Modulations, Quadrature Amplitude Modulation (QAM), Minimum Shift Keying (MSK)	

5.0		**Baseband Modulation & Transmission**	**04**
	5.1	Discrete PAM signals and it's power spectra	
	5.2	Inter-symbol interference, Nyquist criterion for zero ISI, sinusoidal roll-off filtering, correlative coding, equalizers, and eye pattern	
6.0		**Optimum Reception of Digital Signal**	**08**
	6.1	Baseband receiver	
	6.2	Probability of Error	
	6.3	Optimum Receiver and Filter	
	6.4	Matched Filter and its probability of error	
	6.5	Coherent Reception	
	Total		**48**

Text Books:

1. H. Taub, D. Schlling, and G. Saha, "Principles of Communication Systems," Tata Mc- Graw Hill, New Delhi, Third Edition, 2012.
2. Lathi B P, and Ding Z., "Modern Digital and Analog Communication Systems," Oxford University Press, Fourth Edition, 2009.
3. Haykin Simon, "Digital Communication Systems," John Wiley and Sons, New Delhi, Fourth Edition, 2014.

Reference Books:

1. Sklar B, and Ray P. K., "Digital Communication: Fundamentals and applications," Pearson, Dorling Kindersley (India), Delhi, Second Edition, 2009.
2. T L Singal, "Analog and Digital Communication," Tata Mc-Graw Hill, New Delhi, First Edition, 2012.
3. P Ramakrishna Rao, "Digital Communication," Tata Mc-Graw Hill, New Delhi, First Edition, 2011.
4. M F Mesiya, "Contempory Communication systems", Mc-Graw Hill, Singapore, First Edition, 2013.

Internal Assessment:
Assessment consists of two class tests of 20 marks each. The first class test is to be conducted when approximately 40% syllabus is completed and second class test when additional 40% syllabus is completed. The average marks of both the test will be considered for final Internal Assessment. Duration of each test shall be of one hour.

End Semester Examination:
1. Question paper will comprise of 6 questions, each carrying 20 marks.
2. The students need to solve total 4 questions.
3. Question No.1 will be compulsory and based on entire syllabus.
4. Remaining question (Q.2 to Q.6) will be selected from all the modules.

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECC503	Electromagnetic Engineering	04	--	@1	04	--	01	05

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical & Oral	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test2	Avg. Of Test 1 and Test 2					
ECC503	Electromagnetic Engineering	20	20	20	80	25	--	--	125

@ 1 hour to be taken as tutorial class wise

Course prerequisite:

- Vector Algebra and vector Calculus
- Various Co-ordinate system
- Two port network

Course objectives:

- To learn electromagnetics, including static and dynamic electromagnetic fields and waves within and at the boundaries of media.
- To learn mathematical skills, including Vectors and phasors and Partial differential equations.
- To learn Electromagnetic radiation and propagation in space and within transmission lines

Course outcomes:

After successful completion of the course student will be able to explain and evaluate EM fields and key physical parameters for:

- Fields and energies in simple planar, cylindrical, and spherical geometries, Fields within conducting and anisotropic media
- Electric and magnetic forces on charges, wires, and media Sinusoids and transients on TEM lines with mismatched impedances and tuning

Module No.	Unit No.	Topics	Hrs.
1.0		**Electrostatics**	**07**
	1.1	Coulomb's Law & Electric Field Intensity, Electric Field due to point charge, line charge and surface charge distributions	
	1.2	Electric Flux Density, Gauss's Law and its Application to differential volume element, divergence, divergence theorem.	
	1.3	Electric potential, Relationship between Electric field & potential , Potential Gradient., electric dipole	
2.0		**Electric Fields in Material Space**	**06**
	2.1	Energy density in electrostatic field, Current and current Density, continuity equation, Polarization in dielectrics	
	2.2	Capacitance, capacitance of parallel plate; spherical; cylindrical capacitors with multiple di-electrics, Boundary conditions	
	2.3	Poisson's and Laplace's equation, General procedures for solving Poisson's and Laplace's equations.	
3.0		**Steady Magnetic Field**	**07**
	3.1	Biot-Savart's Law, Ampere's Circuital Law and its Applications, magnetic flux density, Magnetic Scalar and vectors potentials, Derivations of Biot-Savart's law and Ampere's law based on Magnetic Potential	
	3.2	Forces due to magnetic field, magnetic dipole, Classification of Magnetic Materials, Magnetic boundary conditions.	
4.0		**Maxwell's Equation and Electromagnetic Wave Propagation**	**12**
	4.1	Faraday's law, Displacement current, Maxwell's equations in point form and integral form, Boundary conditions for time varying field , magnetic vector potential, Time harmonic field, Introduction to the concept of Uniform Plane Wave and Helmholtz equation.	
	4.2	Wave Propagation in Free Space, Lossy and Lossless Dielectrics and in Good Conductors. Reflection of Plane Wave, Poynting Vector, Wave Power, Skin Effect, Wave Polarization and Standing Wave Ratio	
5.0		**Transmission Lines**	**10**
	5.1	Transmission line parameters, Transmission line equations, Input impedance, Standing wave ratio, Power, Transients on transmission lines.	
	5.2	Smith Chart, Applications of Smith Chart in finding VSWR ,and reflection coefficient, admittance calculations, impedance calculations over length of line.	

6.0		**Applications of Electromagnetics**	**06**
	6.1	Electrostatic discharge, Materials with high dielectric constant, Graphene, Inkjet printer, RF mems, Multidielectric systems, magnetic levitation, Memristor, Optical nanocircuits, Metamaterials, Microstrip lines and characterization of Data cables, RFID	
		Total	**48**

Text Books:

1. Engineering Electromagnetics, William H Hayt and John A Buck - Tata McGraw-Hill Publishing Company Limited, Seventh Edition
2. Principles of Electromagnetics, Matthew N. O.Sadiku ,S.V.Kulkarni- Oxford university press, Sixth edition

Reference Books:

1. Electromagnetics with applications by J.D.Krauss and Daniel Fleisch fifth edition
2. Electromagnetic Field Theory Fundamentals, Bhag Singh Guru, Hüseyin R. Hiziroglu Cambridge University Press, Second Edition.
3. Electromagnetics, Joseph Edminister, , Mahmood Nahvi, Schaum Outline Series, Fourth edition.
4. R. K. Shevgaonkar, "Electromagnetic Waves" Tata McGraw Hil

Internal Assessment:
Assessment consists of two class tests of 20 marks each. The first class test is to be conducted when approximately 40% syllabus is completed and second class test when additional 40% syllabus is completed. The average marks of both the test will be considered for final Internal Assessment. Duration of each test shall be of one hour.

End Semester Examination:
1. Question paper will comprise of 6 questions, each carrying 20 marks.
2. The students need to solve total 4 questions.
3. Question No.1 will be compulsory and based on entire syllabus.
4. Remaining question (Q.2 to Q.6) will be selected from all the modules.

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECC504	Discrete Time Signal Processing	04	--	--	04	--	--	04

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical & Oral	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test2	Avg. Of Test 1 and Test 2					
ECC504	Discrete Time Signal Processing	20	20	20	80	--	--	--	100

Course prerequisite:

- Signals & Systems

Course objectives:

- To develop a thorough understanding of DFT and FFT and their applications.
- To teach the design techniques and performance analysis of digital filters
- To introduce the students to digital signal processors and its applications.

Course outcomes:

After successful completion of the course student will be able to

- Understand the concepts of discrete-time Fourier transform and fast Fourier transform.
- Apply the knowledge of design of IIR digital filters to meet arbitrary specifications.
- Apply the knowledge of design of FIR digital filters to meet arbitrary specifications.
- Analyze the effect of hardware limitations on performance of digital filters.
- Apply the knowledge of DSP processors for various applications.

Module No.	Unit No.	Topics	Hrs.
1.0		**Discrete Fourier Transform & Fast Fourier Transform**	**10**
	1.1	Definition and Properties of DFT, IDFT, Circular convolution of sequences using DFT and IDFT. Filtering of long data sequences: Overlap-Save and Overlap-Add Method for computation of DFT	
	1.2	Fast Fourier Transforms (FFT), Radix-2 decimation in time and decimation in frequency FFT algorithms, inverse FFT, and introduction to composite FFT.	
2.0		**IIR Digital Filters**	**10**
	2.1	Types of IIR Filters (Low Pass, High Pass, Band Pass, Band Stop and All Pass), Analog filter approximations: Butterworth, Chebyshev I, Elliptic.	
	2.2	Mapping of S-plane to Z-plane, impulse invariance method, bilinear transformation method, Design of IIR digital filters (Butterworth and Chebyshev-I) from Analog filters with examples.	
	2.3	Analog and digital frequency transformations with design examples.	
3.0		**FIR Digital Filters**	**10**
	3.1	Characteristics of FIR digital filters, Minimum Phase, Maximum Phase, Mixed Phase and Linear Phase Filters. Frequency response, location of the zeros of linear phase FIR filters.	
	3.2	Design of FIR filters using Window techniques (Rectangular, Hamming, Hanning, Blackmann, Kaiser), Design of FIR filters using Frequency Sampling technique, Comparison of IIR and FIR filters.	
4.0		**Finite Word Length effects in Digital Filters**	**06**
	4.1	Quantization, truncation and rounding, Effects due to truncation and rounding, Input quantization error, Product quantization error, Co-efficient quantization error, Zero-input limit cycle oscillations, Overflow limit cycle oscillations, Scaling.	
	4.2	Quantization in Floating Point realization of IIR digital filters, Finite word length effects in FIR digital filters.	
5.0		**DSP Processors**	**06**
	5.1	Introduction to General Purpose and Special Purpose DSP processors, fixed point and floating point DSP processor, Computer architecture for signal processing, Harvard Architecture, Pipelining, multiplier and accumulator (MAC), Special Instructions, Replication, On-chip memory, Extended Parallelism.	

	5.2	General purpose digital signal processors, Selecting digital signal processors, Special purpose DSP hardware, Architecture of TMS320CX fixed and floating DSP processors.	
6.0		**Applications of Digital Signal Processing**	**06**
	6.1	Application of DSP for ECG signals analysis.	
	6.2	Application of DSP for Dual Tone Multi Frequency signal detection.	
	6.3	Application of DSP for Radar Signal Processing.	
		Total	**48**

Text Books:

1. Emmanuel C. Ifeachor, Barrie W. Jervis, "*Digital Signal Processing"*, A Practical Approach by, Pearson Education
2. Tarun Kumar Rawat, " *Digital Signal Processing"*, Oxford University Press, 2015

Reference Books:

1. Proakis J., Manolakis D., "*Digital Signal Processing*", 4th Edition, Pearson Education.
2. Sanjit K. Mitra , Digital Signal Processing – A Computer Based Approach – 4th Edition McGraw Hill Education (India) Private Limited.
3. Oppenheim A., Schafer R., Buck J., "*Discrete Time Signal Processing*", 2nd Edition, Pearson Education.
4. B. Venkata Ramani and M. Bhaskar, "*Digital Signal Processors, Architecture, Programming and Applications*", Tata McGraw Hill, 2004.
5. L. R. Rabiner and B. Gold, "*Theory and Applications of Digital Signal Processi*ng", Prentice-Hall of India, 2006.

Internal Assessment:
Assessment consists of two class tests of 20 marks each. The first class test is to be conducted when approximately 40% syllabus is completed and second class test when additional 40% syllabus is completed. The average marks of both the test will be considered for final Internal Assessment. Duration of each test shall be of one hour.

End Semester Examination:
1. Question paper will comprise of 6 questions, each carrying 20 marks.
2. The students need to solve total 4 questions.
3. Question No.1 will be compulsory and based on entire syllabus.
4. Remaining question (Q.2 to Q.6) will be selected from all the modules.

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECCDLO 5011	Microelectronics	04	--	--	04	--	--	04

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical & Oral	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test2	Avg. Of Test 1 and Test 2					
ECCDLO 5011	Microelectronics	20	20	20	80	--	--	--	100

Course prerequisite:

- Electronics Devices and Circuits- I
- Electronics Devices and Circuits- II

Course objectives:

- To understand integrated circuit biasing using MOSFET.
- To analyze single stage active load MOS amplifier.
- To analyze active load differential amplifier
- To understand implementation of passive components in ICs.

Course outcomes:

After successful completion of the course student will be able to

- Analyze various constant current source circuit using MOS
- Design and implement active load MOS amplifier.
- Design and implement active load differential amplifier

Module No.	Unit No.	Topics	Hrs.
1.0		**Basics of MOSFETs**	**08**
	1.1	Introduction to various fabrication process(in brief) Fabrication of NMOS and PMOS transistors along with mask layout diagram, Multi finger transistor, Scaling of MOSFET, Various Short channel effects in MOSFET, Second order effects in MOSFET, MOS as controlled resistor, MOS device capacitances	
2.0		**Integrated Circuit Biasing & Active Loads using MOSFET**	**08**
	2.1	Current Mirror, cascade current source, Wilson current source, bias independent current source using MOSFET,DC analysis and small signal analysis of MOS active load, DC analysis and small signal analysis of MOS advanced active load	
3.0		**Single Stage MOS Active Load amplifiers**	**08**
	3.1	CS amplifier with current source load, CS amplifier with diode connected load, CS amplifier with current source load, Common gate circuit, Cascode amplifier, Double Cascoding, Folded Cascode.	
4.0		**Active Load MOSFET Differential Amplifier**	**10**
	4.1	Basic MOS Differential Amplifier, DC transfer characteristics, small signal equivalent analysis, MOS differential amplifier with active load, MOS differential amplifier with cascode active load,	
5.0		**Passive Device Fabrication in IC**	**07**
	5.1	Fabrication of inductors, fabrication of transformers, fabrication of varactors, and fixed value capacitors.	
6.0		**Power Amplifiers**	**07**
	6.1	Class A, class B, Class C, Class D, Class E, Class F using MOSFET	
		Total	**48**

Text Books:

1. A. Sedra, K. Smith, adapted by A. Chanorkar "Microelectronic Circuits-Theory and Application *Advanced engineering mathematics*", Oxford Higher Education, 7th Edition
2. D. Neamen, "Electronic Circuits Analysis and Design", McGraw Hill Education, 3rd Edition
3. B. Razavi, "Design of Analog Integrated Circuits", McGraw Hill Education, Indian Edition

Reference Books:

1. B. Razavi,"R F Microelectronics", Pearson Publication, 2nd Edition

Internal Assessment:
Assessment consists of two class tests of 20 marks each. The first class test is to be conducted when approximately 40% syllabus is completed and second class test when additional 40% syllabus is completed. The average marks of both the test will be considered for final Internal Assessment. Duration of each test shall be of one hour.

End Semester Examination:
3. Question paper will comprise of 6 questions, each carrying 20 marks.
4. The students need to solve total 4 questions.
3. Question No.1 will be compulsory and based on entire syllabus.
4. Remaining question (Q.2 to Q.6) will be selected from all the modules.

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECCDLO 5012	TV & Video Engineering	04	--	--	04	--	--	04

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical & Oral	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test2	Avg. Of Test 1 and Test 2					
ECCDLO 5012	TV & Video Engineering	20	20	20	80	--	--	--	100

Course objectives:

- To understand basic concepts of TV system .
- To understand compression techniques
- To introduce to advanced systems and dvb standards

Course outcomes:

After successful completion of the course student will be able to

- Understand overview of TV system.
- Understand details of compression technique.
- Know about different dvb standards.
- Understand advanced digital systems

Module No.	Unit No.	Topics	Hrs.
1.0		**Fundamentals of TV system**	**10**
	1.1	Interlaced scanning, Composite video signal, VSB(Vestigial sideband transmission), Channel bandwidth, Study of transmitter and receiver block diagram of monochrome Television	
	1.2	Camera Tubes: Vidicon, Image Orthicon	
2.0		**Colour Television**	**10**
	2.1	Colour Fundamentals, Chromaticity diagram, Frequency interleaving, compatibility considerations	
	2.2	NTSC system characteristics, Encoder and Decoder block diagram, PAL system characteristics, Encoder and Decoder block diagram, Comparison of NTSC and PAL systems	
3.0		**Digital Video**	**08**
	3.1	Basics of digital video	
	3.2	Chroma subsampling:4:4:4,4:2:2,4:2:0,4:1:1 digital video formats	
	3.3	Video compression standards:MPEG2:DCT coding, codec structure. Introduction to H.264/MPEG-4 AVC, Introduction to H.265	
	3.4	Set-Top Box	
4.0		**Digital Video Broadcasting**	**06**
	4.1	Introduction to DVB-T,DVB-T2,DVB-H,DVB-S,DVB-C	
	4.2	Satellite Television	
5.0		**Advanced Digital TV Systems**	**10**
	5.1	MAC MACd2	
	5.2	HDTV,SUHDTV	
	5.3	Smart TV and its functions	
	5.4	Introduction to IPTV	
	5.5	Application of TV system as CCTV	
6.0		**Displays & Streaming Media Device**	**04**
	6.1	LCD,LED	
	6.2	Chromcast	
		Total	**48**

Text Books:

1. Monochrome and colour Television by R.R.Gulathi
2. Television and video engineering by A.M. Dhake

Reference Books:

1. Digital Television (Practical guide for Engineers) by Fischer

Websites:

1. https://www.dvb.org/resources/public/factsheets
2. https://en.wikipedia.org/wiki/Digital_Video_broadcasting

Internal Assessment:
Assessment consists of two class tests of 20 marks each. The first class test is to be conducted when approximately 40% syllabus is completed and second class test when additional 40% syllabus is completed. The average marks of both the test will be considered for final Internal Assessment. Duration of each test shall be of one hour.

End Semester Examination:
1. Question paper will comprise of 6 questions, each carrying 20 marks.
2. The students need to solve total 4 questions.
3. Question No.1 will be compulsory and based on entire syllabus.
4. Remaining question (Q.2 to Q.6) will be selected from all the modules.

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECCDLO 5013	Finite Automata Theory	04	--	--	04	--	--	04

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical & Oral	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test2	Avg. Of Test 1 and Test 2					
ECCDLO 5013	Finite Automata Theory	20	20	20	80	--	--	--	100

Course prerequisite:

- Digital System Design

Course objectives:

This course provides in-depth knowledge of switching theory and the design techniques of digital circuits, which is the basis for design of any digital circuit. The main objectives are:

- To understand learn basic techniques for the design of digital circuits and fundamental concepts used in the design of digital systems.
- To design combinational logic circuits and its optimization and fault detection.
- To study Mealy and Moore synchronous and asynchronous sequential circuits design and their applications.

Course outcomes:

After successful completion of the course student will be able to

- Manipulate simple Boolean expressions using the theorems and postulates of Boolean algebra and to minimize combinational functions.
- Design and analyze small combinational circuits and to use standard combinational functions/ building blocks to build larger more complex circuits.
- Design and analyze small sequential circuits and devices and to use standard sequential functions/ building blocks to build larger more complex circuits.
- Design finite state machine understand the fundamentals and areas of applications for the integrated circuits.
- Perform symmetric and cascade threshold function and element

Module No.	Unit No.	Topics	Hrs.
1.0		**Combinational Logic**	**09**
	1.1	Notations of sets, Relations and Lattices, Venn diagram	
	1.2	Switching Algebra and functions, Boolean algebras and functions, Minimization of Boolean functions using map method and Tabulation Method, Prime implicant chart, Reduction of the chart, Branching method	
	1.3	Design of combinational Logic circuits, Contact networks, Functional decomposition and symmetric functions. Identification of symmetric functions	
2.0		**Threshold Logic & Synthesis of Threshold Networks**	**06**
	2.1	Threshold Logic, Threshold elements, Capabilities and limitations of threshold logic, elementary properties, Linear separability, Unate functions, Synthesis of threshold functions, Cascading of threshold elements.	
3.0		**Testing of Combinational Circuits**	**09**
	3.1	Reliable Design and fault Diagnosis, Fault Detection in combinational circuits, Fault location experiments, Fault Detection by Boolean Differences and path sensitization, Synthesis for testability, Multiple fault detection using map method, failure-Tolerant Design.	
4.0		**Sequential Circuits**	**12**
	4.1	Synchronous sequential circuits and iterative networks: Memory elements and their excitation functions; Synthesis of synchronous sequential circuits, Capabilities and limitations, State equivalence and Minimization, Minimization of completely specified and Incompletely specified sequential machines, Partition technique, Merger methods	
	4.2	Asynchronous sequential circuits: Hazards, Synthesis, State assignment and minimization	
	4.3	Finite state Machines – Mealy and Moore synchronous and asynchronous sequential circuits Design,	
5.0		**Structure and testing of Sequential Circuits**	**08**
	5.1	Structure of sequential Machines, Lattice of closed partitions, State Assignment using partitions, Reduction of output dependency, Input Independence and Autonomous clock.	
	5.2	Homing sequence, synchronizing sequence, Distinguishing sequence, Checking experiments, Machine identification, Recent Trends/Developments	

6.0		**Algorithmic State Machine**	**04**
	6.1	Introduction and components of ASM charts, Representation of sequential circuits using ASM charts, Example using ASM chart: 2 bit counter, binary multiplier, Weighing machine etc.	
		Total	**48**

Text Books:

1. Zvi Kohavi and Niraj K. Jha. "*Switching and Finite Automata Theory*", 3 Editions, Cambridge University Press.
2. Zvi Kohavi, *"Switching Theory and Finite Automata",* 2nd edition, Tata McGraw Hill
3. R. P. Jain, "*Switching Theory and Logic Design*", Tata McGraw Hill Education, 2003.
4. Lee Samuel C.," *Modern Switching Theory and Digital Design*", Prentice Hall PTR

Reference Books:

1. Morris Mano, "*Digital Logic and Computer Design*", Pearson Education
2. Samuel Lee, "*Digital Circuits and Logic design*", Prentice Hall.
3. William I. Fletcher, "*An Engineering Approach to Digital Design*", Prentice Hall.
4. John F. Wakerly, "*Digital Design ‐ Principles and Practices*", Pearson Education
5. A. Anand Kumar, *"Switching Theory and Logic Design",* PHI Learning private limited, 2014

Internal Assessment:
Assessment consists of two class tests of 20 marks each. The first class test is to be conducted when approximately 40% syllabus is completed and second class test when additional 40% syllabus is completed. The average marks of both the test will be considered for final Internal Assessment. Duration of each test shall be of one hour.

End Semester Examination:
1. Question paper will comprise of 6 questions, each carrying 20 marks.
2. The students need to solve total 4 questions.
3. Question No.1 will be compulsory and based on entire syllabus.
4. Remaining question (Q.2 to Q.6) will be selected from all the modules.

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECCDLO 5014	Data Compression & Encryption	04	--	--	04	--	--	04

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical & Oral	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test2	Avg. Of Test 1 and Test 2					
ECCDLO 5014	Data Compression & Encryption	20	20	20	80	--	--	--	100

Course objectives:
To teach the students

- Lossless and Lossy compression techniques for different types of data.
- Data Encryption Techniques.
- Network and Web Security.

Course outcomes:
After successful completion of the course student will be able to

- Implement text, audio and video compression techniques.
- Understand Symmetric and Asymmetric Key Cryptography schemes.
- Understand network security.

Module No.	Unit No.	Topics	Hrs.
1.0		**Introduction to Data Compression**	**12**
	1.1	Data Compression : Modelling and Coding, Statstical Modelling, Dictionary Schemes, LZ, Lossy Compression	
	1.2	Shannon – Fano Algorithm, Huffman Algorithm, Adaptive Huffman Coding	
	1.3	Difficulties in Huffman Coding, Arithmetic Coding – Decoding, Dictionary Based Compression, Sliding Window Compression: LZ-77, LZ-78, LZW	
2.0		**Image Compression**	**06**
	2.1	DCT, JPEG, JPEG – LS, Differential Lossless Compression, DPCM, JPEG – 2000 Standards	
3.0		**Video and Audio Compression**	**08**
	3.1	Analog Video, Digital Video, MPEG – 2, H – 261 Encoder and Decoder	
	3.2	Sound, Digital Audio, μ-Law and A-Law Companding, MPEG – 1 Audio Layer (MP3 Audio Format)	
4.0		**Data Security**	**06**
	4.1	Security Goals, Cryptographic Attacks, Techniques	
	4.2	Symmetric Key: Substitution Cipher, Transposition Cipher , Stream and Block Cipher	
	4.3	DES, AES	
5.0		**Number Theory and Asymmetric Key Cryptography**	**08**
	5.1	Prime Numbers, Fermat's and Euler's Theorem, Chinese Remainder Theorem, Discreet Logarithms	
	5.2	Principles of Public Key Crypto System, RSA	
	5.3	Key Management, Deffie-Hellman Key Exchange	
	5.4	Message Integrity, Message Authentication and Hash Functions, SHA, H MAC, Digital Signature Standards	
6.0		**Network Security**	**08**
	6.1	Email, PGP, S/MIME, Intrusion Detection System	
	6.2	Web Security Considerations, SSL Architecture, SSL Message Formats, TLS, Secure Electronic Transactions	
	6.3	Kerberos, X.509 Authentication Service, Public Key Infrastructure	
		Total	**48**

Text Books:

1. Mark Nelson, Jean-Loup Gailly,"The Data Compression Book", 2nd edition, BPB Publications
2. Khalid Sayood, "Introduction to Data Compression", 2nd Edition Morgan Kaufmann.
3. William Stallings, "Cryptography and Network Security Principles and Practices 5th Edition", Pearson Education.
4. Behrouz A. Forouzan, "Cryptography and Network Security", Tata McGraw-Hill.

Reference Books:

1. David Salomon, "Data Compression: The Complete Reference", Springer.
2. Matt Bishop, "Computer Security Art and Science", Addison-Wesley.

Internal Assessment:
Assessment consists of two class tests of 20 marks each. The first class test is to be conducted when approximately 40% syllabus is completed and second class test when additional 40% syllabus is completed. The average marks of both the test will be considered for final Internal Assessment. Duration of each test shall be of one hour.

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2. The students need to solve total 4 questions.
3. Question No.1 will be compulsory and based on entire syllabus.
4. Remaining question (Q.2 to Q.6) will be selected from all the modules.

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	TW/Pracs	Tutorial	Total
ECL501	Microprocessors & Peripherals Interfacing Laboratory	--	02	--	--	1	--	1

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical & Oral	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test2	Avg. Of Test 1 and Test 2					
ECL501	Microprocessors & Peripherals Interfacing Laboratory	--	--	--	--	25	25	--	50

Suggested Experiment List

Experiments can be conducted on Assembler, Emulator or Hardware kits, in Assembly language.

- To write an assembly language program to perform 8-bit addition using multiple addressing modes, viz., direct, indirect, register, etc. addressing mode.
- To write an assembly language program to perform 16-bit Logical operations, viz., AND, OR, XOR, NAND, etc.
- To write an assembly language program to perform 32-bit Subtraction
- To write an assembly language program to generate 10 msec delay using software (register) and 8254
- To write an assembly language program to move 10 memory locations using String Instruction
- To write an assembly language subroutine (program) that takes a number as input and returns the square of it
- To write an assembly language program for interfaced 7 segment display or keypad or both, through 8255
- To write an assembly language program to read and save value from ADC
- To write an assembly language program to generate square / triangular / ramp waveforms using DAC
- To write an assembly language program for performing floating point division using 8087
- To write an assembly language program to use INT 21h DOS Functions, viz. read character, write character, get system date, etc

Note: Mini Project can be considered as a part of termwork (Topic based on syllabus)

Term Work:

At least 08 Experiments including 02 simulations covering entire syllabus must be given during the "**Laboratory session batch wise"**. Computation/simulation based experiments are also encouraged. The experiments should be students centric and attempt should be made to make experiments more meaningful, interesting and innovative. Application oriented one mini-project can be conducted for maximum batch of four students.

Term work assessment must be based on the overall performance of the student with every experiments/tutorials and mini-projects (if included) are graded from time to time. The grades will be converted to marks as per "**Choice Based Credit and Grading System"** manual and should be added and averaged. Based on above scheme grading and term work assessment should be done.

The practical and oral examination will be based on entire syllabus.

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	TW/Pracs	Tutorial	Total
ECL502	Digital Communication Laboratory	--	02	--	--	1	--	1

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical & Oral	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test2	Avg. Of Test 1 and Test 2					
ECL502	Digital Communication Laboratory	--	--	--	--	25	25	--	50

Experiments should be performed on Bread-board or on experimentation kits.

Suggested Experiment List

- To understand sampling theorem and reconstruction
- To understand Various line codes
- To observe the performance of Return to Zero (RZ) types of line code
- To observe the performance of Non- Return to Zero (NRZ) types of line code
- Modulation and Demodulation of Binary Amplitude Shift Keying
- Modulation and Demodulation of Binary Frequency Shift Keying
- Modulation and Demodulation of Binary Phase Shift Keying
- Modulation and Demodulation of Quadrature Phase Shift Keying
- To observe the effect of signal Distortion using EYE-Diagram
- To Study and perform Linear Block codes
- To Study and perform cyclic codes

Note: Mini Project can be considered as a part of termwork (Topic based on syllabus)

Term Work:

At least 08 Experiments including 02 simulations covering entire syllabus must be given during the "**Laboratory session batch wise"**. Computation/simulation based experiments are also encouraged. The experiments should be students centric and attempt should be made to make experiments more meaningful, interesting and innovative. Application oriented one mini-project can be conducted for maximum batch of four students.

Term work assessment must be based on the overall performance of the student with every

experiments/tutorials and mini-projects (if included) are graded from time to time. The grades will be converted to marks as per "**Choice Based Credit and Grading System"** manual and should be added and averaged. Based on above scheme grading and term work assessment should be done.

The practical and oral examination will be based on entire syllabus.

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	TW/Pracs	Tutorial	Total
ECL503	Business Communication & Ethics Laboratory	2 (classwise)	2 (batch wise)	--	--	2	--	2

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical & Oral	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test2	Avg. Of Test 1 and Test 2					
ECL503	Business Communication & Ethics Laboratory	--	--	--	--	50	--	--	50

Course objectives:
To teach the students

- To inculcate professional and ethical attitude.
- To enhance effective communication and interpersonal skills.
- To build multidisciplinary approach towards all life tasks.

Course outcomes:
After successful completion of the course student will be able to

- Design a technical document using precise language, suitable vocabulary and apt style.
- Develop the life skills/ interpersonal skills to progress professionally by building stronger relationships.
- Demonstrate awareness of contemporary issues knowledge of professional and ethical responsibilities.
- Apply the traits of a suitable candidate for a job/higher education, upon being trained in the techniques of holding a group discussion, facing interviews and writing resume/SOP.
- Deliver formal presentations effectively implementing the verbal and non-verbal skills.

Module No.	Unit No.	Topics	Hrs.
1.0		**Report Writing**	**05**
	1.1	Objectives of Report Writing	
	1.2	Language and Style in a report	
	1.3	Types : Informative and Interpretative (Analytical, Survey and Feasibility)and Formats of reports (Memo, Letter, Short and Long Report)	
2.0		**Technical Writing**	**03**
	2.1	Technical Paper Writing (IEEE Format)	
	2.2	Proposal Writing	
3.0		**Introduction to Interpersonal Skills**	**09**
	3.1	Emotional Intelligence	
	3.2	Leadership and Motivation	
	3.3	Team Building	
	3.4	Assertiveness	
	3.5	Conflict Resolution and Negotiation Skills	
	3.6	Time Management	
	3.7	Decision Making	
4.0		**Meetings & Documentations**	**02**
	4.1	Strategies for conducting effective meetings	
	4.2	Notice, Agenda and Minutes of a meeting	
	4.3	Business meeting etiquettes	
5.0		**Introduction to Corporate Ethics**	**02**
	5.1	Professional and work ethics (responsible use of social media - Facebook, WA, Twitter etc.)	
	5.2	Introduction to Intellectual Property Rights	
	5.3	Ethical codes of conduct in business and corporate activities (Personal ethics, conflicting values, choosing a moral response and making ethical decisions)	
6.0		**Employment Skills**	**07**
	6.1	Group Discussion	
	6.2	Resume Writing	
	6.3	Interview Skills	
	6.4	Presentation Skills	
	6.5	Statement of Purpose	
		Total	**28**

References

1. Fred Luthans, *"Organizational Behavior",* McGraw Hill, edition
2. Lesiker and Petit, *"Report Writing for Business",* McGraw Hill, edition
3. Huckin and Olsen, *"Technical Writing and Professional Communication",* McGraw Hill
4. Wallace and Masters, *"Personal Development for Life and Work",* Thomson Learning, 12th edition
5. Heta Murphy, *"Effective Business Communication",* Mc Graw Hill, edition
6. Sharma R.C. and Krishna Mohan, *"Business Correspondence and Report Writing",* Tata McGraw-Hill Education
7. Ghosh, B. N., *"Managing Soft Skills for Personality Development",* Tata McGraw Hill.
8. Lehman, Dufrene, Sinha, "BCOM", Cengage Learning, 2nd edition
9. Bell, Smith, "Management Communication" Wiley India Edition, 3rd edition.
10. Dr. Alex, K., "Soft Skills", S Chand and Company
11. Subramaniam, R., "Professional Ethics" Oxford University Press.
12. Robbins Stephens P., "Organizational Behavior", Pearson Education
13. https://grad.ucla.edu/asis/agep/advsopstem.pdf

List of Assignments:

1. Report Writing (Theory)
2. Technical Proposal
3. Technical Paper Writing (Paraphrasing a published IEEE Technical Paper)
4. Interpersonal Skills (Group activities and Role plays)
5. Interpersonal Skills (Documentation in the form of soft copy or hard copy)
6. Meetings and Documentation (Notice, Agenda, Minutes of Mock Meetings)
7. Corporate ethics (Case studies, Role plays)
8. Writing Resume and Statement of Purpose

Term Work:

Term work will consist of all assignments from the list. The distribution of marks for term

Work willl be as follows:

Book Report……..(10) Marks

Assignments …… (10) Marks

Project Report Presentation…….. (15) Marks

Group Discussion.…… (10) Marks

Attendance …… (05) Marks

TOTAL: ……(50) Marks

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	TW/Pracs	Tutorial	Total
ECL504	Open Source technology for Communication Lab	--	2	--	--	1	--	1

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical & Oral	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test2	Avg. Of Test 1 and Test 2					
ECL504	Open Source technology for Communication Lab	--	--	--	--	25	25	--	50

Prerequisites:

- Principals of Communication Engineering
- Digital System Design
- Signals and Systems
- Electronics Circuits and Devices

Course objectives:

- Introduction to open source tools for communication lab.
- To simulate and analyze the various parameters of communication systems.
- To understand and implement the communication system/sub system.

Course outcomes:

After successful completion of the course student will be able to

- Learn open source programming tools for communication technology.
- Simulate and analyze the performance of communication system.
- Implement the communication system/subsystem.

Sample List of Experiments:

Note: These are few examples of experiments; teachers may prepare their own list.

Sr. No	Title	Resource
1	Installation of a. Python, NumPy and commPy or b. Octave or c. Scilab or d. Xilinx using HDL Or e. LT SPICE Or f. SEQUEL Note: Any one tool or a combination of tools .	See the E-resource Links
2	Write a program to represent analog signal to digital signal (A to D conversion)	http://www.scilab.in/files/textbooks/ProfSenthikumar/DC.pdf
3	Write a program to generate basic functions a. Unit Impulse Signal b. Unit Step Signal c. Generate Ramp Signal d. Exponential Sequence e. Generate Sine Sequence f. Cos Sequence	See the E-resource Links
4	Write a program to perform convolution and correlation on the given signal.	See the E-resource Links
5	Plot the ASK, FSK and PSk Waveforms using scilab/python	See the E-resource Links
6	Write a program to apply Low/High Pass Filter on the given signal.	See the E-resource Links
7	Write a program to read a speech signal and plot it and play it.	See the E-resource Links

8	Write a program to apply Low/High Pass Filter on the given signal.	See the E-resource Links
9	Write a code to design Butterworth/Chebyshev filter using Scilab/Octave/Python.	See the E-resource Links
10	Write a program to calculate Hamming distance using Scilab/python.	See the E-resource Links
11	Encoding and decoding of convolutional codes	1.https://github.com/veeresht/CommPy/blob/master/commpy/examples/conv_encode_decode.py 2.https://media.readthedocs.org/pdf/commpy/latest/commpy.pdf
12	Design and programming of of 1-bit Full adder and testing using Testbench.	See the E-resource Links
13	Design and programming of 4-bit adder using Full adder and testing using Testbench	See the E-resource Links
14	Design and programming of 8:1 Mux and testing using Testbench	See the E-resource Links
15	Design and programming of 3:8 Decoder and testing using Testbench	See the E-resource Links
16	Design and programming of D Latch and D Flip Flop and testing using Testbench	See the E-resource Links
17	Design and programming of T FF and testing using Testbench	See the E-resource Links
18	Design and programming of Counter and testing using Testbench	See the E-resource Links
19	Design and programming of RAM and testing using Testbench	See the E-resource Links
20	Design and Programming of FSM and testing using	See the E-resource Links

	Testbench	
21	Design and Simulation of Basic diode Circuits like Clipper, Clapper, Voltage Doubler using Sequel or LT Spice	See the E-resource Links
22	Design and simulation of single stage and Multistage BJT amplifier using Sequel or LT SPICE	See the E-resource Links
23	Design and simulation of Differential amplifier and current mirror circuit using Sequel or LT SPICE	See the E-resource Links
24	Design and Simulation of Basic Op-circuits like Inverting amplifier , Non-Inverting amplifier, Difference amplifier, I to V convertor, V to I Convertor etc using Sequel ot LT SPICE.	See the E-resource Links
25	Design and Simulation of oscillators and Filters using Op-amp using LT SPICE or Sequel.	See the E-resource Links
26	Simulation of non-linear applications of Op-amp like Schmitt Trigger, Window Detector, Precision Rectifier, Square Wave Generator etc using LT SPICE or Sequel.	See the E-resource Links

List of Mini projects:

Note: These are few examples of mini projects; teachers may prepare their own list.

1. Implementing liner block code of (7,4).
2. Implementing FSK TX and RX.
3. Implementing Nyquist criteria with noisy environment.

Suggested List of Mini Projects on Xilinx using HDL Programming

4. 16 bit Multiplier
5. 32 Bit CLA adder
6. Shift and Add Multiplier
7. GCD Calculator
8. 3-bit FIR Filter design
9. 4 Bit ALU
10. 4-bit Comparator
11. 2’s Complement adder

Suggested List of Mini Projects using LT SPICE or SEQUEl

12. Audio Equalizer using Op-amp.
13. Strain Guage amplifier Circuit.
14. Synchronous DC-DC Buck Convertor.
15. RTD based 4 to 20mA transmitter circuit.

Online Repository Sites:

1. Google Drive
2. GitHub
3. Code Guru

E-Resources:

1. Spoken Tutorial : http://spoken-tutorial.org/
2. Scilab: http://www.scilab.org/
3. Octave: https://www.gnu.org/software/octave/
4. Python: https://www.python.org/
5. Xilinx using HDL: **https://www.xilinx.com/products/design-tools/ise-design-suite/ise-webpack.html**
6. LT SPICE : http://www.linear.com/designtools/software/
7. SEQUEL**:** https://www.ee.iitb.ac.in/~sequel/

Note: Mini Project can be considered as a part of termwork (Topic based on syllabus)

Term Work:

At least 08 Experiments covering entire syllabus must be given during the "**Laboratory session batch wise"**. Computation/simulation based experiments are also encouraged. The experiments should be students centric and attempt should be made to make experiments more meaningful, interesting and innovative. Application oriented one mini-project can be conducted for maximum batch of four students.
Term work assessment must be based on the overall performance of the student with every experiments/tutorials and mini-projects (if included) are graded from time to time. The grades will be converted to marks as per "**Choice Based Credit and Grading System"** manual and should be added and averaged. Based on above scheme grading and term work assessment should be done.
The practical and oral examination will be based on entire syllabus.

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	TW/Pracs	Tutorial	Total
ECLDLO 5011	Microelectronics Laboratory	--	--	02	--	1	--	1

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical & Oral	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test2	Avg. Of Test 1 and Test 2					
ECLDLO 5011	Microelectronics Laboratory	--	--	--	--	25	--	--	25

Term Work:

At least 08 tutorials covering entire syllabus must be given during the "**Tutorial session batch wise"**

Term work assessment must be based on the overall performance of the student with every tutorial graded from time to time. The grades will be converted to marks as per "**Choice Based Credit and Grading System"** manual and should be added and averaged. Based on above scheme grading and term work assessment should be done.

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	TW/Pracs	Tutorial	Total
ECLDLO 5012	TV & Video Laboratory	--	--	02	--	1	--	1

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical & Oral	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test2	Avg. Of Test 1 and Test 2					
ECLDLO 5012	TV & Video Laboratory	--	--	--	--	25	--	--	25

Suggested List of Experiments

- To study CVS
- Measurement of horizontal and vertical scanning frequency
- To study sound section of TV receiver
- To study receiver sections by using fault simulation switches
- To study DTH receiver
- To study HDTV
- To study set top box trainer
- To study LCD display
- To study LED display

Term Work:

At least 8 Practicals/ Tutorials covering entire syllabus must be given during the "**Laboratory session batch wise"**. Computation/simulation based experiments are also encouraged. The experiments should be students centric and attempt should be made to make experiments more meaningful, interesting and innovative. Application oriented one mini-project can be conducted for maximum batch of four students.

Term work assessment must be based on the overall performance of the student with every experiments/tutorials and mini-projects (if included) are graded from time to time. The grades will be converted to marks as per "**Choice Based Credit and Grading System"** manual and should be added and averaged. Based on above scheme grading and term work assessment should be done.

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	TW/Pracs	Tutorial	Total
ECLDLO 5013	Finite Automata Theory	--	--	02	--	1	--	1

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical & Oral	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test2	Avg. Of Test 1 and Test 2					
ECLDLO 5013	Finite Automata Theory	--	--	--	--	25	--	--	25

List of Mini Projects:

1. Combinational circuits
2. Synchronous sequential circuits (Finite state machine)
3. Asynchronous sequential circuits (Finite state machine)
4. Algorithmic state machine

Note: Mini Project can be considered as a part of term-work.

Term Work:

At least 8 Tutorials covering entire syllabus must be given during the "**Laboratory session batch wise"**. Computation/simulation based experiments are also encouraged. The experiments should be students centric and attempt should be made to make experiments more meaningful, interesting and innovative. Application oriented one mini-project can be conducted for maximum batch of four students.

Term work assessment must be based on the overall performance of the student with every experiments/tutorials and mini-projects (if included) are graded from time to time. The grades will be converted to marks as per "**Choice Based Credit and Grading System"** manual and should be added and averaged. Based on above scheme grading and term work assessment should be done.

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	TW/Pracs	Tutorial	Total
ECLDLO 5014	Data Compression & Encryption	--	02	--	--	1	--	1

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical & Oral	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test2	Avg. Of Test 1 and Test 2					
ECLDLO 5014	Data Compression & Encryption	--	--	--	--	25	--	--	25

Suggested Practical List:

- Huffman Code.
- Adaptive Huffman Code.
- Arithmetic Code.
- LZW Compression and Decompression.
- Companding Implementation.
- Implementation of DCT.
- RSA and MD5 Algorithm.
- Packet Analyzer.
- PGP (Pretty Good Privacy).
- Vulnerability Scanner.
- Intrusion Detection System.
- Firewall.
- SSL

Note: Mini Project can be considered as a part of term-work.

Term Work:

At least 08 Experiments covering entire syllabus must be given during the "**Laboratory session batch wise"**. Computation/simulation based experiments are also encouraged. The experiments should be students centric and attempt should be made to make experiments more meaningful,

interesting and innovative. Application oriented one mini-project can be conducted for maximum batch of four students.

Term work assessment must be based on the overall performance of the student with every experiments/tutorials and mini-projects (if included) are graded from time to time. The grades will be converted to marks as per "**Choice Based Credit and Grading System"** manual and should be added and averaged. Based on above scheme grading and term work assessment should be done.

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECC601	Microcontrollers & Applications	04	--	--	04	--	--	04

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical & Oral	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test2	Avg. Of Test 1 and Test 2					
ECC601	Microcontrollers & Applications	20	20	20	80	--	--	--	100

Course objectives:

- To develop background knowledge and core expertise in microcontrollers.
- To understand peripheral devices and their interfacing to microcontrollers.
- To write programs for microcontrollers and their applications in Assembly and Embedded C Language.

Course outcomes:

After successful completion of the course student will be able to

- Understand the detailed architecture of 8051 and ARM7 microcontroller.
- Study the in-depth working of the microcontrollers and their Instruction set.
- Interface various peripheral devices to the microcontrollers.
- Write Assembly language and Embedded C program for microcontrollers.

Module No.	Unit No.	Topics	Hrs.
1.0		**8051 Microcontroller**	**12**
	1.1	Comparison between Microprocessor and Microcontroller	
	1.2	Features, architecture and pin configurations	
	1.3	CPU timing and machine cycle	
	1.4	Input / Output ports	
	1.5	Memory organization	
	1.6	Counters and timers	
	1.7	Interrupts	
	1.8	Serial data input and output	
2.0		**8051 Programming**	**08**
	2.1	Instruction set	
	2.2	Addressing mode	
	2.3	Assembler Directives	
	2.4	**Programs related to**: arithmetic, logical, delay, input, output, timer, counters, port, serial communication, and interrupts	
3.0		**8051 Interfacing and Applications**	**06**
	3.1	Interfacing of Display: LED, LCD and Seven Segment display	
	3.2	Stepper Motor and Relay	
	3.3	UART	
4.0		**ARM7: A 32 bit Microcontroller**	**08**
	4.1	The RISC and the CISC design philosophy	
	4.2	Concept of Cortex-A, the Cortex-R and the Cortex-M	
	4.3	Features of ARM Microcontroller	
	4.4	Pipeline Architecture	
	4.5	Registers	
	4.6	Exceptions, Interrupt and Vector Table	
	4.7	Memory Management	
5.0		**ARM7 Programming**	**08**
	5.1	Data Processing Instructions	
	5.2	Conditional and Branching Instructions	
	5.3	ARM-THUMB Interworking	
	5.4	Single-Register Load-Store Instructions	
	5.5	Stack Instructions	
	5.6	Software Interrupt Instructions	
6.0		**ARM Programming with Embedded C**	**06**
	6.1	General Purpose Input Output	
	6.2	Timer Mode	
	6.3	Pulse –Width Modulator Configuration	
		Total	**48**

Text Books:

1. M. A. Mazidi, J. G. Mazidi and R. D. Mckinlay, "*The 8051 Microcontroller & Embedded systems*", Pearson Publications, Second Edition 2006.
2. C. Kenneth J. Ayala and D. V. Gadre, "*The 8051 Microcontroller & Embedded system using assembly & 'C*' ", Cengage Learning, Edition 2010.
3. Satish Shah, "*The 8051 Microcontrollers",* Oxford publication first edition 2010.
4. Andrew Sloss, Dominic Symes, and Chris Wright, "*ARM System Developer's Guide*" Morgan Kaufmann Publishers, First Edition 2004.
5. Lyla Das, "*Embedded Systems: An Integrated Approach*", Pearson Publication, First Edition 2013
6. James A. Langbridge, "*Professional Embedded Arm Development*", Wrox, John Wiley Brand& Sons Inc., Edition 2014

Internal Assessment:
Assessment consists of two class tests of 20 marks each. The first class test is to be conducted when approximately 40% syllabus is completed and second class test when additional 40% syllabus is completed. The average marks of both the test will be considered for final Internal Assessment. Duration of each test shall be of one hour.

End Semester Examination:
1. Question paper will comprise of 6 questions, each carrying 20 marks.
2. The students need to solve total 4 questions.
3. Question No.1 will be compulsory and based on entire syllabus.
4. Remaining question (Q.2 to Q.6) will be selected from all the modules.

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECC602	Computer Communication Networks	04	--	--	04	--	--	04

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical & Oral	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test2	Avg. Of Test 1 and Test 2					
ECC602	Computer Communication Networks	20	20	20	80	--	--	--	100

Course Pre requisite:

- Analog Communication

Course objectives:

- To introduce analysis and design of computer and communication networks.
- To design and configure a network for an organization. To implement client-server socket programs.
- To analyse the traffic flow and the contents of protocol frames.

Course outcomes:

After successful completion of the course student will be able to

- Design a small or medium sized computer network including media types, end devices, and interconnecting devices that meets a customer's specific needs.
- Perform basic configurations on routers and Ethernet switches.
- Demonstrate knowledge of programming for network communications.
- Learn to simulate computer networks and analyse the simulation results.
- Troubleshoot connectivity problems in a host occurring at multiple layers of the OSI model.
- Develop knowledge and skills necessary to gain employment as computer network engineer and network administrator.

Module No.	Unit No.	Topics	Hrs.
1.0		**Introduction**	**06**
	1.1	Network Applications	
	1.2	Network Hardware	
	1.3	Network Software	
	1.4	Reference Models, overview of TCP/IP, layer Functions, services, sockets and ports, Encapsulation.	
2.0		**Introduction to Physical layer Services and System**	**08**
	2.1	Introduction to physical media, Coax, RJ 45 , fiber, twisted pair, DSL, HFC, WiMax, cellular, satellite, and telephone networks, bit transmission, frequency division multiplexing. time division multiplexing.	
3.0		**The Data Link Layer**	**08**
	3.1	Data link Layer Design Issues	
	3.2	Error Detection and Correction	
		Elementary Data Link Protocols, Sliding Window Protocols	
		Example Data Link Protocols: HDLC: High-Level Data Link Control, The Data Link Layer in The Internet.	
4.0		**The Medium Access Sub- Layer**	**06**
	4.1	Channel Allocation Problem.	
	4.2	Multiple Access Protocols.	
5.0		**The Network Layer**	**10**
	5.1	Network Layer Design Issues.	
	5.2	Routing Algorithms.	
	5.3	Congestion Control Algorithms, Quality of Service.	
	5.4	Internetworking.	
	5.5	The Network Layer In The Internet: The IP Protocol, IPv4 header, IP Addressesing, Subnetting.	
	5.6	Internet Control Protocols, The Interior Gateway Routing Protocol: OSPF, The Exterior Gateway Routing Protocol: BGP.	
6.0		**The Transport Layer**	**10**
	6.1	The Transport Service.	
	6.2	Elements of Transport Protocols.	
	6.3	The Internet Transport Protocol: UDP	
	6.4	The Internet Transport Protocol: TCP:-Introduction to TCP, The TCP Service Model, The TCP Protocol.	
	6.5	The TCP Segment Header.	
	6.6	TCP Connection Establishment, TCP Connection Release.	
	6.7	Modeling TCP Connection Management.	
	6.8	TCP Transmission Policy.	
	6.9	TCP Congestion Control.	
	6.10	TCP Timer Management, Transactional TCP.	

		Total	**48**

Text Books:

1. A. S. Tanenbaum,"Computer Networks", 4th edition, Prentice Hall
2. B. F. Ferouzan,"Data and Computer Communication", Tata McGraw Hill.

Reference Books:

1. Peterson & Davie, "Computer Networks", 2nd Edition, Morgan Kaufmann.
2. Kurose, Ross, "Computer Networking", Addison Wesley
3. S. Keshav, "An Engg, Approach To Computer Networking", Addison Wesley.
4. W. Richard Stevens, "TCP/IP Volume1, 2, 3", Addison Wesley.
5. D. E. Comer, "Computer Networks And Internets", Prentice Hall.
6. B. F. Ferouzan , "TCP/IP Protocol Suite", Tata McGraw Hill.

Internal Assessment:
Assessment consists of two class tests of 20 marks each. The first class test is to be conducted when approximately 40% syllabus is completed and second class test when additional 40% syllabus is completed. The average marks of both the test will be considered for final Internal Assessment. Duration of each test shall be of one hour.

End Semester Examination:
1. Question paper will comprise of 6 questions, each carrying 20 marks.
2. The students need to solve total 4 questions.
3. Question No.1 will be compulsory and based on entire syllabus.
4. Remaining question (Q.2 to Q.6) will be selected from all the modules.

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECC603	Antenna & Radio Wave Propagation	04	--	--	04	--	--	04

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical & Oral	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test2	Avg. Of Test 1 and Test 2					
ECC603	Antenna & Radio Wave Propagation	20	20	20	80	--	--	--	100

Prerequisites:

- Electromagnetic Field
- Two port network
- Transmission Line

Course objectives:

- To learn fundamental parameters of Antenna
- To learn about linear wire antenna elements and Antenna arrays
- To learn about Special types of Antennas
- To learn about Antenna measurements and radio wave propagation

Course outcomes:

After successful completion of the course student will be able to

- Define Basic antenna parameters like radiation pattern, directivity and gain.
- Derive the field equations for the basic radiating elements like linear wire antenna and loop antenna.
- Design of uniform linear and planar antenna arrays using isotropic and directional Sources.
- Implement special types of Antennas like microstrip antennas and reflectors.

Module No.	Unit No.	Topics	Hrs.
1.0		**Antenna Fundamentals**	**08**
	1.1	Introduction, Radiation Mechanism, basic antenna parameters, Radiation pattern, radiation power density, radiation intensity, Beamwidth, directivity, Antenna efficiency, Gain, beam efficiency, bandwidth, polarization, input impedance, antenna vector effective length and equivalent areas, Antenna radiation efficiency, FRIIS transmission equation	
	1.2	Basic concepts of Maxwell's equation, vector potential, wave equation, near field and far field radiation, dual equations for electric and magnetic current sources.	
2.0		**Wire Elements: Dipoles, Monopoles, Loops and Helical**	**12**
	2.1	Infinitesimal dipole, radiation fields, radiation resistance, radiation sphere, near field, far field directivity, small dipole, finite length dipole, half wave length dipole, linear elements near or on infinite perfect conductors, Monopole antenna, Folded dipole. Design of dipole and monopole antenna	
	2.2	Loop Antenna: Small circular loop, comparison of small loop with short dipole, Ferrite loop, radiation patterns its parameters and their application.	
	2.3	Helical Antennas: Input impedance matching, Axial mode and normal mode propagation, Circular polarization using Helical Antenna	
3.0		**Arrays**	**12**
	3.1	Linear arrays, Array of two isotropic point sources, linear arrays of N elements, principle of pattern multiplication applicable to non-isotropic sources, Phase scanning arrays, broadside and End-fire Array, Increased Directivity end fire array, Calculations of Directivity, Beam width, Maxima and null directions for N-element Array.	
	3.2	Introduction to planner and circular arrays	
	3.3	Design of Yagi antenna and Log Periodic antenna	
4.0		**Aperture Antennas**	**06**
	4.1	Horn Antennas :E-Plane Sectoral Horn, H-Plane Sectoral Horn, Pyramidal Horn, Conical Horn	
	4.2	Reflector Antennas: Introduction, Plane Reflector, Corner Reflector, Parabolic Reflector, Design considerations	
5.0		**Patch Antenna**	**04**
	5.1	Microstrip antenna (MSA): Introduction, Feeding Techniques, Regular Shape MSAs (Rectangular, Circular, Equilateral Triangular), Design of Regular shape MSAs	
6.0		**Antenna Measurements & Wave Propagation**	**06**

	6.1	**Antenna Measurements:** Measurement of Antenna parameters: Input Impedance, Radiation Pattern, Gain (Two and Three antenna method), Polarization.	
	6.2	**Ground Wave Propagation:** Ground waves, effect of Earth's Curvature on Ground wave propagation, impact of imperfect earth	
	6.3	**Sky Wave Propagation** Ionosphere and Earth magnetic field effect, Critical frequency, Angle of incidence, Maximum usable frequency, Skip distance, Virtual height, Variations in ionosphere and Attenuation and fading of waves in ionosphere	
	6.4	Space Wave Propagation	
		Total	**48**

Text Books:

1. C. A. Balanis, Antenna Theory: Analysis and Design (3rd eds.), John Wiley & Sons, Hoboken, NJ, 2005.
2. J. D. Kraus, R. J. Marhefka, A.S. Khan "Antennas & Wave Propagation", McGraw Hill Publications, 4th Edition, 2011
3. G. Kumar, K. P. Ray, Broadband Microstrip Antenna, Artech House, 2002.

Reference Books:

1. Stutzman, Theile, " Antenna Theory and Design", John Wiley and Sons , 3rd Edition
2. R. E. Collin, "Antennas and Radio Wave Propagation", International Student Edition, McGraw Hill.

Internal Assessment:
Assessment consists of two class tests of 20 marks each. The first class test is to be conducted when approximately 40% syllabus is completed and second class test when additional 40% syllabus is completed. The average marks of both the test will be considered for final Internal Assessment. Duration of each test shall be of one hour.

End Semester Examination:
1. Question paper will comprise of 6 questions, each carrying 20 marks.
2. The students need to solve total 4 questions.
3. Question No.1 will be compulsory and based on entire syllabus.
4. Remaining question (Q.2 to Q.6) will be selected from all the modules.

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECC604	Image Processing & Machine Vision	04	--	--	04	--	--	04

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical & Oral	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test2	Avg. Of Test 1 and Test 2					
ECC604	Image Processing & Machine Vision	20	20	20	80	--	--	--	100

Prerequisites:

- Signals and Systems
- Discrete Time Signal Processing

Course objectives:

- To cover the fundamentals and mathematical models in digital image processing and Machine Vision
- To develop time and frequency domain techniques for image enhancement.
- To expose the students to classification techniques in Machine Vision
- To develop Applications using image processing and Machine Vision

Course outcomes:

After successful completion of the course student will be able to

- Understand theory and models in image processing.
- Interpret and analyze 2D signals in Spatial and frequency domain through image transforms.
- Apply quantitative models of image processing for segmentation and restoration for various applications.
- Find shape using various representation techniques and classify the object using different classification methods.

Module No.	Unit No.	Topics	Hrs.
1.0		**Digital Image Fundamentals**	**04**
	1.1	**Introduction** – Origin – Steps in Digital Image Processing , Components, Elements of Visual Perception – Image Sensing and Acquisition, Image Sampling and Quantization – Relationships between pixels, Transformation: Orthogonal, Euclidean, Affine	
	1.2	**Color Image Processing**: Color Fundamentals Color models.	
2.0		**Image Transforms**	**06**
	2.1	1-D DFT, 2-D Discrete Fourier Transform and Its Inverse, Some Properties of 2D DFT ,Walsh -Hadamard, Discrete Cosine Transform, Haar Transform	
3.0		**Image Enhancement**	**08**
	3.1	Image Negative, Log Transform, Power Law transform, Histogram equalization and Histogram Specification	
	3.2	**Spatial Domain**: Basics of Spatial Filtering, The Mechanics of Spatial Filtering, Generating Spatial Filter Masks–Smoothing and Sharpening Spatial Filtering	
	3.3	**Frequency Domain**:, The Basics of Filtering in the Frequency Domain, Smoothing and Sharpening frequency domain filters – Ideal, Butterworth and Gaussian filters, Laplacian, Unsharp Masking and Homomorphic filters	
4.0		**Morphological & Image Restoration**	**06**
	4.1	**Morphology**: Erosion and Dilation, Opening and Closing, The Hit-or-Miss Transformation.	
	4.2	**Restoration :**Noise models – Mean Filters – Order Statistics – Adaptive filters – Band reject Filters – Band pass Filters – Notch Filters	
5.0		**Patch Antenna**	**12**
	5.1	**Point, Line, and Edge Detection:** Detection of Isolated Points, Line detection, edge models, basic and advance edge detection, Edge linking and boundary detection , Canny's edge detection algorithm	
	5.2	**Thresholding** : Foundation, Role of illumination, Basic Global thresholding	
	5.3	**Region Based segmentation**: Region Growing, Region Splitting and merging	
	5.4	**Region Identification**, chain code, simple geometric border representation, Fourier Transform of boundaries, Boundary description using segment sequences, B-spline representation	
6.0		**Boundary Description & Object Recognition**	**12**

	6.1	**Texture:** Statistical Texture Description Methods- Methods based on spatial frequencies, co-occurrence matrices, edge frequency, primitive length, Law's texture energy measures	
	6.2	**Object Recognition** Knowledge representation, Classification Principles, Classifier setting, Classifier Learning, Support vector machine, cluster analysis	
		Total	**48**

Text Books:

1. Milan Sonka, Vaclav Hlavac, Roger Boyle, "Image Processing, Analysis, and Machine Vision" Cengage Engineering, 3rd Edition, 2013
2. Gonzales and Woods, "Digital Image Processing", Pearson Education, India, Third Edition,

Reference books:

1. Anil K.Jain, "Fundamentals of Image Processing", Prentice Hall of India, First Edition, 1989.
2. W Pratt, "Digital Image Processing", Wiley Publication, 3rd Edition, 2002

Internal Assessment:
Assessment consists of two class tests of 20 marks each. The first class test is to be conducted when approximately 40% syllabus is completed and second class test when additional 40% syllabus is completed. The average marks of both the test will be considered for final Internal Assessment. Duration of each test shall be of one hour.

End Semester Examination:
1. Question paper will comprise of 6 questions, each carrying 20 marks.
2. The students need to solve total 4 questions.
3. Question No.1 will be compulsory and based on entire syllabus.
4. Remaining question (Q.2 to Q.6) will be selected from all the modules.

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECCDLO 6021	Digital VLSI Design	04	--	--	04	--	--	04

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical & Oral	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test2	Avg. Of Test 1 and Test 2					
ECCDLO 6021	Digital VLSI Design	20	20	20	80	--	--	--	100

Prerequisites:

- Digital System Design
- Microelectronics

Course objectives:

- To highlight the circuit design issues in the context of Digital VLSI technology
- A profound understanding of Digital VLSI design circuits using different design styles.
- To provides an exposure to RTL design and programming

Course outcomes:

After successful completion of the course student will be able to

- Understand the semiconductor technology, scaling and performance.
- Realize logic circuits with different design styles.
- To understand operation of memory, storage circuits and data path elements.
- Simulate and synthesize digital circuits using HDL language.
- Demonstrate an understanding of system level design issues such as protection, clocking, and routing.
- Learn the RTL design techniques and methodologies

Module No.	Unit No.	Topics	Hrs.
1.0		**MOS Circuit Design Styles**	**10**
	1.1	Static CMOS, Dynamic CMOS , Pseudo NMOS, Domino, C^2MOS, NORA logic, NP Domino logic	
	1.2	Realization of Multiplexer (upto 4:1 Mux) , Encoder, Decoder,SR Latch, JK FF, D FF, 1 Bit Shift Register with different design styles and their layouts	
2.0		**Memory and Storage circuits**	**08**
	2.1	ROM array, SRAM (operation, design strategy, leakage currents, read /write circuits), layout of SRAM	
	2.2	DRAM (Operation of 1T, 3T, operation modes, leakage currents, refresh operation, Input-Output circuits), layout of DRAM	
	2.3	Flash memory: NAND and NOR flash memory	
3.0		**Data path design**	**08**
	3.1	Full adder, Ripple carry adder, CLA adder, Carry Skip Adder, Carry Save Adder and carry select adder	
	3.2	Array Multiplier	
	3.3	Barrel shifter	
4.0		**VLSI Clocking, Protection and Interconnect**	**06**
	4.1	CMOS clocking styles, pipelined systems, Clock generation, stabilization and distribution	
	4.2	ESD protection, Input circuits, Output circuits, power distribution scheme	
	4.3	Interconnect delay model, interconnect scaling and crosstalk	
5.0		**Design methods**	**08**
	5.1	Semicustom, Full custom design, ASIC	
	5.2	PLA, PLD, PAL, FPGA	
	5.3	System based and Data path design using HDL	
6.0		**RTL Design**	**08**
	6.1	High Level state machines, RTL design process	
	6.2	Soda dispenser machine, laser based distance measure, Sum of absolute	
	6.3	FIR filter design	
		Total	**48**

Text Books:

1. Sung-Mo Kang and Yusuf Leblebici, "*CMOS Digital Integrated Circuits Analysis and Design*", Tata McGraw Hill, 3rd Edition, 2012.
2. P. Uyemura, "*Introduction to VLSI Circuits and Systems*", John Wiley & Sons.
3. Frank Vahid, " Digital Design with RTL design, VHDL and VERILOG", John Wiley and Sons Publisher 2011.

4. Neil H. E. Weste, David Harris and Ayan Banerjee, "*CMOS VLSI Design: A Circuits and Systems Perspective*", Pearson Education, 3rd Edition.
5. Samir Palnitkar,"Verilog HDL: A Guide to Digital Design and Synthesis", PHI, Second Edition
6. Douglas L. Perry " VHDL: Programming by Example" , McGrawHill, 4th Edition

Reference Books:

1. Jan M. Rabaey, Anantha Chandrakasan and Borivoje Nikolic, "Digital Integrated Circuits: A Design Perspective", Pearson Education, 2nd Edition..
2. Volnei A. Pedroni ,"Circuit Design and Simulation with VHDL", MIT Press, 2nd Edition

Internal Assessment:
Assessment consists of two class tests of 20 marks each. The first class test is to be conducted when approximately 40% syllabus is completed and second class test when additional 40% syllabus is completed. The average marks of both the test will be considered for final Internal Assessment. Duration of each test shall be of one hour.

End Semester Examination:
1. Question paper will comprise of 6 questions, each carrying 20 marks.
2. The students need to solve total 4 questions.
3. Question No.1 will be compulsory and based on entire syllabus.
4. Remaining question (Q.2 to Q.6) will be selected from all the modules.

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECCDLO 6022	Radar Engineering	04	--	--	04	--	--	04

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical & Oral	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test2	Avg. Of Test 1 and Test 2					
ECCDLO 6022	Radar Engineering	20	20	20	80	--	--	--	100

Prerequisties:

- Communication Fundamentals
- Electromagnetic field
- Transmission Lines and Antenna

Course objectives:

- To interpret Radar equations
- To explain different types of radar
- To design RADAR transmitters and receivers for given conditions

Course outcomes:

After successful completion of the course student will be able to

- Explain generalized concept of RADAR.
- Solve problems using radar equations.
- Describe different types of radar for specific application.
- Explain concept of tracking radar.
- Evaluate the design constraints for transmitter.
- Evaluate the design constraints for receiver.

Module No.	Unit No.	Topics	Hrs.
1.0		**Introduction to Radar**	**04**
	1.1	Basics Radar, Radar equation	
	1.2	Block Diagram, Radar Frequencies	
	1.3	Applications of Radar	
2.0		**Radar Equation**	**08**
	2.1	Detection of signal in noise	
	2.2	Receiver Noise and Signal-to-noise Ratio	
	2.3	Probability of detection and false alarm: Simple , complex Targets	
	2.4	Pulse Repetition Frequency	
3.0		**MTI and Pulse Doppler Radar**	**12**
	3.1	Introduction to Doppler and MTI radar, Doppler frequency shift	
	3.2	Simple CW Doppler radar, MTI radar block diagram	
	3.3	Delay line canceler	
	3.4	Moving-target-detection	
	3.5	Pulse Doppler radar	
4.0		**Tracking Radar**	**08**
	4.1	Monopulse tracking	
	4.2	Conical scan and sequential lobbing	
	4.3	Limitation of tracking accuracy	
	4.4	Low angle tracking	
5.0		**Radar Transmitters**	**10**
	5.1	Radar RF power sources: Klystron, Travelling wave tube	
	5.2	Solid state RF power source: low power transmitter, high power transmitter, Advantages of solid state RF power source	
	5.3	Magnetron: coaxial magnetron	
	5.4	Crossed field amplifiers: CFA operation, modulating a CFA, system implementation	
6.0		**Radar Receivers**	**06**
	6.1	Receiver noise figure	
	6.2	Superheterodyne Receiver	
	6.3	Radar Display: Types of displays	
		Total	**48**

Text Books:

1. Merill Skolnik, –Introduction to RADAR Systems, Tata McGraw Hill, Third Edition
2. Merill Skolnik, –Radar Handbook, TataMcgraw Hill, Second Edition

Reference books:

1. Mark A. Richards, James A. Scheer, William A. Holm, "Principles of Modern Radar Basic Principals", Scitech Publishing.
2. Simon Kingsley, Shaun Quegon, "Understanding Radar Systems", Scientech Publishing Inc.
3. G. S. N. Raju, "Radar Engineering and Fundamentals of Navigational Aids", I. K International publishing House Pvt. Ltd.

Internal Assessment:
Assessment consists of two class tests of 20 marks each. The first class test is to be conducted when approximately 40% syllabus is completed and second class test when additional 40% syllabus is completed. The average marks of both the test will be considered for final Internal Assessment. Duration of each test shall be of one hour.

End Semester Examination:
1. Question paper will comprise of 6 questions, each carrying 20 marks.
2. The students need to solve total 4 questions.
3. Question No.1 will be compulsory and based on entire syllabus.
4. Remaining question (Q.2 to Q.6) will be selected from all the modules.

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECCDLO 6023	Database Management System	04	--	--	04	--	--	04

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical & Oral	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test2	Avg. Of Test 1 and Test 2					
ECCDLO 6023	Database Management System	20	20	20	80	--	--	--	100

Prerequisites:

- Basic knowledge of programming

Course objectives:

- Learn and practice data modeling using the entity-relationship and developing database designs.
- Understand the use of Structured Query Language (SQL) and learn SQL syntax.
- Understand the needs of database processing and learn techniques for controlling the consequences of concurrent data access

Course outcomes:

After successful completion of the course student will be able to

- Understand the different issues involved in the design and implementation of a database system.
- Transform an information model into a relational database schema and to use a data definition language and/or utility to implement the schema using a DBMS.
- Demonstrate an understanding of normalization theory and apply such knowledge to the normalization of a database.
- Understand the concepts of constraints, views, concurrency control, deadlock

Module No.	Unit No.	Topics	Hrs.
1.0		**Introduction to Databases and Transactions**	**02**
	1.1	Introduction to databases, History of database system, Benefits of Database system over file system, relational databases, database architecture, transaction management	
2.0		**Data Models**	**06**
	2.1	The importance of data models, Basic building blocks, Business rules, Evolution of data models (hierarchical, Network, Relational, Entity relationship and object model), Degrees of data abstraction.	
3.0		**Database Design, ER-Diagram and Unified Modeling Language**	**10**
	3.1	Database design and ER Model: overview, ER-Model, Constraints, ER-Diagrams, ERD Issues, weak entity sets, Codd's rules, Relational Schemas, Introduction to UML Relational database model: Logical view of data, keys, integrity rules. Relational Database design: features of good relational database design, atomic domain and Normalization (1NF, 2NF, 3NF, BCNF).	
4.0		**Relational Algebra and Calculus**	**10**
	4.1	Relational algebra: introduction, Selection and projection, set operations, renaming, Joins, Division, syntax, semantics. Operators, grouping and ungrouping, relational comparison. Calculus: Tuple relational calculus, Domain relational Calculus, calculus vs algebra, computational capabilities.	
5.0		**Constraints, Views and SQL**	**10**
	5.1	What is constraints, types of constrains, Integrity constraints, Views: Introduction to views, data independence, security, updates on views, comparison between tables and views SQL: data definition, aggregate function, Null Values, nested sub queries, Joined relations. Triggers.	
6.0		**Transaction management and Concurrency control**	**10**
	6.1	Transaction management: ACID properties, serializability and concurrency control, Lock based concurrency control (2PL, Deadlocks), Time stamping methods, optimistic methods, database recovery management.	
		Total	**48**

Text Books:

1. A Silberschatz, H Korth, S Sudarshan, "Database System and Concepts", Fifth Edition McGraw-Hill
2. Rob, Coronel, "Database Systems", Seventh Edition, Cengage Learning.
3. Ramez Elmasri, Shamkant B. Navathe, "Fundamentals of Database System", Seventh Edition, Person.
4. G. K. Gupta: "Database Management Systems", McGraw – Hill.

Reference Books:

1. Peter Rob and Carlos Coronel, "Database Systems Design, Implementation and Management", Thomson Learning, 5th Edition.
2. P.S. Deshpande, "SQL and PL/SQL for Oracle 11g, Black Book", Dreamtech Press
3. Mark L. Gillenson, Paulraj Ponniah, "Introduction to Database Management", Wiley
4. Raghu Ramkrishnan and Johannes Gehrke, "Database Management Systems", TMH
5. Debabrata Sahoo "Database Management Systems" Tata McGraw Hill, Schaum's Outline

E-Resources:

1. https://www.tutorialspoint.com/dbms/index.htm
2. https://www.studytonight.com/dbms/
3. https://beginnersbook.com/2015/04/dbms-tutorial/
4. https://www.w3schools.in/dbms/
5. https://www.tutorialcup.com/dbms

Internal Assessment:
Assessment consists of two class tests of 20 marks each. The first class test is to be conducted when approximately 40% syllabus is completed and second class test when additional 40% syllabus is completed. The average marks of both the test will be considered for final Internal Assessment. Duration of each test shall be of one hour.

End Semester Examination:
1. Question paper will comprise of 6 questions, each carrying 20 marks.
2. The students need to solve total 4 questions.
3. Question No.1 will be compulsory and based on entire syllabus.
4. Remaining question (Q.2 to Q.6) will be selected from all the modules.

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECCDLO 6024	Audio Processing	04	--	--	04	--	--	04

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical & Oral	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test2	Avg. Of Test 1 and Test 2					
ECCDLO 6024	Audio Processing	20	20	20	80	--	--	--	100

Prerequisites

- Signal System

Course objectives:

- To understand basic concepts and methodologies for the analysis and modeling of speech signal.
- To characterize the speech signal as generated by a speech production model.
- To understand the mechanism of speech and audio perception.
- To understand the digital representation of the speech waveform.
- To perform the analysis of speech signal using STFT.
- To extract the information of the speech or audio signals.
- To provide a foundation for developing application in this field.

Course outcomes:

After successful completion of the course student will be able to

- Demonstrate advanced Knowledge in Digital model representation of speech signal.
- Design and implement algorithms for processing speech and audio signals considering the properties of acoustic signals and human hearing.
- Analyze speech signal to extract the characteristic of vocal tract (formants) and vocal cords (pitch).
- Formulate and design a system for speech recognition and speaker recognition.
- Acquired knowledge about audio and speech signal estimation and detection.

Module No.	Unit No.	Topics	Hrs.
1.0		**Introduction**	**06**
	1.1	Review of digital signal and systems, Transforms representations of signal and systems, Sampling Theorem, Goertzel algorithm, Chirp algorithm.	
2.0		**Digital Models for Speech signals**	**06**
	2.1	Speech production and acoustic tube modeling, acoustic phonetics, anatomy, and physiology of the vocal tract and ear, hearing and perception.	
3.0		**Digital Representations of the Speech Waveform**	**08**
	3.1	Sampling speech signals, Instantaneous quantization, Adaptive quantization, Differential quantization, Delta Modulation, Differential PCM, Comparison of systems, Direct digital code conversion.	
4.0		**Time Domain Models for Speech Processing**	**12**
	4.1	Time dependent processing of speech, Short time energy and average magnitude, Short time average zero crossing rate, Speech V/S silence discrimination using energy & Zero crossings, Pitch period estimation, Short time autocorrelation function, Short time average magnitude difference function, Pitch period estimation using autocorrelation function, Median smoothing.	
5.0		**Short time Fourier Transform**	**10**
	5.1	Introduction- Definition and Properties, Fourier Transform Interpretation ,Linear Filtering Interpretation ,Sampling rates of X_n (e^{jw})in Time and Frequency ,Filter Bank Summation Method of Short -Time Synthesis ,Overlap Addition Method for Short -Time Synthesis.	
6.0		**Speech and Audio Processing**	**06**
	6.1	Vocoder- Voice excited channel vocoder, Voice excited and error signal excited LPC vocoders. Adaptive predictive coding of speech, Auditory Modeling. Audio signal processing for Music applications. Speech recognition pattern comparison techniques, Artificial Neural Network.	
		Total	**48**

Text Books:

1. L R Rabiner and S W Schafer, “Digital processing of speech signals”, Pearson Education, 2009.
2. L R Rabiner, B H Juang, B Yegnanarayana, “Fundamentals of speech Recognition”, Pearson Education, 1993.

Reference Books

1. Thomas F Quateri, “ Discrete Time Speech Signal Processing “Pearson Edition,2006.
2. Ben Gold and Nelson Morgan, “Speech & Audio Signal Processing”, wiley, 2007.
3. Douglas O Shaughnessy, “Speech Communications”, 2nd Edition, Oxford university press, 2000.

Internal Assessment:
Assessment consists of two class tests of 20 marks each. The first class test is to be conducted when approximately 40% syllabus is completed and second class test when additional 40% syllabus is completed. The average marks of both the test will be considered for final Internal Assessment. Duration of each test shall be of one hour.

End Semester Examination:
1. Question paper will comprise of 6 questions, each carrying 20 marks.
2. The students need to solve total 4 questions.
3. Question No.1 will be compulsory and based on entire syllabus.
4. Remaining question (Q.2 to Q.6) will be selected from all the modules.

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	TW/Pracs	Tutorial	Total
ECL601	Microcontroller & Applications Laboratory	--	02	--	--	1	--	1

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical & Oral	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test2	Avg. Of Test 1 and Test 2					
ECL601	Microcontroller & Applications Laboratory	--	--	--	--	25	25	--	50

Suggested Experiment List

1. Perform Arithmetic and Logical Operations
2. Transfer of data bytes between Internal and External Memory
3. Experiments based on General Purpose Input-Output, Timers, Interrupts, Delay, etc
4. Interfacing of LED,LCD, Stepper Motor, UART

Mini project based on any application related to 8051 or ARM7 can be implemented.

Note: Mini Project can be considered as a part of term-work.

Term Work:

At least 08 Experiments including 02 simulations covering entire syllabus must be given during the "**Laboratory session batch wise"**. Computation/simulation based experiments are also encouraged. The experiments should be students centric and attempt should be made to make experiments more meaningful, interesting and innovative. Application oriented one mini-project can be conducted for maximum batch of four students.

Term work assessment must be based on the overall performance of the student with every experiments/tutorials and mini-projects (if included) are graded from time to time. The grades will be converted to marks as per "**Choice Based Credit and Grading System"** manual and should be added and averaged. Based on above scheme grading and term work assessment should be done.

The practical and oral examination will be based on entire syllabus.

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	TW/Pracs	Tutorial	Total
ECL602	Computer Communication Network Laboratory	--	02	--	--	1	--	1

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical & Oral	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test2	Avg. Of Test 1 and Test 2					
ECL602	Computer Communication Network Laboratory	--	--	--	--	25	25	--	50

Suggested Experiment List

1. Create a Virtual Network using NETKIT emulator and use networking commands like route, arp, netstat, traceroute, ping on created topology.
2. To study installation and configuration of NS 2.35 simulator.
3. Design a connectionless and connection oriented network topology for static routing and dynamic routing with the help of NS2 simulator.
4. To study three way handshaking process as well as working process for connection oriented Protocols like FTP, TELNET and analysing packets generated by using packet capturing tool like tcpdump
5. To implement stream socket that can serve multiple clients at the same time.
6. To study requirements and scope of Subnetting and Network Translation by using Netkit Emulator.
7. Case Study: To study installation of linux operating system by using DHCP, TFTP and any repository server like HTTP, FTP or NFS.

Note: Small Project can be considered as a part of term-work.

Term Work:

At least 08 Experiments including 02 simulations covering entire syllabus must be given during the "**Laboratory session batch wise"**. Computation/simulation based experiments are also encouraged. The experiments should be students centric and attempt should be made to make experiments more meaningful, interesting and innovative. Application oriented one mini-project can be conducted for maximum batch of four students.

Term work assessment must be based on the overall performance of the student with every experiments/tutorials and mini-projects (if included) are graded from time to time. The grades will be converted to marks as per "**Choice Based Credit and Grading System"** manual and should be added and averaged. Based on above scheme grading and term work assessment should be done.

The practical and oral examination will be based on entire syllabus.

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	TW/Pracs	Tutorial	Total
ECL603	Antenna & Radio Wave Propagation Laboratory	--	02	--	--	1	--	1

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical & Oral	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test2	Avg. Of Test 1 and Test 2					
ECL603	Antenna & Radio Wave Propagation Laboratory	--	--	--	--	25	25	--	50

Suggested Experiment List

- Introduction to different Antenna parameters and its importance
- Introduction to Different Antenna Types
- Study of Radiation pattern of dipole, folded dipole and Monopole antenna
- Study of Antenna Arrays – N element array for given angle, Parametric study for various arrays parameters
- Study of Yagi-Uda Antenna
- Study of Aperture Antennas – Horn / Reflector Antennas
- Design, implementation and Pattern measurement of Regular shape MSA
- Case Study of Recent reported variations of Antenna types (Paper from reputed journal is to be referred and thoroughly study and present the report, maximum four students per group)

Note: Small Project can be considered as a part of term-work.

Term Work:

At least 08 Experiments including 02 simulations covering entire syllabus must be given during the "**Laboratory session batch wise"**. Computation/simulation based experiments are also encouraged. The experiments should be students centric and attempt should be made to make experiments more meaningful, interesting and innovative. Application oriented one mini-project can be conducted for maximum batch of four students.

Term work assessment must be based on the overall performance of the student with every experiments/tutorials and mini-projects (if included) are graded from time to time. The grades will be converted to marks as per "**Choice Based Credit and Grading System"** manual and should be added and averaged. Based on above scheme grading and term work assessment should be done.

The practical and oral examination will be based on entire syllabus.

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	TW/Pracs	Tutorial	Total
ECL604	Image Processing and Machine Vision Laboratory	--	02	--	--	1	--	1

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical & Oral	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test2	Avg. Of Test 1 and Test 2					
ECL604	Image Processing and Machine Vision Laboratory	--	--	--	--	25	25	--	50

Suggested Experiment List

- At least 8 programs written in C/MATLAB software

Note: Small Project can be considered as a part of term-work.

Term Work:

At least 08 Experiments including 02 simulations covering entire syllabus must be given during the "**Laboratory session batch wise"**. Computation/simulation based experiments are also encouraged. The experiments should be students centric and attempt should be made to make experiments more meaningful, interesting and innovative. Application oriented one mini-project can be conducted for maximum batch of four students.

Term work assessment must be based on the overall performance of the student with every experiments/tutorials and mini-projects (if included) are graded from time to time. The grades will be converted to marks as per "**Choice Based Credit and Grading System"** manual and should be added and averaged. Based on above scheme grading and term work assessment should be done.

The practical and oral examination will be based on entire syllabus.

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	TW/Pracs	Tutorial	Total
ECLDLO 6021	Digital VLSI Design Laboratory	--	02	--	--	1	--	1

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical & Oral	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test2	Avg. Of Test 1 and Test 2					
ECLDLO 6021	Digital VLSI Design Laboratory	--	--	--	--	25	--	--	25

Suggested Experiment List

1. At least **08** experiments covering entire syllabus of Digital VLSI should be set to have well predefined inference and conclusion.
2. The first 05 experiments as described below can be conducted by using Free or Professional tools
    - **01** experiments on Layouts of NAND and NOR gates to understand design rules
    - **01** experiment on Layout design of logical expression
    - **01** experiments on NAND/NOR gate implementation using at least 03 design styles
    - **02** experiment on Multiplexer/Decoder/Flip flop/Memory etc design
3. Last **03** experiments on HDL

Note: Small Project can be considered as a part of term-work.
Term Work:

At least 08 Experiments including 02 simulations covering entire syllabus must be given during the "**Laboratory session batch wise"**. Computation/simulation based experiments are also encouraged. The experiments should be students centric and attempt should be made to make experiments more meaningful, interesting and innovative. Application oriented one mini-project can be conducted for maximum batch of four students.

Term work assessment must be based on the overall performance of the student with every experiments/tutorials and mini-projects (if included) are graded from time to time. The grades will be converted to marks as per "**Choice Based Credit and Grading System"** manual and should be added and averaged. Based on above scheme grading and term work assessment should be done.

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	TW/Pracs	Tutorial	Total
ECLDLO 6022	Radar Engineering Laboratory	--	02	--	--	1	--	1

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical & Oral	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test2	Avg. Of Test 1 and Test 2					
ECLDLO 6022	Radar Engineering Laboratory	--	--	--	--	25	--	--	25

Note: Small Project can be considered as a part of term-work.

Term Work:

At least 08 Experiments including 02 simulations covering entire syllabus must be given during the "**Laboratory session batch wise"**. Computation/simulation based experiments are also encouraged. The experiments should be students centric and attempt should be made to make experiments more meaningful, interesting and innovative. Application oriented one mini-project can be conducted for maximum batch of four students.

Term work assessment must be based on the overall performance of the student with every experiments/tutorials and mini-projects (if included) are graded from time to time. The grades will be converted to marks as per "**Choice Based Credit and Grading System"** manual and should be added and averaged. Based on above scheme grading and term work assessment should be done.

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	TW/Pracs	Tutorial	Total
ECLDLO 6023	Database Management System Laboratory	--	02	--	--	1	--	1

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical & Oral	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test2	Avg. Of Test 1 and Test 2					
ECLDLO 6023	Database Management System Laboratory	--	--	--	--	25	--	--	25

Suggested Experiment List

- Design a Database and create required tables. For e.g. Bank, College Database
- Apply the constraints like Primary Key, Foreign key, NOT NULL to the tables.
- Write a sql statement for implementing ALTER, UPDATE and DELETE
- Write the queries to implement the joins
- Write the query for implementing the following functions: MAX (), MIN (), AVG (), COUNT ()
- Write the query to implement the concept of Integrity constrains
- Write the query to create the views
- Perform the queries for triggers
- Perform the following operation for demonstrating the insertion, updation and deletion using the referential integrity constraints
- Write the query for creating the users and their role

List of Mini projects:

Note: These are few examples of mini projects; teachers may prepare their own list.

1. Library Management System
2. Hospital Management System
3. Pharmacy Management System
4. Human Resource Database Management System in Java
5. Student Database Management System
6. Employee Management System
7. Inventory Control Management Database

8. Pay Roll Management System
9. Railway System Database
10. Airline Reservation System
11. Blood Donation System
12. School Management System

Online Repository Sites:

1. Google Drive
2. GitHub
3. Code Guru

Note: Small Project can be considered as a part of term-work.

Term Work:

At least 08 Experiments including 02 simulations covering entire syllabus must be given during the "**Laboratory session batch wise"**. Computation/simulation based experiments are also encouraged. The experiments should be students centric and attempt should be made to make experiments more meaningful, interesting and innovative. Application oriented one mini-project can be conducted for maximum batch of four students.

Term work assessment must be based on the overall performance of the student with every experiments/tutorials and mini-projects (if included) are graded from time to time. The grades will be converted to marks as per "**Choice Based Credit and Grading System"** manual and should be added and averaged. Based on above scheme grading and term work assessment should be done.

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	TW/Pracs	Tutorial	Total
ECLDLO 6024	Audio Processing Laboratory	--	02	--	--	1	--	1

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical & Oral	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test2	Avg. Of Test 1 and Test 2					
ECLDLO 6024	Audio Processing Laboratory	--	--	--	--	25	--	--	25

Note: Small Project can be considered as a part of term-work.

Term Work:

At least 08 Experiments including 02 simulations covering entire syllabus must be given during the "**Laboratory session batch wise"**. Computation/simulation based experiments are also encouraged. The experiments should be students centric and attempt should be made to make experiments more meaningful, interesting and innovative. Application oriented one mini-project can be conducted for maximum batch of four students.

Term work assessment must be based on the overall performance of the student with every experiments/tutorials and mini-projects (if included) are graded from time to time. The grades will be converted to marks as per "**Choice Based Credit and Grading System"** manual and should be added and averaged. Based on above scheme grading and term work assessment should be done.

Semester VIII

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned		
		Theory	Pracs	Tut	Theory	TW/ Pracs	Total
ECC801	RF Design	4	-	--	4	--	4
ECC802	Wireless Networks	4	-	-	4	-	4
ECCDLO 804X	Department Level Optional Course IV	4	-	-	4	-	4
ILO802X	Institute Level Optional Course II	3	-	-	3	-	3
ECL801	RF Design Lab	-	2	-	-	1	1
ECL802	Wireless Networks Lab	-	2	-	-	1	1
ECLDLO 804X	Department Level Optional Lab IV	-	2	-	-	1	1
ECL803	Project-II	-	12	-	-	6	6
Total		**15**	**18**	**-**	**15**	**9**	**24**

Course Code	Course Name	Examination Scheme							
		Theory					TW	Oral & Prac	Total
		Internal Assessment			End Sem Exam	Exam Duration (Hrs)			
		Test1	Test 2	Avg					
ECC801	RF Design	20	20	20	80	03	--	--	100
ECC802	Wireless Networks	20	20	20	80	03	--	--	100
ECCDLO 804X	Department Level Optional Course IV	20	20	20	80	03	--	--	100
ILO802X	Institute Level Optional Course II	20	20	20	80	03	--	--	100
ECL801	RF Design Lab	--	--	--	--	--	25	25	50
ECL802	Wireless Networks Lab	--	--	--	--	--	25	25	50
ECLDLO 804X	Department Level Optional Lab IV	--	--	--	--	--	25	25	50
ECL803	Project-II	--	--	--	--	--	100	50	150
Total				**80**	**320**		**175**	**125**	**700**

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECC801	RF Design	04	--	--	04	--	--	04

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical & Oral	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test2	Avg. Of Test 1 and Test 2					
ECC801	RF Design	20	20	20	80	--	--	--	100

Prerequisites:

- Electromagnetic Engineering
- Antenna & Radio Wave Propagation
- Communication Engineering
- Microwave Engineering

Course objectives:

- To learn RF circuit fundamentals for designing various circuit building blocks in a typical RF transceiver
- To learn importance of EMI/EMC

Course outcomes:

After successful completion of the course student will be able to

- Design impedance matching networks and passive RF filters.
- Design and appraise RF amplifiers and oscillators.
- Analyze EMI and EMC in RF circuits.

Module No.	Unit No.	Topics	Hrs.
1.0		**RF Filter Design**	**10**
	1.1	Image parameter method	
	1.2	Insertion loss method- Maximally flat low pass prototype, Equal ripple low pass prototype, Filter transformation and filter implementation	
2.0		**Amplifier Design**	**08**
	2.1	Two-port power gain stability	
	2.2	Single stage amplifier design: Design for maximum gain, design for specified gain, low noise amplifier design	
	2.3	Power amplifier design.: Characteristics of power amplifier and classes of amplifiers, design of class A power amplifier	
3.0		**Frequency Generation & Mixer**	**08**
	3.1	One-port and two-port microwave oscillator design.	
	3.2	Analysis of phase noise in oscillators.	
	3.3	Mixers: Characteristics, Various types of Mixers: Single ended diode mixers, FET mixers, Balanced mixers, Image reject mixers and other types of mixers.	
4.0		**Frequency Synthesizers**	**06**
	4.1	Direct Frequency Synthesis, Frequency Synthesis by Phase Lock, Effects of Reference Frequency on Loop Performance,	
	4.2	Variable-Modulus Dividers, Down Conversion, Methods for Reducing Switching Time, Direct Digital Synthesis, Synthesizer Design .	
	4.3	Phase Noise: A Model for Oscillator Phase Noise, Phase Noise in Phase-Locked Loops, Effect of Frequency Division and Multiplication on Phase Noise.	
5.0		**Electromagnetic Interference in RF circuits**	**08**
	5.1	Introduction. Natural and Nuclear Sources of EMI, EMI From Apparatus and Circuits. **Quantification Of Communication System EMI**	
	5.2	Elements Of Interference, Including Antennas, Transmitters, Receivers And Propagation. **Electronic Equipment And System EMI Concepts.** Examples Of EMI Coupling Modes	
	5.3	**Equipment Emissions And Susceptibilities- Types of coupling: Common-Mode Coupling:** Common-Mode Coupling Mechanisms Including Field To Cable, Ground Impedance, Ground Loop And Coupling Reduction Techniques **Differential-Mode Coupling** : Differential-Mode Coupling Mechanisms Including Field To Cable, Cable To Cable And Coupling Reduction Techniques.	
	5.4	**Other Coupling mechanisms:** Power Supplies And Victim Amplifiers	

6.0		**Electromagnetic Compatibility**	**08**
	6.1	**The Importance Of Grounding For Achieving EMC.** Grounding, Including The Reasons (I.E., Safety, Lightning Control, EMC, Grounding Schemes (Single Point, Multi-Point And Hybrid), Shield Grounding And Bonding. Shielding Effectiveness, Shielding Considerations (Reflective And Absorptive), Shielding Compromises (I.E., Apertures, Gaskets, Waveguide Beyond Cut-Off)	
	6.2	**EMI Diagnostics And Fixes:** Techniques Used In EMI Diagnostics And Fixes	
	6.3	**EMC Specifications, Standards And Measurements.** A Discussion Of The Genesis Of EMC Documentation Including A Historical Summary, The Rationale, And A Review Of MIL-Stds, FCC And CISPR Requirements.	
		Total	**48**

Text Books

1. David M Pozar, Microwave Engineering, John Wiely and Sons, 2005
2. Ludwig R. and Bogdanov G, RF Circuit Design, Prentice Hall, 2007.
3. Jack Smith, Modern Communication circuits,Tata McgrawHill.
4. W. Prasad Kodali, Engineering Electromagnetic Compatibility: Principles, Measurements, Technologies, and Computer Models, 2nd Edition, ISBN: 978-0-7803-4743-4, January 2001, Wiley-IEEE Press
5. David. A. Weston, Electromagnetic Compatibility-principles and applications, Second Edition, Publisher: Marcel Dekker, Inc. 2001, ISBN 0-8247-8889-3

References:

1. Guillermo Gonzalez, 'Microwave Transistor Amplifiers Analysis and Design', Prentice Hall, 2nd Edition.
2. Devendra Misra, 'Radio Frequency and Microwave Communication Circuits-Analysis and Design', John Wiley & Sons, 2nd Edition.
3. Clayton R. Paul, 'Electromagnetic Compatibility', John Wiley & Sons, 2nd Edition.

Internal Assessment:
Assessment consists of two class tests of 20 marks each. The first class test is to be conducted when approximately 40% syllabus is completed and second class test when additional 40% syllabus is completed. The average marks of both the test will be considered for final Internal Assessment. Duration of each test shall be of one hour.

End Semester Examination:
1. Question paper will comprise of 6 questions, each carrying 20 marks.
2. The students need to solve total 4 questions.
3. Question No.1 will be compulsory and based on entire syllabus.
4. Remaining question (Q.2 to Q.6) will be selected from all the modules.

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECC802	Wireless Networks	04	--	--	04	--	--	04

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical & Oral	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test2	Avg. Of Test 1 and Test 2					
ECC802	Wireless Networks	20	20	20	80	--	--	--	100

Prerequisites:

- Mobile Communication

Course objectives:

- Introduction to wireless Body Networks and study emerging technologies like Bluetooth and Zigbee
- To study Wireless LAN, PAN & MAN.
- Understanding Ultra Wideband communication.
- Introduction to Femtocells

Course outcomes:

After successful completion of the course student will be able to

- Explain the working of different wireless technologies like bluetooth and zigbee.
- Understand the working of wireless LAN, PAN & MAN
- Analyze the different types of Wireless Networks like LAN,PAN & MAN
- Comprehend the working of Femtocells.

Module No.	Unit No.	Topics	Hrs.
1.0		**Wireless Body Area Networks**	**12**
	1.1	Introduction to WBAN, Network Architecture, Network Components	
	1.2	Network Protocol: Physical Layer, Data Link layer, Media Access Control (MAC) Layer, Network Layer	
	1.3	**WBAN Technologies:** Bluetooth: Concept of Piconet, Scatternet, Protocol Stack Connection establishment **Zigbee:** Components, Protocol Stack, Architecture & Network Topologies	
2.0		**Wireless LAN**	**10**
	2.1	Introduction to wireless LAN, Transmission Techniques	
	2.2	Medium Access Control Protocol Issues: Hidden Terminal Problem, Reliability, Collision Avoidance, Congestion Avoidance, Congestion Control, Energy Efficiency	
	2.3	IEEE 802.11 Standard for Wireless LAN: Network Architecture, Physical Layer, MAC Layer, Security, System design and considerations	
	2.4	Enhancements to IEEE 802.11 MAC: Power Control, Spatial Reusability & QoS Provisioning	
3.0		**Wireless PAN**	**08**
	3.1	Introduction to wireless PAN, Need of Wireless PAN	
	3.2	Bluetooth Technology: History & Applications, Technical Overview, Bluetooth Specifications, Piconet Synchronization, master-slave switch, Bluetooth security.	
	3.3	Enhancements to Bluetooth: Bluetooth Interface issues, Intra & Inter Piconet Scheduling, Scatternet Formation, QoS Assignment	
	3.4	IEEE 802.15 Working group for WPAN, IEEE 802.15.3 & IEEE 802.15.4	
	3.5	Comparison between WPAN System & Comparison between WLAN & WPAN	
4.0		**Wireless MAN**	**08**
	4.1	Introduction to Wireless Metropolitan Area Networks, IEEE 802.16 Standards Advantages of IEEE 802.16	
	4.2	WMAN Network Architecture: Network Components, features of WiMAX, WiMAX Mobility Support	
	4.3	Network Protocols : Physical Layer, MAC Layer	
		WMAN Applications: Banking Networks, Educational Networks, Public Safety	
5.0		**Ultra wideband Communication**	**06**
	5.1	Introduction, UWB indoor channel, UWB capacity	

	5.2	Pulsed UWB: Pulse shape, Modulation & Multiple access techniques, Pulsed UWB transceivers,	
	5.3	Multiband UWB: Modulation of pulsed multiband UWB, MB-OFDM UWB	
6.0		**Femtocells**	**04**
	6.1	Introduction to Femtocell, Femtocell Attributes, Femtocell Standards,	
	6.2	Concept of Femtocells, Types of Femtocells	
	6.3	Applications of Femtocells.	
		Total	

Text Books & References :

1. Carlos de Morais Cordeiro, Dharma Prakash Agrawal, "AD HOC & Sensor Networks – Theory & Applications ", Cambridge University Press India Pvt. Ltd.
2. KE- LIN DU & M. N. S. Swamy, "Wireless Communication Systems", Cambridge University Press India Pvt. Ltd.
3. D. E. Comer, "Femtocells- Opportunity & Challenges for Business & Technology", Wiley Publications.
4. Dr. Sunilkumar S. Manvi, Mahabaleshwar S. kakkasageri, "Wireless & Mobile Networks: Concepts and Protocols".

Internal Assessment:
Assessment consists of two class tests of 20 marks each. The first class test is to be conducted when approximately 40% syllabus is completed and second class test when additional 40% syllabus is completed. The average marks of both the test will be considered for final Internal Assessment. Duration of each test shall be of one hour.

End Semester Examination:
1. Question paper will comprise of 6 questions, each carrying 20 marks.
2. The students need to solve total 4 questions.
3. Question No.1 will be compulsory and based on entire syllabus.
4. Remaining question (Q.2 to Q.6) will be selected from all the modules.

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECCDLO 8041	Optical Networks	04	--	--	04	--	--	04

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical & Oral	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test2	Avg. Of Test 1 and Test 2					
ECCDLO 8041	Optical Networks	20	20	20	80	--	--	--	100

Prerequisites:

- Principles of Communication Engineering
- Digital Communication
- Antenna Wave Propagation
- Optical Communication

Course objectives:

- The issues related to signal degradation due to linear impairment
- High data rate WDM optical transport networks
- Link budget and optical networks, design and management.

Course outcomes:

After successful completion of the course student will be able to

- Identify the issues related to signal degradation and multiplexing.
- Explore concepts of designing and operating principles of modern optical communication systems and networks.
- Apply the knowledge developed in-class to contemporary optical fiber communication research and industrial areas.

Module No.	Unit No.	Topics	Hrs.
1.0		**Introduction to Optical Components and Networks**	**06**
	1.1	OPTICAL Components - Couplers, Isolators and Circulators, Multiplexes and Filters Optical Amplifiers. Transmitters, Detectors, Switches, Wavelength Converters	
	1.2	OPTICAL Networks - Telecommunication networks, First generation optical networks, Multiplexing techniques, Second generation optical networks, System and network evolution	
2.0		**Optical Networks Architecture**	**08**
	2.1	SONET/SDH, Computer interconnects, MANS, Layered architecture for SONET and second generation networks.	
	2.2	Broadcast and Select Networks – Topologies for Broadcast Networks, Media-Access Control Protocols,	
	2.3	Operational principle of WDM, WDM network elements and Architectures, Introduction to DWDM, Solitons	
3.0		**Packet Switching and Access Networks**	**08**
	3.1	Photonic Packet Switching – OTDM, Multiplexing and Demultiplexing,	
	3.2	Synchronization, Broadcast OTDM networks, Switch-based networks	
	3.3	Access Networks – Network Architecture overview, Future Access Networks,	
	3.4	Optical Access Networks Architectures; and OTDM networks	
4.0		**Wavelength Routing Networks**	**10**
	4.1	Optical layer, Node design, Network design and operation, routing and wavelength assignment architectural variations	
	4.2	Optical Network Routing Principles - Impairment Aware Routing Optical Circuit Switching ,Optical Packet Switching Optical Burst Switching	
	4.3	Energy Awareness in Optical Networking ,Network Modeling Tools Network Design Guidelines	
5.0		**Design of Optical Networks**	**10**
	5.1	Core Optical Networks, Metro Optical networks, Access Optical Networks	
	5.2	Wavelength Routing and Assignment, Traffic Grooming and Protection, Multilayer Network Structure	
	5.3	Transmission system model, power penalty-transmitter, receiver optical amplifiers, crosstalk, dispersion, wavelength stabilization	
6.0		**Virtual topology, Network Control and Management**	**06**
	6.1	Virtual topology design problem, Combines SONET/WDM network design, an ILP formulation, Regular virtual topologies,	

	6.2	Control and management, Network management configuration management, Performance management, fault management. Network management functions, Optical safety	
		Total	**48**

Text Books :

1. Kumar Sivarajan and Rajiv Ramaswamy, Morgan Kauffman, Optical Networks: A Practical Perspective, Elsevier Publication Elsevier India Pvt. Ltd, 3rd Edition, 2010.
2. Harry G. Parros, Communication Oriented Networks, Wiley
3. G. Agrwal, Fiber Optic Communication Systems, John Wiley and Sons, 3rd Edition, New York, 2014.

References:

1. C. Siva Ram Moorthy and Mohan Gurusamy, WDM Optical Networks: Concept, Design and Algorithms, Prentice Hall of India, 1st Edition, 2002.
2. Biswajit Mukherjee, Optical Communication Networks, TMG1998.
3. Jane M. Simoons, Optical Network Design and Planning, Second Edition, Springer
4. Ulysees Black, Optical Networks, Pearson education 2007.
5. Milorad Cvijetic, Ivan B. Djordjevic, Advanced Optical Communication Systems and Networks, Artech House Applied Photonics, 2012.

Internal Assessment:
Assessment consists of two class tests of 20 marks each. The first class test is to be conducted when approximately 40% syllabus is completed and second class test when additional 40% syllabus is completed. The average marks of both the test will be considered for final Internal Assessment. Duration of each test shall be of one hour.

End Semester Examination:
1. Question paper will comprise of 6 questions, each carrying 20 marks.
2. The students need to solve total 4 questions.
3. Question No.1 will be compulsory and based on entire syllabus.
4. Remaining question (Q.2 to Q.6) will be selected from all the modules.

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECCDLO 8042	Advanced Digital Signal Processing	04	--	--	04	--	--	04

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical & Oral	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test2	Avg. Of Test 1 and Test 2					
ECCDLO 8042	Advanced Digital Signal Processing	20	20	20	80	--	--	--	100

Prerequisites:

- Discrete Time Signal Processing

Course objectives:

The aim of this course is to provide in-depth treatment on methods and techniques on

- Multirate Signal Processing, Power Spectrum Estimation, Adaptive Filtering and Wavelet Transform.
- Application of signal processing to real world problems.

Course outcomes:

After successful completion of the course student will be able to

- Demonstrate an understanding of multirate sampling and its mechanism.
- Study and apply the techniques of power spectrum estimation and wavelet theory for various applications.
- Implement adaptive filters for given applications.
- Apply signal processing tools to Biomedical and Telecommunication Applications

Module No.	Unit No.	Topics	Hrs.
1.0		**Multirate Digital Signal Processing**	**08**
	1.1	Advantages of Multirate Signal Processing	
	1.2	Interpolation and Decimation	
	1.3	Sampling Rate Conversion by Non Integer Factor	
	1.4	Multistage Interpolation and Decimation	
	1.5	Polyphase Decomposition	
	1.6	Digital Filter Banks	
	1.7	Applications of Multirate Signal Processing	
2.0		**Power Spectrum Estimation**	**10**
	2.1	**Non Parametric Method of Power Spectrum Estimation:** Periodogram, Modified Periodogram, Barlett Method, Welch's Method, Blackman-Tukey Approach	
	2.2	**Parametric Methods of Power Spectrum Estimation:** Auto Regressive Spectrum Estimation, Model Parameters-Yule-Walker Equation, Least Square Method and Linear Prediction, Moving Average Spectrum Estimation, Autoregressive Moving Average Spectrum Estimation	
	2.3	Eigen Analysis Algorithm for Spectrum Estimation	
3.0		**Linear Prediction and Optimum Linear Filters**	**10**
	3.1	Representation of Stationary Random Process	
	3.2	Forward and Backward Linear Prediction	
	3.3	Solution of Normal Equation(Levinson-Durbin and Schur Algorithm)	
	3.4	AR Lattice and ARMA Lattice Ladder Filters	
	3.5	Weiner Filters for Filtering and Prediction	
4.0		**Adaptive Filters**	**10**
	4.1	**Applications of Adaptive Filters:** System Identification, Adaptive Channel Equalization, Echo Cancellation, Adaptive Noise Cancellation, Suppression of Narrowband Interference in Wideband Signals, Adaptive Arrays.	
	4.2	**Adaptive Algorithms:** LMS Algorithm, RLS Algorithm, Lattice Ladder Algorithm	
5.0		**Wavelet Transform**	**06**
	5.1	Introduction to Time Frequency Analysis	
	5.2	Short Time Fourier Transform	
	5.3	Continuous Wavelet Transform	
	5.4	Discrete Wavelet Transform	
	5.5	Multiresolution Analysis	

	5.6	Applications	
6.0		**Application Of Signal Processing**	**04**
	6.1	Biomedical Applications	
	6.2	Audio Applications	
	6.3	Telecommunication Applications(Radar)	
		Total	**48**

Textbooks

1. John G. Proakis, Dimitris G. Monolakis " Digital Signal Processing", PHI 2007.
2. Emmanuel C. Ifeachor, Barrie W. Jervis, "Digital Signal Processing A Practical Approach", Pearson Education 2008.

Reference Books

1. Simon Haykin, "Adaptive Filter Theory", Pearson Education 2013.
2. Tarun Kumar Rawat, "Digital Signal Processing", Oxford University Press.
3. Raghuveer M. Rao and Ajit S. Bopardikar, "Wavelet Transforms", "Introduction to Theory and Applications", Pearson Education Asia 2000.

Internal Assessment:
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End Semester Examination:
1. Question paper will comprise of 6 questions, each carrying 20 marks.
2. The students need to solve total 4 questions.
3. Question No.1 will be compulsory and based on entire syllabus.
4. Remaining question (Q.2 to Q.6) will be selected from all the modules.

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECCDLO 8043	Satellite Communication	04	--	--	04	--	--	04

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical & Oral	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test2	Avg. Of Test 1 and Test 2					
ECCDLO 8043	Satellite Communication	20	20	20	80	--	--	--	100

Prerequisites:

- Analog Communication
- Digital Communication

Course objectives:

- To understand the basics of satellite communications and different satellite communication orbits
- Provide an in-depth understanding of satellite communication system operation, launching techniques, satellite link design and earth station technology
- To explain the tools necessary for the calculation of basic parameters in a satellite communication system.
- Review the state of the art in new research areas such as speech and video coding, satellite networking and satellite personal communications, mobile satellite communication, Laser satellite

Course outcomes:

After successful completion of the course student will be able to

- Explain basics of satellite communication, space segment and earth segment
- Understand different satellite orbits and orbital parameters
- Explain and analyze link budget of satellite signal for proper communication
- Understand various applications of satellite communications

Module No.	Unit No.	Topics	Hrs.
1.0		**Overview of Satellite Systems, Orbits and Launching**	**09**
	1.1	An overview of space and satellite, Frequency allocation for satellite communication, Polar orbiting satellites, Kepler's First, second and third law, orbital elements, apogee, perigee heights, orbital perturbations, effects of a non-spherical earth, atmospheric drag	
	1.2	Wave Propagation & Polarization, Atmospheric Losses, Ionospheric Effects, Rain Attenuation, Other impairments, Antenna Polarization, Polarization of Satellite signals, Cross polarization discrimination, Ionospheric depolarization, Rain depolarization, Ice depolarization	
	1.3	Sub-satellite Point, predicting satellite position, antenna look angels, polar mount antenna, limits of visibility, near geostationary orbits, earth eclipse of satellite, sun transit outage	
	1.4	Selection of launching site, launch window, zero and non-zero degree latitude launching, sea launch, launch vehicles; satellite launch vehicle (SLV), augmented satellite launch vehicle (ASLV), polar SLV, geostationary satellite launch vehicle (GSLV)	
2.0		**Space Segment**	**06**
	2.1	Satellite configuration, Transponder sub-system, Antenna sub-system, AOC Sub-system, TT&C Sub-system, power sub-system, Thermal sub-system, reliability and quality Assurance.	
3.0		**Earth station**	**05**
	3.1	Design consideration	
	3.2	General configuration- Block diagram, Receive only type earth, transmit-receive type earth station, Antenna system, Feed system, Tracking system, LNA, HPA	
4.0		**Satellite Links**	**10**
	4.1	Isotropic radiated power, transmission losses, free-space transmission, feeder losses, antenna misalignment losses, fixed atmospheric and ionospheric losses, link power budget	
	4.2	System noise, antenna noise, amplifier noise temperature, amplifiers in cascade, noise factor, noise temperature of absorptive networks, overall system noise temperature, carrier to noise ratio	
	4.3	Uplink: Saturation flux density, input back off, earth station HPA, Downlink: Output back off, satellite TWTA output	
	4.4	Effects of rain, uplink rain-fade margin, downlink rain-fade margin, combined uplink and downlink C/N ratio, inter-modulation noise	
5.0		**The Space Segment Access and Utilization**	**08**
	5.1	Space segment access methods, pre-assigned FDMA, demand assigned FDMA, SPADE system, bandwidth-limited and power-limited TWT amplifier operation	

	5.2	TDMA: Reference Burst; Preamble and Postamble, carrier recovery, network synchronization, unique word detection, traffic date, frame efficiency, channel capacity, preassigned TDMA, demand assigned TDMA, satellite switched TDMA	
	5.3	Code Division Multiple Access: Direct-sequence spread spectrum–acquisition and tracking, spectrum spreading and dispreading – CDMA throughput	
6.0		**Satellite Applications**	**10**
	6.1	VSAT systems: Advantages, configurations, frequency bands, elements, Applications	
	6.2	Broadcast services: Television broadcast systems, DAB,	
	6.3	Mobile satellite communication: INMARSAT, LMSS, mobile satellite systems with non GEO satellites	
	6.4	Satellite navigation systems	
	6.5	Laser Satellite Communication: Link analysis, optical satellite link transmitter, optical satellite link receiver, satellite beam acquisition, tracking & positioning, deep space optical communication link	
	6.6	Recent applications	
	6.7	Modern development and future trends	
		Total	**48**

Text Books & References :

1. Dennis Roddy, “Satellite Communications”, 4th Ed., Mc. Graw-Hill International Ed. 2009.
2. M. Richharia, “Satellite Communication Systems Design Principles”, Macmillan Press Ltd. Second Edition 2003.
3. R. N. Mutangi, “ Satellite Communication”, Oxford university press, 2016.
4. Gerard Maral and Michel Bousquet, “Satellite Communication Systems”, 4th Edition Wiley Publication
5. Gerard Maral, “VSAT Networks”, John Willy & Sons
6. Timothy Pratt, Charles Bostian, and Jeremy Allmuti, “Satellite Communications”, John Willy & Sons (Asia) Pvt. Ltd. 2004
7. Wilbur L. Pritchard, Henri G. Suyderehoud, and Robert A. Nelson, “Satellite Communication systems Engineering”, Pearson Publication

Internal Assessment:
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Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECCDLO 8044	Network Management in TeleCommunication	04	--	--	04	--	--	04

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical & Oral	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test2	Avg. Of Test 1 and Test 2					
ECCDLO 8044	Network Management in TeleCommunication	20	20	20	80	--	--	--	100

Prerequisites:

- Computer Communication and Networks,
- Operating System
- Basic Programming skills

Course objectives:

- To understand the concept of Telecommunication, network management, architecture and protocol

Course outcomes:

After successful completion of the course student will be able to

- Explain the need for interoperable network management & analyze the trends and development of the Telecommunications Network Management.
- Demonstrate broad knowledge of fundamental principles and technical standards underlying.
- Describe the concepts and architecture behind standards based network management associated with SNMP and CMIP.
- Apply basic of telecommunication, networking and information technologies and architect and implement networked informative systems.
- Continuously improve their technology knowledge and communication skills.

Module No.	Unit No.	Topics	Hrs.
1.0		**Introduction of Network Management**	**10**
	1.1	Introducing Network Design Concepts: Network designers ensure that our communications networks can adjust and scale to the demands for new services. To support our network-based economy, designers must work to create networks that are available nearly 100 percent of the time. Challenges of IT managers.	
	1.2	Network Management: Goals, organization and functions	
	1.3	Network management architecture and organization network management perspectives	
2.0		**OSI Network Management**	**04**
	2.1	Network management standards	
	2.2	Network management models	
	2.3	Organization model	
	2.4	Information model	
	2.5	Communication model and functional model	
	2.6	Abstract syntax notation – encoding structure, macros functional model CMIP/CMISE	
3.0		**Internet Management**	**12**
	3.1	SNMP-organizational model-	
	3.2	System overview.	
	3.3	Information model, communication model, functional model	
	3.4	SNMP proxy server, Management information, Protocol	
	3.5	SNMPv1,v2 and V3	
	3.6	Remote monitoring. RMON	
4.0		**Telecommunication Management Networks(TMN)**	**04**
	4.1	Need for TMN , Conceptual TNM model	
	4.2	TMN Network Management Architecture	
	4.3	TMN management services architecture and TMN implementation	
5.0		**Network Management Tools and Applications**	**12**
	5.1	System Utilities for network management	
	5.2	Network statistics and measurements	
	5.3	NMS Design, NMS components, NMS Server Architecture	
	5.4	Network Management Systems and FCAPS	
	5.5	Automatic Fault Management and Event correlation Techniques	
	5.6	Security Management	
6.0		**Broadband Network Management**	**06**
	6.1	Broadband networks and services, ATM Technology – VP, VC, ATM Packet, Integrated service, ATM LAN emulation, Virtual LAN	

	6.2	ATM Network Management – ATM network reference model, integrated local management interface. ATM management management information base, role of SNMP and ILMI in ATM.	
	6.3	M1, M2, M3, M4 interface. ATM digital exchange interface management	
		Total	**48**

Text Books & References :

1. Mani Subramaniam, ―Network Management Principles and Practise", Addison Wisely, New York, 2000.
2. Designing and Supporting Computer Networks, CCNA Discovery Learning Guide By Kenneth Stewart, Aubrey Adams, Allan Reid, Jim Lorenz, Cisco Press
3. Network Management: Concepts and Practice, A Hands-On Approach by J. Richard Burke, Pearson Publications.
4. Network Management: Accounting and Performance Strategies by Benoit Claise - CCIE No. 2686; Ralf Wolter CISCO Press
5. Network Management Fundamentals, Alexander Clemm, Cisco Press, December 2006, ISBN-13: 978-158720137
6. Python for Software Design by Allen B. Downey, Cambridge University Press, March 2009, ISBN-13: 978-0521725965. A free manuscript is available at the author's website.

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Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECCILO 8021	Project Management	03	--	--	03	--	--	03

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical & Oral	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test2	Avg. Of Test 1 and Test 2					
ECCILO 8021	Project Management	20	20	20	80	--	--	--	100

Course objectives:

- To familiarize the students with the use of a structured methodology/approach for each and every unique project undertaken, including utilizing project management concepts, tools and techniques.
- To appraise the students with the project management life cycle and make them knowledgeable about the various phases from project initiation through closure.

Course outcomes:

After successful completion of the course student will be able to

- Apply selection criteria and select an appropriate project from different options.
- Write work break down structure for a project and develop a schedule based on it.
- Identify opportunities and threats to the project and decide an approach to deal with them strategically.
- Use Earned value technique and determine & predict status of the project.
- Capture lessons learned during project phases and document them for future reference

Module No.	Unit No.	Topics	Hrs.
1.0		**Project Management Foundation**	**05**
	1.1	Definition of a project, Project Vs Operations, Necessity of project management, Triple constraints, Project life cycles (typical & atypical) Project phases and stage gate process. Role of project manager, Negotiations and resolving conflicts, Project management in various organization structures, PM knowledge areas as per Project Management Institute (PMI)	
2.0		**Initiating Projects**	**06**
	2.1	How to get a project started, Selecting project strategically, Project selection models (Numeric /Scoring Models and Non-numeric models), Project portfolio process, Project sponsor and creating charter; Project proposal. Effective project team, Stages of team development & growth (forming, storming, norming &performing), team dynamics.	
3.0		**Project Planning and Scheduling**	**08**
	3.1	Work Breakdown structure (WBS) and linear responsibility chart, Interface Co-ordination and concurrent engineering, Project cost estimation and budgeting, Top down and bottoms up budgeting, Networking and Scheduling techniques. PERT, CPM, GANTT chart, Introduction to Project Management Information System (PMIS).	
4.0		**Planning Projects**	**06**
	4.1	Crashing project time, Resource loading and levelling, Goldratt's critical chain, Project Stakeholders and Communication plan Risk Management in projects: Risk management planning, Risk identification and risk register, Qualitative and quantitative risk assessment, Probability and impact matrix. Risk response strategies for positive and negative risks	
5.0			**08**
	5.1	**Executing Projects:** Planning monitoring and controlling cycle, Information needs and reporting, engaging with all stakeholders of the projects, Team management, communication and project meetings	
	5.2	**Monitoring and Controlling Projects:** Earned Value Management techniques for measuring value of work completed; Using milestones for measurement; change requests and scope creep, Project audit	
	5.3	**Project Contracting** Project procurement management, contracting and outsourcing,	
6.0			**06**
	6.1	**6.1 Project Leadership and Ethics:** Introduction to project leadership, ethics in projects, Multicultural and virtual projects	

	6.2	**Closing the Project:** Customer acceptance; Reasons of project termination, Various types of project terminations (Extinction, Addition, Integration, Starvation), Process of project termination, completing a final report; doing a lessons learned analysis; acknowledging successes and failures; Project management templates and other resources; Managing without authority; Areas of further study.	
		Total	**39**

References:

1. Project Management: A managerial approach, Jack Meredith & Samuel Mantel, 7th Edition, Wiley India
2. A Guide to the Project Management Body of Knowledge (PMBOK® Guide), 5th Ed, Project Management Institute PA, USA
3. Project Management, Gido Clements, Cengage Learning
4. Project Management, Gopalan, Wiley India
5. Project Management, Dennis Lock, 9th Edition, Gower Publishing England

Internal Assessment:
Assessment consists of two class tests of 20 marks each. The first class test is to be conducted when approximately 40% syllabus is completed and second class test when additional 40% syllabus is completed. The average marks of both the test will be considered for final Internal Assessment. Duration of each test shall be of one hour.

End Semester Examination:
1. Question paper will comprise of 6 questions, each carrying 20 marks.
2. The students need to solve total 4 questions.
3. Question No.1 will be compulsory and based on entire syllabus.
4. Remaining question (Q.2 to Q.6) will be selected from all the modules.

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECCILO 8022	Finance Management	03	--	--	03	--	--	03

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical & Oral	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test2	Avg. Of Test 1 and Test 2					
ECCILO 8022	Finance Management	20	20	20	80	--	--	--	100

Course objectives:

- Overview of Indian financial system, instruments and market
- Basic concepts of value of money, returns and risks, corporate finance, working capital and its management
- Knowledge about sources of finance, capital structure, dividend policy

Course outcomes:

After successful completion of the course student will be able to

- Understand Indian finance system and corporate finance
- Take investment, finance as well as dividend decisions

Module No.	Unit No.	Topics	Hrs.
1.0			**06**
	1.1	**Overview of Indian Financial System** Characteristics, Components and Functions of Financial System. **Financial Instruments:** Meaning, Characteristics and Classification of Basic Financial Instruments — Equity Shares, Preference Shares, Bonds-Debentures, Certificates of Deposit, and Treasury Bills. **Financial Markets:** Meaning, Characteristics and Classification of Financial Markets — Capital Market, Money Market and Foreign Currency Market **Financial Institutions:** Meaning, Characteristics and Classification of Financial Institutions — Commercial Banks, Investment-Merchant Banks and Stock Exchanges	
2.0			**06**
	2.1	**Concepts of Returns and Risks:** Measurement of Historical Returns and Expected Returns of a Single Security and a Two-security Portfolio; Measurement of Historical Risk and Expected Risk of a Single Security and a Two-security Portfolio. **Time Value of Money:** Future Value of a Lump Sum, Ordinary Annuity, and Annuity Due; Present Value of a Lump Sum, Ordinary Annuity, and Annuity Due; Continuous Compounding and Continuous Discounting.	
3.0			**09**
	3.1	**Overview of Corporate Finance:** Objectives of Corporate Finance; Functions of Corporate Finance—Investment Decision, Financing Decision, and Dividend Decision. **Financial Ratio Analysis:** Overview of Financial Statements—Balance Sheet, Profit and Loss Account, and Cash Flow Statement; Purpose of Financial Ratio Analysis; Liquidity Ratios; Efficiency or Activity Ratios; Profitability Ratios; Capital Structure Ratios; Stock Market Ratios; Limitations of Ratio Analysis.	
4.0			**10**
	4.1	**Capital Budgeting:** Meaning and Importance of Capital Budgeting; Inputs for Capital Budgeting Decisions; Investment Appraisal Criterion—Accounting Rate of Return, Payback Period, Discounted Payback Period, Net Present Value(NPV), Profitability Index, Internal Rate of Return (IRR), and Modified Internal Rate of Return (MIRR) **Working Capital Management:** Concepts of Meaning Working Capital; Importance of Working Capital Management; Factors Affecting an Entity's Working Capital Needs; Estimation of Working Capital Requirements; Management of Inventories; Management of Receivables; and Management of Cash and Marketable Securities.	

5.0			**05**
	5.1	**Sources of Finance:** Long Term Sources—Equity, Debt, and Hybrids; Mezzanine Finance; Sources of Short Term Finance—Trade Credit, Bank Finance, Commercial Paper; Project Finance. **Capital Structure:** Factors Affecting an Entity's Capital Structure; Overview of Capital Structure Theories and Approaches— Net Income Approach, Net Operating Income Approach; Traditional Approach, and Modigliani-Miller Approach. Relation between Capital Structure and Corporate Value; Concept of Optimal Capital Structure	
6.0			**03**
	6.1	**Dividend Policy:** Meaning and Importance of Dividend Policy; Factors Affecting an Entity's Dividend Decision; Overview of Dividend Policy Theories and Approaches—Gordon's Approach, Walter's Approach, and Modigliani-Miller Approach	
		Total	**39**

References:

1. Fundamentals of Financial Management, 13th Edition (2015) by Eugene F. Brigham and Joel F. Houston; Publisher: Cengage Publications, New Delhi.
2. Analysis for Financial Management, 10th Edition (2013) by Robert C. Higgins; Publishers: McGraw Hill Education, New Delhi.
3. Indian Financial System, 9th Edition (2015) by M. Y. Khan; Publisher: McGraw Hill Education, New Delhi.
4. Financial Management, 11th Edition (2015) by I. M. Pandey; Publisher: S. Chand (G/L) & Company Limited, New Delhi.

Internal Assessment:
Assessment consists of two class tests of 20 marks each. The first class test is to be conducted when approximately 40% syllabus is completed and second class test when additional 40% syllabus is completed. The average marks of both the test will be considered for final Internal Assessment. Duration of each test shall be of one hour.

End Semester Examination:
1. Question paper will comprise of 6 questions, each carrying 20 marks.
2. The students need to solve total 4 questions.
3. Question No.1 will be compulsory and based on entire syllabus.
4. Remaining question (Q.2 to Q.6) will be selected from all the modules.

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECCILO 8023	Entrepreneurship Development and Management	03	--	--	03	--	--	03

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical & Oral	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test2	Avg. Of Test 1 and Test 2					
ECCILO 8023	Entrepreneurship Development and Management	20	20	20	80	--	--	--	100

Course objectives:

- To acquaint with entrepreneurship and management of business
- Understand Indian environment for entrepreneurship
- Idea of EDP, MSME

Course outcomes:

After successful completion of the course student will be able to

- Understand the concept of business plan and ownerships
- Interpret key regulations and legal aspects of entrepreneurship in India
- Understand government policies for entrepreneurs

Module No.	Unit No.	Topics	Hrs.
1.0			**04**
	1.1	**Overview Of Entrepreneurship:** Definitions, Roles and Functions/Values of Entrepreneurship, History of Entrepreneurship Development, Role of Entrepreneurship in the National Economy, Functions of an Entrepreneur, Entrepreneurship and Forms of Business Ownership Role of Money and Capital Markets in Entrepreneurial Development: Contribution of Government Agencies in Sourcing information for Entrepreneurship	
2.0			**09**
	2.1	**Business Plans And Importance Of Capital To Entrepreneurship:** Preliminary and Marketing Plans, Management and Personnel, Start-up Costs and Financing as well as Projected Financial Statements, Legal Section, Insurance, Suppliers and Risks, Assumptions and Conclusion, Capital and its Importance to the Entrepreneur **Entrepreneurship And Business Development:** Starting a New Business, Buying an Existing Business, New Product Development, Business Growth and the Entrepreneur Law and its Relevance to Business Operations	
3.0			**05**
	3.1	Women's Entrepreneurship Development, Social entrepreneurship-role and need, EDP cell, role of sustainability and sustainable development for SMEs, case studies, exercises	
4.0			**08**
	4.1	**Indian Environment for Entrepreneurship:** key regulations and legal aspects , MSMED Act 2006 and its implications, schemes and policies of the Ministry of MSME, role and responsibilities of various government organisations, departments, banks etc., Role of State governments in terms of infrastructure developments and support etc., Public private partnerships, National Skill development Mission, Credit Guarantee Fund, PMEGP, discussions, group exercises etc	
5.0			**08**
	5.1	**Effective Management of Business:** Issues and problems faced by micro and small enterprises and effective management of M and S enterprises (risk management, credit availability, technology innovation, supply chain management, linkage with large industries), exercises, e-Marketing	
6.0			**05**
	6.1	**Achieving Success In The Small Business:** Stages of the small business life cycle, four types of firm-level growth strategies, Options – harvesting or closing small business Critical Success factors of small business	
		Total	**39**

References:

1. Education Robert D Hisrich, Michael P Peters, Dean A Shapherd, Entrepreneurship, latest edition, The McGrawHill Company
2. T N Chhabra, Entrepreneurship Development, Sun India Publications, New Delhi
3. C N Prasad, Small and Medium Enterprises in Global Perspective, New century Publications, New Delhi
4. Vasant Desai, Entrepreneurial development and management, Himalaya Publishing House
5. Maddhurima Lall, Shikah Sahai, Entrepreneurship, Excel Books
6. Poornima Charantimath, Entrepreneurship development- Small Business Enterprise, Pearson
7. Rashmi Bansal, STAY hungry STAY foolish, CIIE, IIM Ahmedabad
8. Law and Practice relating to Micro, Small and Medium enterprises, Taxmann Publication Ltd.
9. Kurakto, Entrepreneurship- Principles and Practices, Thomson Publication
10. Laghu Udyog Samachar
11. www.msme.gov.in
12. www.dcmesme.gov.in
13. www.msmetraining.gov.in

Internal Assessment:
Assessment consists of two class tests of 20 marks each. The first class test is to be conducted when approximately 40% syllabus is completed and second class test when additional 40% syllabus is completed. The average marks of both the test will be considered for final Internal Assessment. Duration of each test shall be of one hour.

End Semester Examination:
1. Question paper will comprise of 6 questions, each carrying 20 marks.
2. The students need to solve total 4 questions.
3. Question No.1 will be compulsory and based on entire syllabus.
4. Remaining question (Q.2 to Q.6) will be selected from all the modules.

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECCILO 8024	Human Resource Management	03	--	--	03	--	--	03

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical & Oral	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test2	Avg. Of Test 1 and Test 2					
ECCILO 8024	Human Resource Management	20	20	20	80	--	--	--	100

Course objectives:

- To introduce the students with basic concepts, techniques and practices of the human resource management
- To provide opportunity of learning Human resource management (HRM) processes, related with the functions, and challenges in the emerging perspective of today's organizations
- To familiarize the students about the latest developments, trends & different aspects of HRM
- To acquaint the student with the importance of inter-personal & inter-group behavioural skills in an organizational setting required for future stable engineers, leaders and managers

Course outcomes:

After successful completion of the course student will be able to

- Understand the concepts, aspects, techniques and practices of the human resource management.
- Understand the Human resource management (HRM) processes, functions, changes and challenges in today's emerging organizational perspective.
- Gain knowledge about the latest developments and trends in HRM.
- Apply the knowledge of behavioural skills learnt and integrate it with in inter personal and intergroup environment emerging as future stable engineers and managers.

Module No.	Unit No.	Topics	Hrs.
1.0		**Introduction to HR**	**05**
	1.1	Human Resource Management- Concept, Scope and Importance, Interdisciplinary Approach Relationship with other Sciences, Competencies of HR Manager, HRM functions	
	1.2	Human resource development (HRD): changing role of HRM – Human resource Planning, Technological change, Restructuring and rightsizing, Empowerment, TQM, Managing ethical issues	
2.0		**Organizational Behaviour (OB)**	**07**
	2.1	Introduction to OB Origin, Nature and Scope of Organizational Behaviour, Relevance to Organizational Effectiveness and Contemporary issues	
	2.2	Personality: Meaning and Determinants of Personality, Personality development, Personality Types, Assessment of Personality Traits for Increasing Self Awareness	
	2.3	Perception: Attitude and Value, Effect of perception on Individual Decision-making, Attitude and Behaviour	
	2.4	Motivation: Theories of Motivation and their Applications for Behavioural Change (Maslow, Herzberg, McGregor)	
	2.5	Group Behaviour and Group Dynamics: Work groups formal and informal groups and stages of group development, Team Effectiveness: High performing teams, Team Roles, cross functional and self-directed team	
	2.6	Case study	
3.0		**Organizational Structure &Design**	**06**
	3.1	Structure, size, technology, Environment of organization; Organizational Roles & conflicts: Concept of roles; role dynamics; role conflicts and stress.	
	3.2	Leadership: Concepts and skills of leadership, Leadership and managerial roles, Leadership styles and contemporary issues in leadership.	
	3.3	Power and Politics: Sources and uses of power; Politics at workplace, Tactics and strategies.	
4.0		**Human resource Planning**	**05**
	4.1	Recruitment and Selection process, Job-enrichment, Empowerment - Job-Satisfaction, employee morale	
	4.2	Performance Appraisal Systems: Traditional & modern methods, Performance Counselling, Career Planning	
	4.3	Training & Development: Identification of Training Needs, Training Methods	
5.0		**Emerging Trends in HR**	**06**

	5.1	Organizational development; Business Process Re-engineering (BPR), BPR as a tool for organizational development , managing processes & transformation in HR. Organizational Change, Culture, Environment	
	5.2	Cross Cultural Leadership and Decision Making**:** Cross Cultural Communication and diversity at work**,** Causes of diversity, managing diversity with special reference to handicapped, women and ageing people, intra company cultural difference in employee motivation	
6.0			**10**
	6.1	**HR & MIS:** Need, purpose, objective and role of information system in HR, Applications in HRD in various industries (e.g. manufacturing R&D, Public Transport, Hospitals, Hotels and service industries	
	6.2	**Strategic HRM:** Role of Strategic HRM in the modern business world, Concept of Strategy, Strategic Management Process, Approaches to Strategic Decision Making; Strategic Intent – Corporate Mission, Vision, Objectives and Goals	
	6.3	**Labor Laws & Industrial Relations:** Evolution of IR, IR issues in organizations, Overview of Labor Laws in India; Industrial Disputes Act, Trade Unions Act, Shops and Establishments Act	
		Total	**39**

References:

1. Stephen Robbins, Organizational Behavior, 16th Ed, 2013
2. V S P Rao, Human Resource Management, 3rd Ed, 2010, Excel publishing
3. Aswathapa, Human resource management: Text & cases, 6th edition, 2011
4. C. B. Mamoria and S V Gankar, Dynamics of Industrial Relations in India, 15th Ed, 2015, Himalaya Publishing, 15thedition, 2015
5. P. Subba Rao, Essentials of Human Resource management and Industrial relations, 5th Ed, 2013, Himalaya Publishing
6. Laurie Mullins, Management & Organizational Behavior, Latest Ed, 2016, Pearson Publications

Internal Assessment:
Assessment consists of two class tests of 20 marks each. The first class test is to be conducted when approximately 40% syllabus is completed and second class test when additional 40% syllabus is completed. The average marks of both the test will be considered for final Internal Assessment. Duration of each test shall be of one hour.

End Semester Examination:
1. Question paper will comprise of 6 questions, each carrying 20 marks.
2. The students need to solve total 4 questions.
3. Question No.1 will be compulsory and based on entire syllabus.
4. Remaining question (Q.2 to Q.6) will be selected from all the modules.

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECCILO 8025	Professional Ethics and Corporate Social Responsibility (CSR)	03	--	--	03	--	--	03

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical & Oral	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test2	Avg. Of Test 1 and Test 2					
ECCILO 8025	Professional Ethics and Corporate Social Responsibility (CSR)	20	20	20	80	--	--	--	100

Course objectives:

- To understand professional ethics in business
- To recognized corporate social responsibility

Course outcomes:

After successful completion of the course student will be able to

- Understand rights and duties of business
- Distinguish different aspects of corporate social responsibility
- Demonstrate professional ethics
- Understand legal aspects of corporate social responsibility

Module No.	Unit No.	Topics	Hrs.
1.0			**04**
	1.1	**Professional Ethics and Business :** The Nature of Business Ethics; Ethical Issues in Business; Moral Responsibility and Blame; Utilitarianism: Weighing Social Costs and Benefits; Rights and Duties of Business	
2.0			**08**
	2.1	**Professional Ethics in the Marketplace:** Perfect Competition; Monopoly Competition; Oligopolistic Competition; Oligopolies and Public Policy **Professional Ethics and the Environment:** Dimensions of Pollution and Resource Depletion; Ethics of Pollution Control; Ethics of Conserving Depletable Resources	
3.0			**06**
	3.1	**Professional Ethics of Consumer Protection:** Markets and Consumer Protection; Contract View of Business Firm's Duties to Consumers; Due Care Theory; Advertising Ethics; Consumer Privacy **Professional Ethics of Job Discrimination:** Nature of Job Discrimination; Extent of Discrimination; Reservation of Jobs.	
4.0			**05**
	4.1	**Introduction to Corporate Social Responsibility:** Potential Business Benefits—Triple bottom line, Human resources, Risk management, Supplier relations; Criticisms and concerns—Nature of business; Motives; Misdirection. Trajectory of Corporate Social Responsibility in India	
5.0			**08**
	5.1	**Corporate Social Responsibility:** Articulation of Gandhian Trusteeship Corporate Social Responsibility and Small and Medium Enterprises (SMEs) in India, Corporate Social Responsibility and Public-Private Partnership (PPP) in India	
6.0			**08**
	6.1	**Corporate Social Responsibility in Globalizing India:** Corporate Social Responsibility Voluntary Guidelines, 2009 issued by the Ministry of Corporate Affairs, Government of India, Legal Aspects of Corporate Social Responsibility—Companies Act, 2013.	
		Total	**39**

References:

1. Business Ethics: Texts and Cases from the Indian Perspective (2013) by Ananda Das Gupta; Publisher: Springer.

2. Corporate Social Responsibility: Readings and Cases in a Global Context (2007) by Andrew Crane, Dirk Matten, Laura Spence; Publisher: Routledge.
3. Business Ethics: Concepts and Cases, 7th Edition (2011) by Manuel G. Velasquez; Publisher: Pearson, New Delhi.
4. Corporate Social Responsibility in India (2015) by Bidyut Chakrabarty, Routledge, New Delhi.

Internal Assessment:
Assessment consists of two class tests of 20 marks each. The first class test is to be conducted when approximately 40% syllabus is completed and second class test when additional 40% syllabus is completed. The average marks of both the test will be considered for final Internal Assessment. Duration of each test shall be of one hour.

End Semester Examination:
1. Question paper will comprise of 6 questions, each carrying 20 marks.
2. The students need to solve total 4 questions.
3. Question No.1 will be compulsory and based on entire syllabus.
4. Remaining question (Q.2 to Q.6) will be selected from all the modules.

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECCILO 8026	Research Methodology	03	--	--	03	--	--	03

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical & Oral	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test2	Avg. Of Test 1 and Test 2					
ECCILO 8026	Research Methodology	20	20	20	80	--	--	--	100

Course objectives:

- To understand Research and Research Process
- To acquaint students with identifying problems for research and develop research strategies
- To familiarize students with the techniques of data collection, analysis of data and interpretation

Course outcomes:

After successful completion of the course student will be able to

- Prepare a preliminary research design for projects in their subject matter areas
- Accurately collect, analyze and report data
- Present complex data or situations clearly
- Review and analyze research findings

Module No.	Unit No.	Topics	Hrs.
1.0		**Introduction and Basic Research Concepts**	**09**
	1.1	Research – Definition; Concept of Construct, Postulate, Proposition, Thesis, Hypothesis, Law, Principle. Research methods vs Methodology	
	1.2	Need of Research in Business and Social Sciences	
	1.3	Objectives of Research	
	1.4	Issues and Problems in Research	
	1.5	Characteristics of Research: Systematic, Valid, Verifiable, Empirical and Critical	
2.0		**Types of Research**	**07**
	2.1	Basic Research	
	2.2	Applied Research	
	2.3	Descriptive Research	
	2.4	Analytical Research	
	2.5	Empirical Research	
	2.6	Qualitative & Quantitative Approaches	
3.0		**Research Design and Sample Design**	**07**
	3.1	Research Design – Meaning, Types and Significance	
	3.2	Sample Design – Meaning and Significance Essentials of a good sampling Stages in Sample Design Sampling methods/techniques Sampling Errors	
4.0		**Research Methodology**	**08**
	4.1	Meaning of Research Methodology	
	4.2	Stages in Scientific Research Process: a. Identification and Selection of Research Problem b. Formulation of Research Problem c. Review of Literature d. Formulation of Hypothesis e. Formulation of research Design f. Sample Design g. Data Collection h. Data Analysis i. Hypothesis testing and Interpretation of Data j. Preparation of Research Report	
5.0		**Formulating Research Problem**	**04**
	5.1	Considerations: Relevance, Interest, Data Availability, Choice of data, Analysis of data, Generalization and Interpretation of analysis	
6.0		**Outcome of Research**	**04**

	6.1	Preparation of the report on conclusion reached	
	6.2	Validity Testing & Ethical Issues	
	6.3	Suggestions and Recommendation	
		Total	**39**

References:

1. Dawson, Catherine, 2002, Practical Research Methods, New Delhi, UBS Publishers Distributors.
2. Kothari, C. R.,1985, Research Methodology-Methods and Techniques, New Delhi, Wiley Eastern Limited.
3. Kumar, Ranjit, 2005, Research Methodology-A Step-by-Step Guide for Beginners, (2^{nd} ed.), Singapore, Pearson Education

Internal Assessment:
Assessment consists of two class tests of 20 marks each. The first class test is to be conducted when approximately 40% syllabus is completed and second class test when additional 40% syllabus is completed. The average marks of both the test will be considered for final Internal Assessment. Duration of each test shall be of one hour.

End Semester Examination:
1. Question paper will comprise of 6 questions, each carrying 20 marks.
2. The students need to solve total 4 questions.
3. Question No.1 will be compulsory and based on entire syllabus.
4. Remaining question (Q.2 to Q.6) will be selected from all the modules.

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECCILO 8027	IPR and Patenting	03	--	--	03	--	--	03

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical & Oral	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test2	Avg. Of Test 1 and Test 2					
ECCILO 8027	IPR and Patenting	20	20	20	80	--	--	--	100

Course objectives:

- To understand intellectual property rights protection system
- To promote the knowledge of Intellectual Property Laws of India as well as International treaty procedures
- To get acquaintance with Patent search and patent filing procedure and applications

Course outcomes:

After successful completion of the course student will be able to

- Understand Intellectual Property assets
- Assist individuals and organizations in capacity building
- Work for development, promotion, protection, compliance, and enforcement of Intellectual Property and Patenting

Module No.	Unit No.	Topics	Hrs.
1.0			**05**
	1.1	**Introduction to Intellectual Property Rights (IPR)**: Meaning of IPR, Different category of IPR instruments - Patents, Trademarks,Copyrights, Industrial Designs, Plant variety protection, Geographical indications,Transfer of technology etc. **Importance of IPR in Modern Global Economic Environment:** Theories of IPR, Philosophical aspects of IPR laws, Need for IPR, IPR as an instrument of development	
2.0			**07**
	2.1	**Enforcement of Intellectual Property Rights:** Introduction, Magnitude of problem, Factors that create and sustain counterfeiting/piracy, International agreements, International organizations (e.g. WIPO, WTO) active in IPR enforcement **Indian Scenario of IPR:** Introduction, History of IPR in India, Overview of IP laws in India, Indian IPR, Administrative Machinery, Major international treaties signed by India, Procedure for submitting patent and Enforcement of IPR at national level etc.	
3.0			**05**
	3.1	**Emerging Issues in IPR:** Challenges for IP in digital economy, e-commerce, human genome, biodiversity and traditional knowledge etc.	
4.0			**07**
	4.1	**Basics of Patents:** Definition of Patents, Conditions of patentability, Patentable and non-patentable inventions, Types of patent applications (e.g. Patent of addition etc), Process Patent and Product Patent, Precautions while patenting, Patent specification Patent claims, Disclosures and non-disclosures, Patent rights and infringement, Method of getting a patent	
5.0			**08**
	5.1	**Patent Rules:** Indian patent act, European scenario, US scenario, Australia scenario, Japan scenario, Chinese scenario, Multilateral treaties where India is a member (TRIPS agreement, Paris convention etc.)	
6.0			**07**
	6.1	**Procedure for Filing a Patent (National and International):** Legislation and Salient Features, Patent Search, Drafting and Filing Patent Applications, Processing of patent, Patent Litigation, Patent Publication, Time frame and cost, Patent Licensing, Patent Infringement **Patent databases:** Important websites, Searching international databases	
		Total	**39**

References:

1. Rajkumar S. Adukia, 2007, A Handbook on Laws Relating to Intellectual Property Rights in India, The Institute of Chartered Accountants of India
2. Keayla B K, Patent system and related issues at a glance, Published by National Working Group on Patent Laws
3. T Sengupta, 2011, Intellectual Property Law in India, Kluwer Law International
4. Tzen Wong and Graham Dutfield, 2010, Intellectual Property and Human Development: Current Trends and Future Scenario, Cambridge University Press
5. Cornish, William Rodolph & Llewelyn, David. 2010, Intellectual Property: Patents, Copyrights, Trade Marks and Allied Right, 7th Edition, Sweet & Maxwell
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7. Prabhuddha Ganguli, 2012, Intellectual Property Rights, 1st Edition, TMH
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10. Kompal Bansal and Praishit Bansal, 2012, Fundamentals of IPR for Engineers, 1st Edition, BS Publications
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13. N S Rathore, S M Mathur, Priti Mathur, Anshul Rathi, IPR: Drafting,Interpretation of Patent Specifications and Claims, New India Publishing Agency
14. Vivien Irish, 2005, Intellectual Property Rights for Engineers,IET
15. Howard B Rockman, 2004, Intellectual Property Law for Engineers and scientists, Wiley-IEEE Press

Internal Assessment:
Assessment consists of two class tests of 20 marks each. The first class test is to be conducted when approximately 40% syllabus is completed and second class test when additional 40% syllabus is completed. The average marks of both the test will be considered for final Internal Assessment. Duration of each test shall be of one hour.

End Semester Examination:
1. Question paper will comprise of 6 questions, each carrying 20 marks.
2. The students need to solve total 4 questions.
3. Question No.1 will be compulsory and based on entire syllabus.
4. Remaining question (Q.2 to Q.6) will be selected from all the modules.

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECCILO 8028	Digital Business Management	03	--	--	03	--	--	03

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical & Oral	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test2	Avg. Of Test 1 and Test 2					
ECCILO 8028	Digital Business Management	20	20	20	80	--	--	--	100

Course objectives:

- To familiarize with digital business concept
- To acquaint with E-commerce
- To give insights into E-business and its strategies

Course outcomes:

After successful completion of the course student will be able to

- Identify drivers of digital business
- Illustrate various approaches and techniques for E-business and management
- Prepare E-business plan

Module No.	Unit No.	Topics	Hrs.
1.0			**09**
	1.1	**Introduction to Digital Business-** Introduction, Background and current status, E-market places, structures, mechanisms, economics and impacts Difference between physical economy and digital economy, **Drivers of digital business**- Big Data & Analytics, Mobile, Cloud Computing, Social media, BYOD, and Internet of Things(digitally intelligent machines/services) Opportunities and Challenges in Digital Business,	
2.0			**06**
	2.1	**Overview of E-Commerce** **E-Commerce-** Meaning, Retailing in e-commerce-products and services, consumer behavior, market research and advertisement B2B-E-commerce-selling and buying in private e-markets, public B2B exchanges and support services, e-supply chains, Collaborative Commerce, Intra business EC and Corporate portals Other E-C models and applications, innovative EC System-From E-government and learning to C2C, mobile commerce and pervasive computing EC Strategy and Implementation-EC strategy and global EC, Economics and Justification of EC, Using Affiliate marketing to promote your e-commerce business, Launching a successful online business and EC project, Legal, Ethics and Societal impacts of EC	
3.0			**06**
	3.1	**Digital Business Support services**: ERP as e –business backbone, knowledge Tope Apps, Information and referral system **Application Development:** Building Digital business Applications and Infrastructure	
4.0			**06**
	4.1	**Managing E-Business**-Managing Knowledge, Management skills for e-business, Managing Risks in e –business Security Threats to e-business -Security Overview, Electronic Commerce Threats, Encryption, Cryptography, Public Key and Private Key Cryptography, Digital Signatures, Digital Certificates, Security Protocols over Public Networks: HTTP, SSL, Firewall as Security Control, Public Key Infrastructure (PKI) for Security, Prominent Cryptographic Applications	
5.0			**04**
	5.1	**E-Business Strategy**-E-business Strategic formulation- Analysis of Company's Internal and external environment, Selection of strategy, E-business strategy into Action, challenges and E-Transition (Process of Digital Transformation)	

6.0			**08**
	6.1	**Materializing e-business: From Idea to Realization**-Business plan preparation **Case Studies and presentations**	
		Total	**39**

References:

1. A textbook on E-commerce, Er Arunrajan Mishra, Dr W K Sarwade,Neha Publishers & Distributors, 2011
2. E-commerce from vision to fulfilment, Elias M. Awad, PHI-Restricted, 2002
3. Digital Business and E-Commerce Management, 6th Ed, Dave Chaffey, Pearson, August 2014
4. Introduction to E-business-Management and Strategy, Colin Combe, ELSVIER, 2006
5. Digital Business Concepts and Strategy, Eloise Coupey, 2nd Edition, Pearson
6. Trend and Challenges in Digital Business Innovation, VinocenzoMorabito, Springer
7. Digital Business Discourse Erika Darics, April 2015, Palgrave Macmillan
8. E-Governance-Challenges and Opportunities in : Proceedings in 2nd International Conference theory and practice of Electronic Governance
9. Perspectives the Digital Enterprise –A framework for Transformation, TCS consulting journal Vol.5
10. Measuring Digital Economy-A new perspective- DoI:10.1787/9789264221796-enOECD Publishing

Internal Assessment:
Assessment consists of two class tests of 20 marks each. The first class test is to be conducted when approximately 40% syllabus is completed and second class test when additional 40% syllabus is completed. The average marks of both the test will be considered for final Internal Assessment. Duration of each test shall be of one hour.

End Semester Examination:
1. Question paper will comprise of 6 questions, each carrying 20 marks.
2. The students need to solve total 4 questions.
3. Question No.1 will be compulsory and based on entire syllabus.
4. Remaining question (Q.2 to Q.6) will be selected from all the modules.

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	Practical	Tutorial	Total
ECCILO 8029	Environmental Management	03	--	--	03	--	--	03

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical & Oral	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test2	Avg. Of Test 1 and Test 2					
ECCILO 8029	Environmental Management	20	20	20	80	--	--	--	100

Course objectives:

- Understand and identify environmental issues relevant to India and global concerns
- Learn concepts of ecology
- Familiarise environment related legislations

Course outcomes:

After successful completion of the course student will be able to

- Understand the concept of environmental management
- Understand ecosystem and interdependence, food chain etc.
- Understand and interpret environment related legislations

Module No.	Unit No.	Topics	Hrs.
1.0			**10**
	1.1	Introduction and Definition of Environment: Significance of Environment Management for contemporary managers, Career opportunities, Environmental issues relevant to India, Sustainable Development, the Energy scenario	
2.0			**06**
	2.1	Global Environmental concerns : Global Warming, Acid Rain, Ozone Depletion, Hazardous Wastes, Endangered life-species, Loss of Biodiversity, Industrial/Man-made disasters, Atomic/Biomedical hazards, etc.	
3.0			**05**
	3.1	Concepts of Ecology: Ecosystems and interdependence between living organisms, habitats, limiting factors, carrying capacity, food chain, etc.	
4.0			**10**
	4.1	Scope of Environment Management, Role and functions of Government as a planning and regulating agency Environment Quality Management and Corporate Environmental Responsibility	
5.0			**05**
	5.1	Total Quality Environmental Management, ISO-14000, EMS certification	
6.0			**03**
	6.1	General overview of major legislations like Environment Protection Act, Air (P & CP) Act, Water (P & CP) Act, Wildlife Protection Act, Forest Act, Factories Act, etc.	
		Total	**39**

References:

1. Environmental Management: Principles and Practice, C J Barrow, Routledge Publishers London, 1999
2. A Handbook of Environmental Management Edited by Jon C. Lovett and David G. Ockwell, Edward Elgar Publishing
3. Environmental Management **V Ramachandra and Vijay Kulkarni, TERI Press**
4. Indian Standard Environmental Management Systems — Requirements With Guidance For Use, Bureau Of Indian Standards, February 2005
5. Environmental Management: An Indian Perspective, S N Chary and Vinod Vyasulu, Maclillan India, 2000
6. Introduction to Environmental Management, Mary K Theodore and Louise Theodore, CRC

Press
7. Environment and Ecology, Majid Hussain, 3rd Ed. Access Publishing.2015

Internal Assessment:
Assessment consists of two class tests of 20 marks each. The first class test is to be conducted when approximately 40% syllabus is completed and second class test when additional 40% syllabus is completed. The average marks of both the test will be considered for final Internal Assessment. Duration of each test shall be of one hour.

End Semester Examination:
1. Question paper will comprise of 6 questions, each carrying 20 marks.
2. The students need to solve total 4 questions.
3. Question No.1 will be compulsory and based on entire syllabus.
4. Remaining question (Q.2 to Q.6) will be selected from all the modules.

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	TW/Pracs	Tutorial	Total
ECL801	RF Design Laboratory	--	02	--	--	1	--	1

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical & Oral	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test2	Avg. Of Test 1 and Test 2					
ECL801	RF Design Laboratory	--	--	--	--	25	25	--	50

Suggested List of experiments

- Calibration of Network analyser for measurements.
- Characterization of RF low pass filter.
- Characterization of RF high pass filter.
- Characterization of RF band pass filter.
- Design of passive matching networks.
- Stability circles for microwave transistor
- Gain and Noise circles for transistor amplifier design
- Measurement of radiated emission using EMI Probes for DOT.
- Measurement of conducted radiations.
- Grounding & shielding for EMC.
- Testing of various emission standards like MIL CESPARE.

Term Work:

At least 08 Experiments including 02 simulations covering entire syllabus must be given during the "**Laboratory session batch wise"**. Computation/simulation based experiments are also encouraged. The experiments should be students centric and attempt should be made to make experiments more meaningful, interesting and innovative. Application oriented one mini-project can be conducted for maximum batch of four students.

Term work assessment must be based on the overall performance of the student with every experiments/tutorials and mini-projects (if included) are graded from time to time. The grades will be converted to marks as per "**Choice Based Credit and Grading System"** manual and should be added and averaged. Based on above scheme grading and term work assessment should be done.

The practical and oral examination will be based on entire syllabus.

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	TW/Pracs	Tutorial	Total
ECL801	Wireless Networks Laboratory	--	02	--	--	1	--	1

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical & Oral	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test2	Avg. Of Test 1 and Test 2					
ECL801	Wireless Networks Laboratory	--	--	--	--	25	25	--	50

Note: Small Project can be considered as a part of term-work.

Term Work:

At least 08 Experiments including 02 simulations covering entire syllabus must be given during the "**Laboratory session batch wise"**. Computation/simulation based experiments are also encouraged. The experiments should be students centric and attempt should be made to make experiments more meaningful, interesting and innovative. Application oriented one mini-project can be conducted for maximum batch of four students.

Term work assessment must be based on the overall performance of the student with every experiments/tutorials and mini-projects (if included) are graded from time to time. The grades will be converted to marks as per "**Choice Based Credit and Grading System"** manual and should be added and averaged. Based on above scheme grading and term work assessment should be done.

The practical and oral examination will be based on entire syllabus.

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	TW/Pracs	Tutorial	Total
ECLDLO 8041	Optical Networks Laboratory	--	02	--	--	1	--	1

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical & Oral	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test2	Avg. Of Test 1 and Test 2					
ECLDLO 8041	Optical Networks Laboratory	--	--	--	--	25	25	--	50

Note: Small Project can be considered as a part of term-work.

Term Work:

At least 05 Experiments, 02 tutorials and 1 mini project covering entire syllabus must be given during the "**Laboratory session batch wise"**. Computation/simulation based experiments are also encouraged. The experiments should be students centric and attempt should be made to make experiments more meaningful, interesting and innovative. Application oriented one mini-project can be conducted for maximum batch of four students.

Term work assessment must be based on the overall performance of the student with every experiments/tutorials and mini-projects (if included) are graded from time to time. The grades will be converted to marks as per "**Choice Based Credit and Grading System"** manual and should be added and averaged. Based on above scheme grading and term work assessment should be done.

The practical and oral examination will be based on entire syllabus.

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	TW/Pracs	Tutorial	Total
ECLDLO 8042	Advanced Digital Signal Processing Laboratory	--	02	--	--	1	--	1

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical & Oral	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test2	Avg. Of Test 1 and Test 2					
ECLDLO 8042	Advanced Digital Signal Processing Laboratory	--	--	--	--	25	25	--	50

Suggested List of Experiments

- Write a program to implement multirate sampling technique for Interpolation.
- Write a program to implement multirate sampling techniques for Decimation.
- Design Anti-aliasing and Anti-Imaging filters.
- Write a program to demonstrate LMS algorithm for noise cancellations.
- Write a program to demonstrate RLS algorithm to calculate it's error function.
- Demonstrate application of Wavelet Transform for denoising.
- Analyse the frequency contents in EEG record.
- Write a program to generate ECG signal and isolate the QRS complex.

Instructions:

1. Minimum 4 experiments and 4 assignments must be submitted by each student.
2. Assignments can be designed on problem based learning from the content of the syllabus.
3. Simulation tools like Matlab/Scilab can be used.

Note: Small Project can be considered as a part of term-work.

Term Work:

At least 08 Experiments including 02 simulations covering entire syllabus must be given during the "**Laboratory session batch wise"**. Computation/simulation based experiments are also encouraged. The experiments should be students centric and attempt should be made to make experiments more meaningful, interesting and innovative. Application oriented one mini-project can be conducted for maximum batch of four students.

Term work assessment must be based on the overall performance of the student with every experiments/tutorials and mini-projects (if included) are graded from time to time. The grades will be converted to marks as per "**Choice Based Credit and Grading System"** manual and should be added and averaged. Based on above scheme grading and term work assessment should be done.

The practical and oral examination will be based on entire syllabus.

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	TW/Pracs	Tutorial	Total
ECLDLO 8043	Satellite Communication Laboratory	--	02	--	--	1	--	1

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical & Oral	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test2	Avg. Of Test 1 and Test 2					
ECLDLO 8043	Satellite Communication Laboratory	--	--	--	--	25	25	--	50

Note: Small Project can be considered as a part of term-work.

Term Work:

At least 08 Experiments covering entire syllabus must be given during the "**Laboratory session batch wise"**. Computation/simulation based experiments are also encouraged. The experiments should be students centric and attempt should be made to make experiments more meaningful, interesting and innovative. Application oriented one mini-project can be conducted for maximum batch of four students.

Term work assessment must be based on the overall performance of the student with every experiments/tutorials and mini-projects (if included) are graded from time to time. The grades will be converted to marks as per "**Choice Based Credit and Grading System"** manual and should be added and averaged. Based on above scheme grading and term work assessment should be done.

The practical and oral examination will be based on entire syllabus.

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	TW/Pracs	Tutorial	Total
ECLDLO 8044	Network Management in TeleCommunication Laboratory	--	02	--	--	1	--	1

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical & Oral	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test2	Avg. Of Test 1 and Test 2					
ECLDLO 8044	Network Management in TeleCommunication Laboratory	--	--	--	--	25	25	--	50

Suggested List of Experiments

- Network Monitoring tools

  a) Status b)Route c)Traffic Tools

- Network Audit using NMAP Gui
- Monitoring and management network using SNMP

  a) Basic SNMP b) Advanced SNMP v3 Authentication/Encryption and ACL

  c) SNMP Trap Daemon Implementation

- Configuration SNMP Protocol on Cisco Router using Packet Tracer
- Install and configure SNMP MIB browser

  a) qtmib b)snmpB c) OpManager MIB browser

- Configuration manageable Switch: Dlink DES 3026 24 Port L2 Switch
- Network Statistics and measurement

a] LAN Traffic Monitoring b) Protocol statistics

- LAN Troubleshooting using Wireshark
- Monitoring of services and Servers using Observium\
- Monitoring of services and Servers using Cacti
- Install and configure NAGIOS and monitor server
- Installation and Configuration of OpenNMS as a NMS.
- Implementation of Centralized Log Management System: Syslog-ng
- Study of commercial network management tools: HPOpenView, OpManager, GFILanguard and IBM NMS.

Note: Small Project can be considered as a part of term-work.

Term Work:

At least 08 Experiments covering entire syllabus must be given during the "**Laboratory session batch wise"**. Computation/simulation based experiments are also encouraged. The experiments should be students centric and attempt should be made to make experiments more meaningful, interesting and innovative. Application oriented one mini-project can be conducted for maximum batch of four students.

Term work assessment must be based on the overall performance of the student with every experiments/tutorials and mini-projects (if included) are graded from time to time. The grades will be converted to marks as per "**Choice Based Credit and Grading System"** manual and should be added and averaged. Based on above scheme grading and term work assessment should be done.

The practical and oral examination will be based on entire syllabus.

Subject Code	Subject Name	Teaching Scheme (Hrs.)			Credits Assigned			
		Theory	Practical	Tutorial	Theory	TW/Pracs	Tutorial	Total
ECL803	Project Stage-II	--	12	--	--	6	--	6

Subject Code	Subject Name	Examination Scheme							
		Theory Marks				Term Work	Practical & Oral	Oral	Total
		Internal assessment			End Sem. Exam				
		Test 1	Test2	Avg. Of Test 1 and Test 2					
ECL803	Project Stage-II	--	--	--	--	100	50	--	150

Objective: The primary objective is to meet the milestones formed in the overall project plan decided in Project - I. The idea presented in Project -I should be implemented in Project -II with results, conclusion and future work. The project will culminate in the production of a thesis by each individual student.

Guidelines:

Project Report Format:

At the end of the semester the student needs to prepare a project report which should be prepared as per the guidelines issued by the University of Mumbai. Along with the project report a CD containing: project documentation, Implementation code, required utilities, Software‘s and user Manuals need to be attached.

Term Work:

Student has to submit weekly progress report to the internal guide and the internal guide has to keep a track on the progress of the project and also has to maintain the attendance report. This progress report can be used for awarding the term work marks. In case of industry projects, visit by internal guide will be preferred to get the status of project. Distribution of marks for term work shall be as follows:

a) Weekly Attendance on Project Day
b) Project work contributions as per objective
c) Project Report (Hard Bound)
d) Term End Presentation (Internal)

The final certification and acceptance of TW ensures the satisfactory performance on the above aspects.

Oral & Practical:

Oral & Practical examination of Project- II should be conducted by Internal and External examiners approved by University of Mumbai. Students have to give presentation and demonstration on the Project-II.